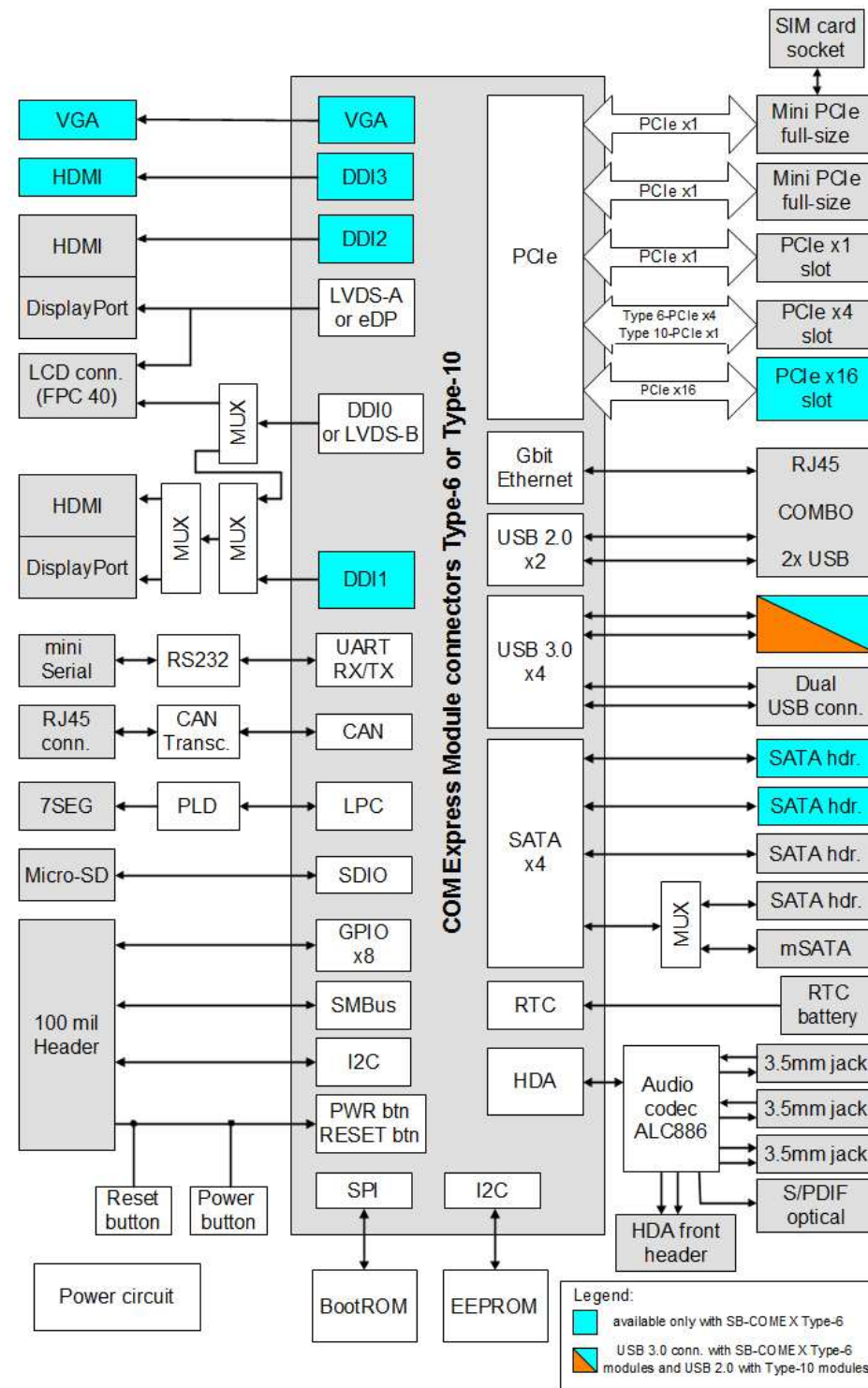


01. Index, Block Diagram
02. COM Express AB connector
03. COM Express CD connector
04. PCIe CLK
05. PCIe MUX
06. PCIe x1, x4, PEG
07. mPCIe
08. SATA, mSATA (muxed)
09. USB
10. GBE, RS232
11. DDI[0-1] (Muxed), DP, HDMI-0
12. HDMI 1,2
13. LVDS, eDP, VGA
14. HD Audio
15. Poscode 7SEG
16. micro SD, Feature
17. SPI Flash, I2C, CAN
18. Type Det, SPK, BAT, FAN
19. System Power 12V, 5V, 3.3V

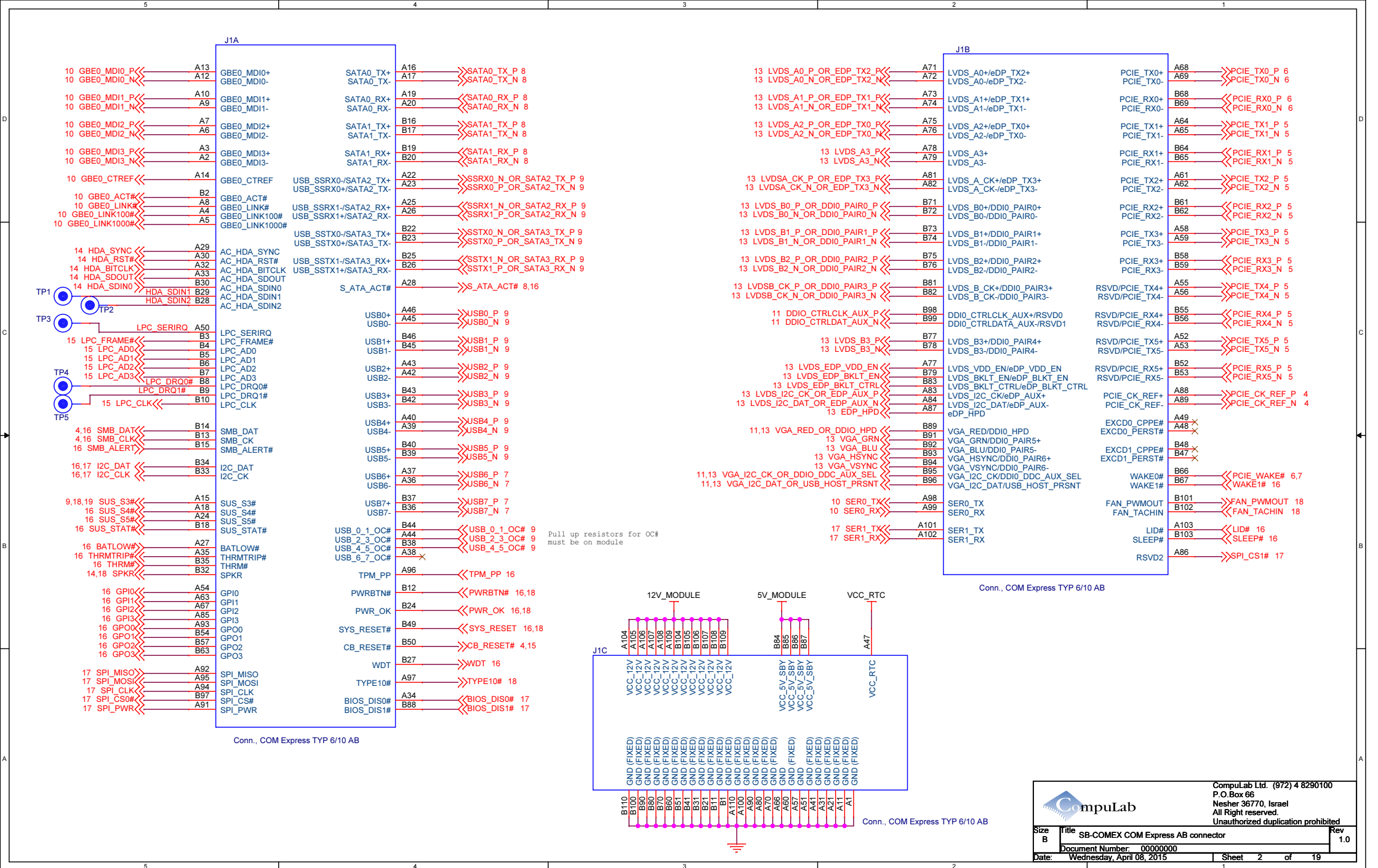


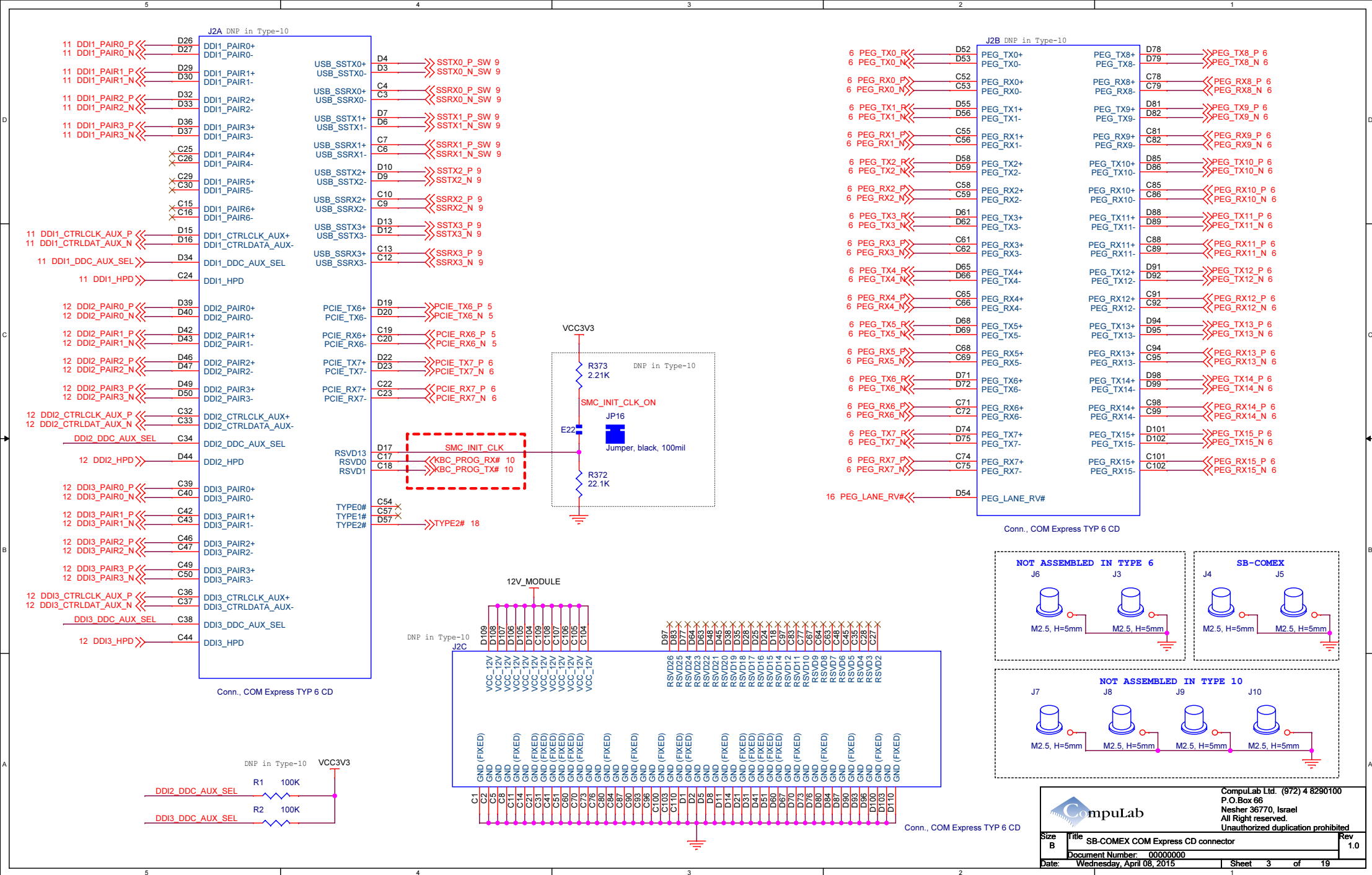
PCB1

PCB, SB-COMEX, Rev 1.0  
P/N = 188C02770

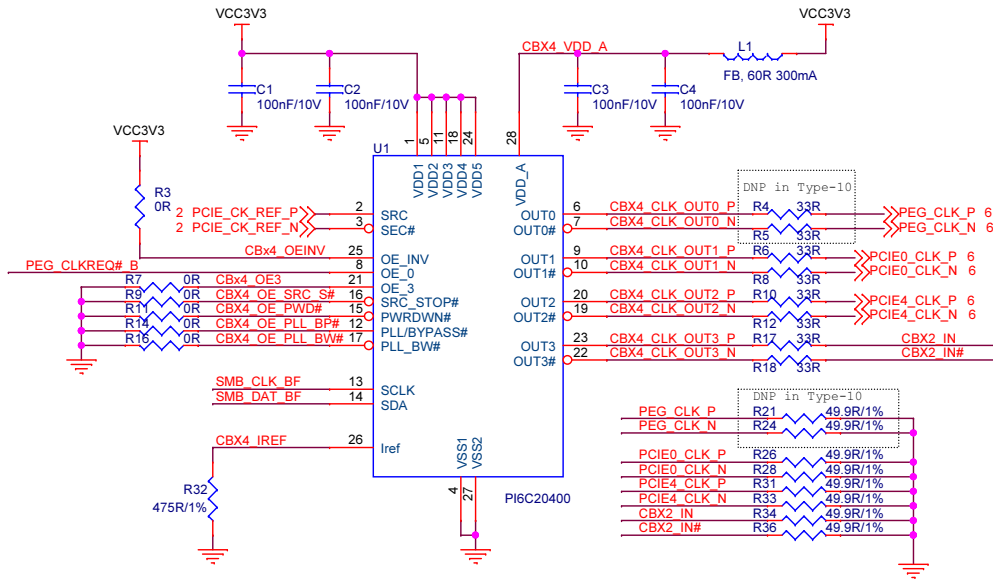
ZZ1

P/N = 188Z10030  
PARSER\_VERSION\_1.0

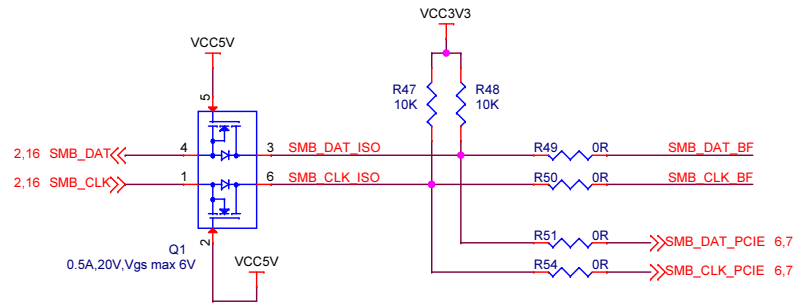
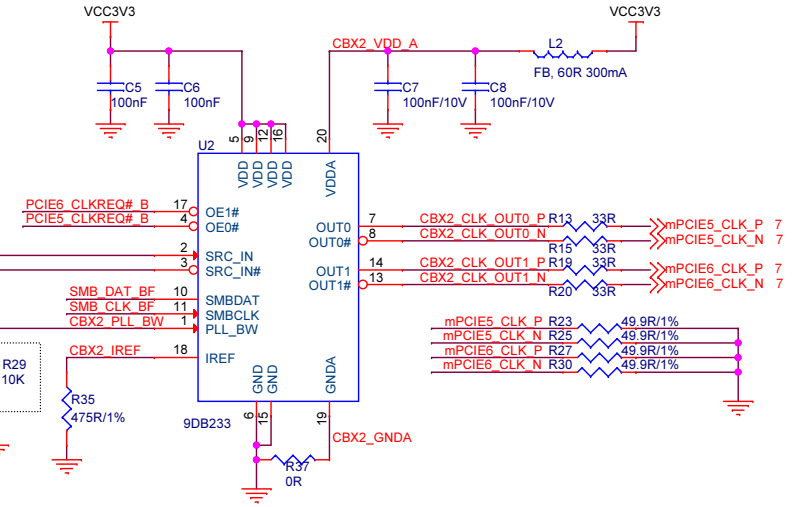




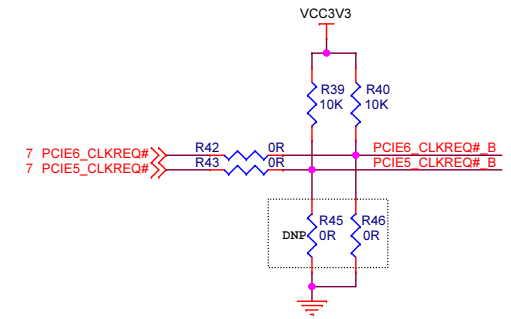
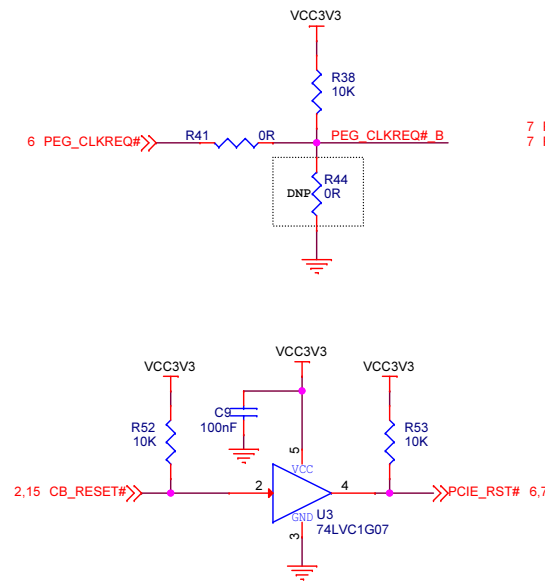
## PCIe Clock x4 Buffer



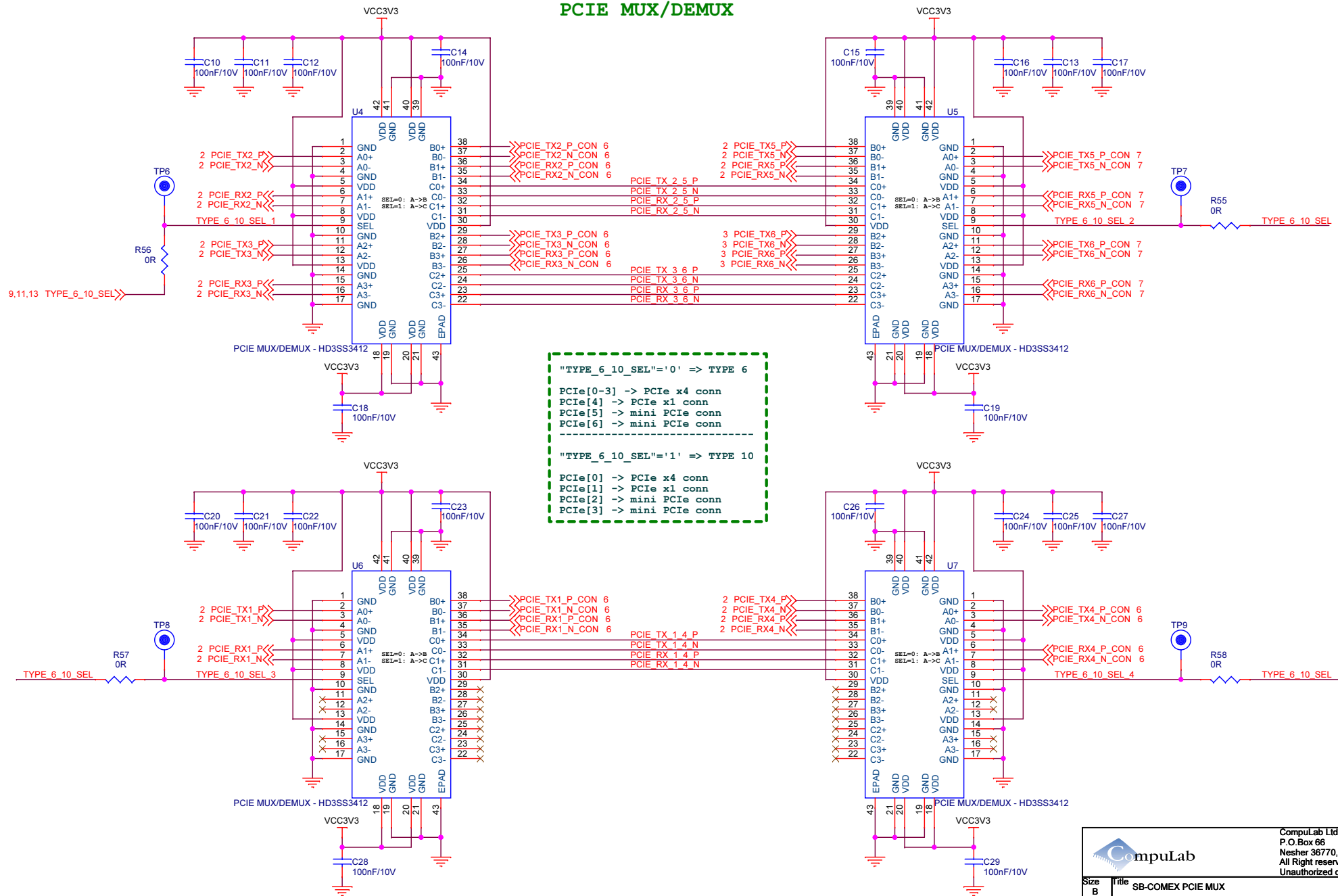
## PCIe Clock x2 Buffer



SM Bus on module uses pull ups to 3.3V\_SBY,  
while pull ups of PCIe SM-Bus connected to 3.3V\_S0,  
so separating switch must be used



# PCIE MUX/DEMUX





## DNP in Type-10

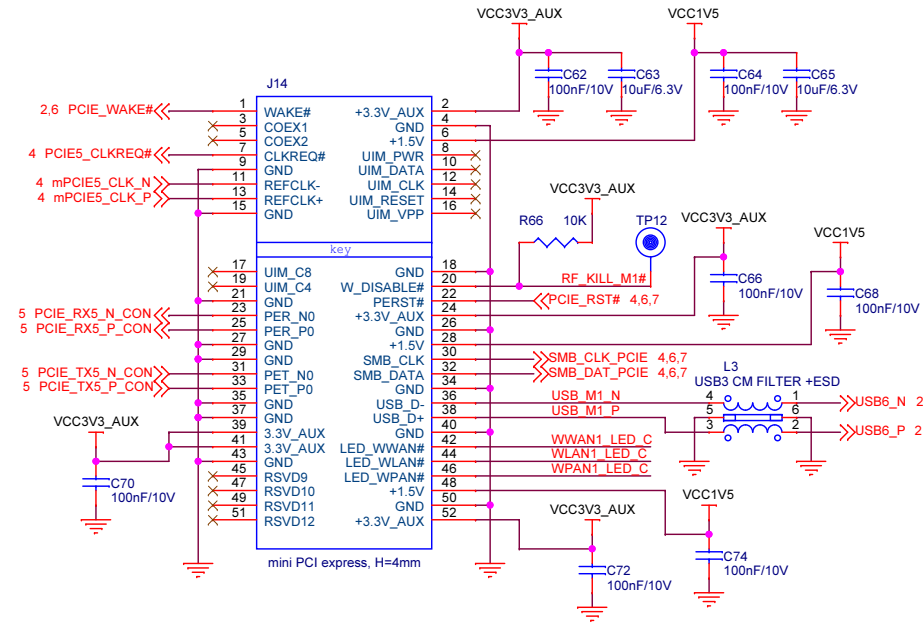


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P.O.Box 66  
Nesher 36770, Israel  
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Unauthorized duplication prohibited

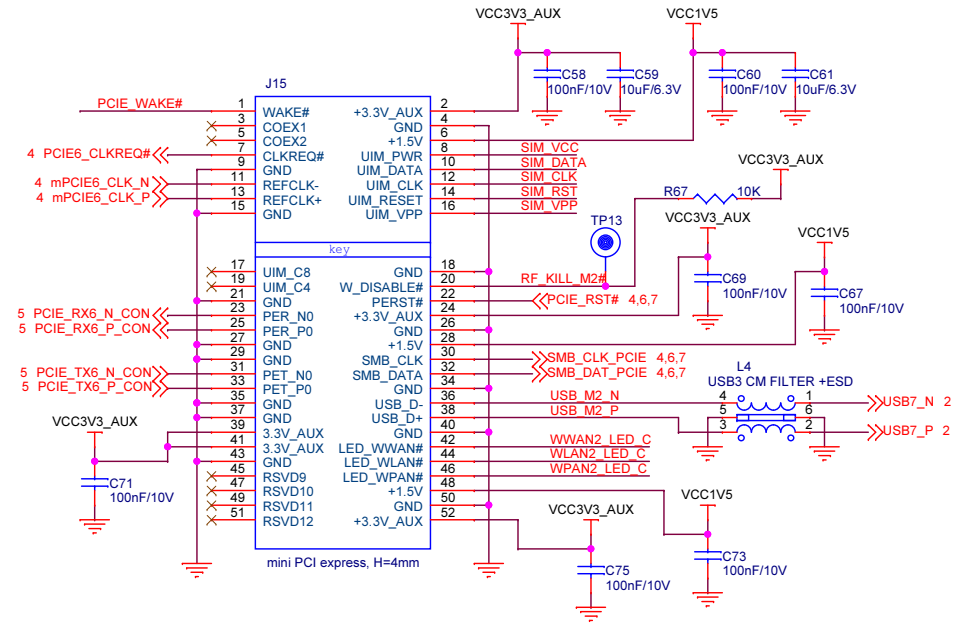
Size A3	Title SB-COMEX PCIE x4, x1, PEG
	Document Number: 00000000
Date:	Wednesday, April 08, 2015

Rev	1.0
-----	-----

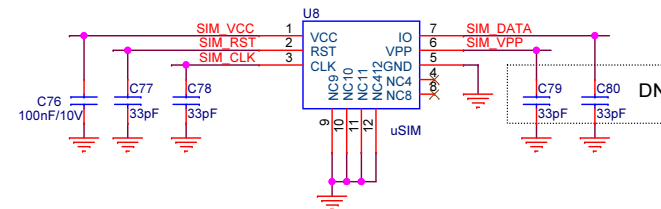
## Mini PCIE SLOT LANE 5

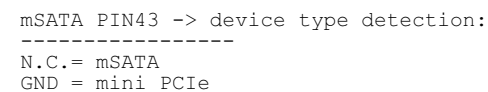
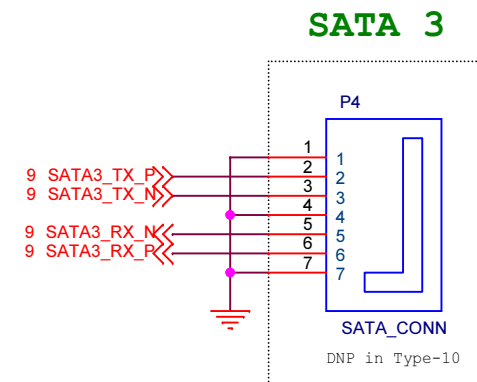
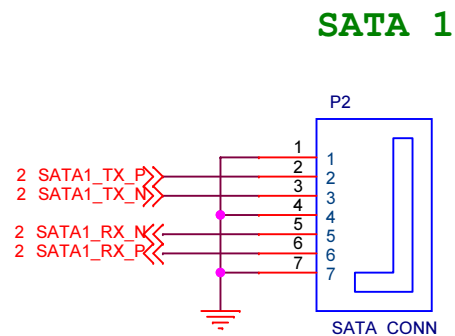


## Mini PCIE SLOT LANE 6



## SIM CARD SOCKET

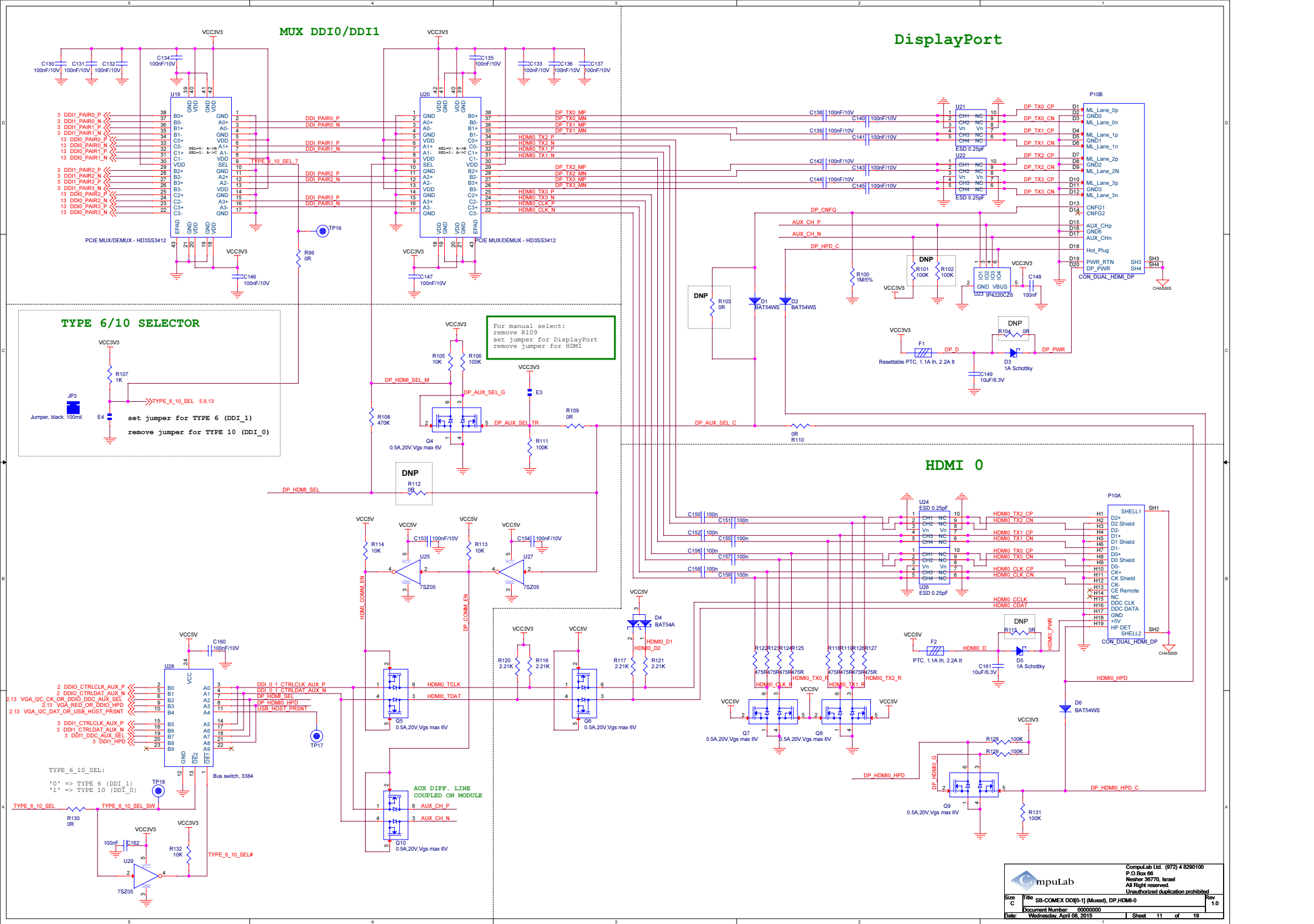




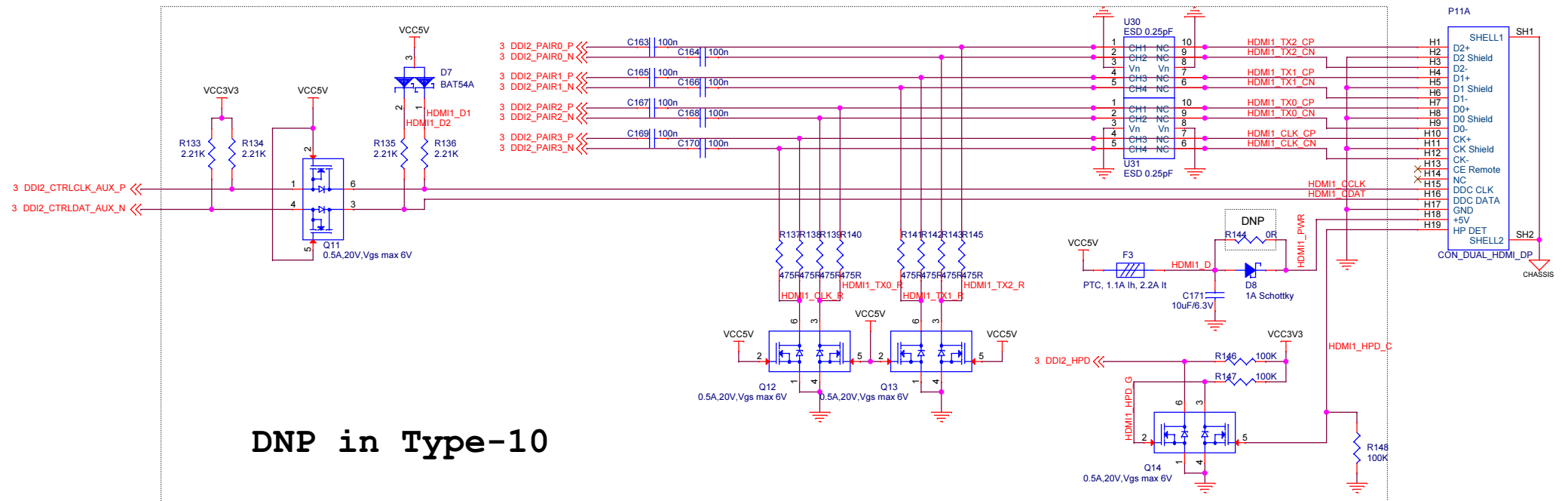




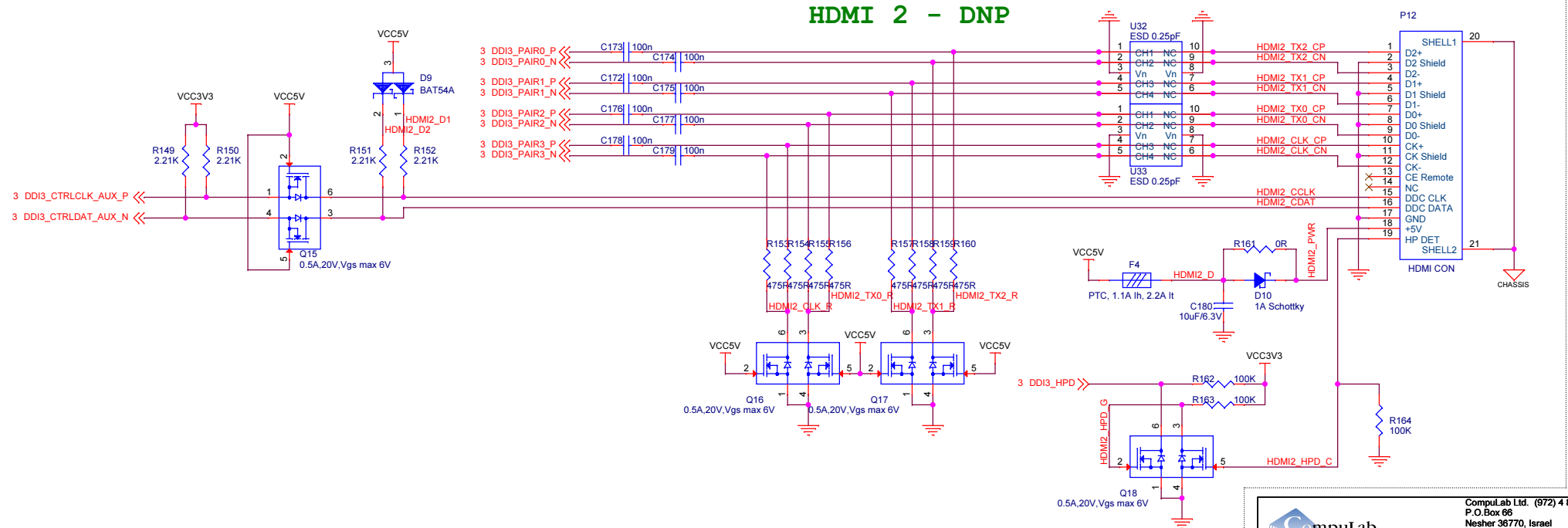




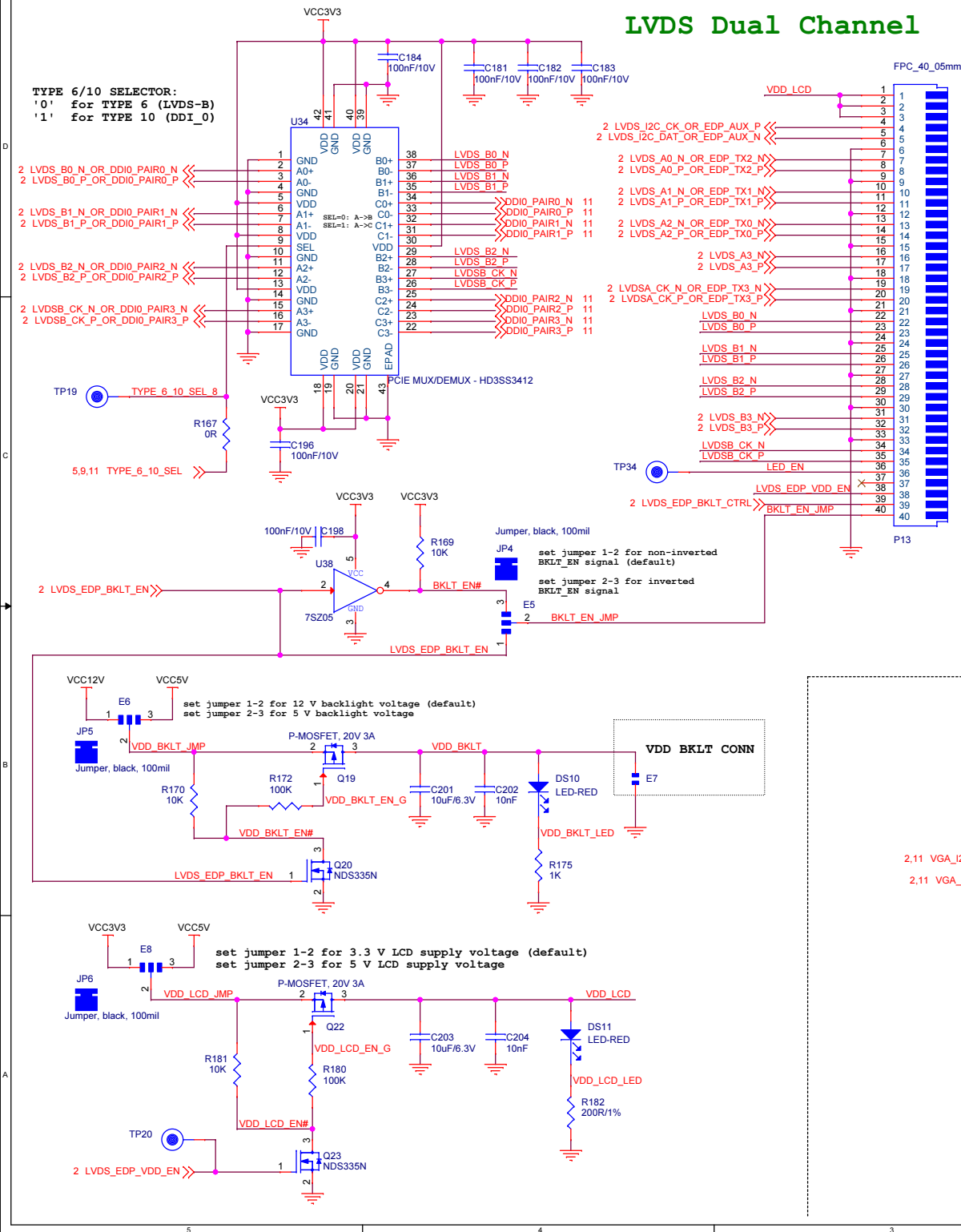
## HDMI 1



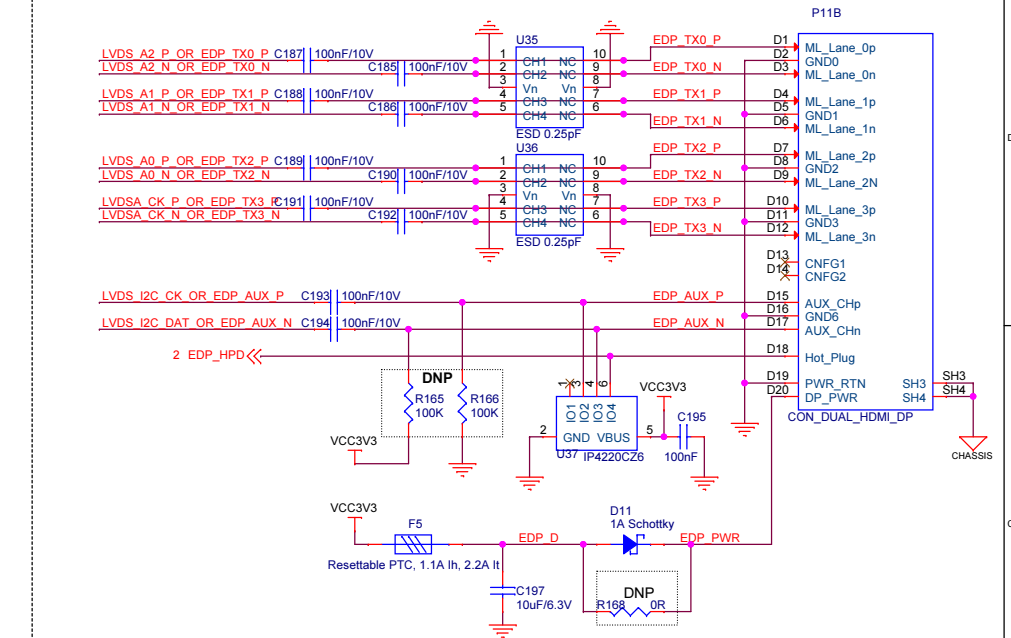
## HDMI 2 - DNP



## LVDS Dual Channel

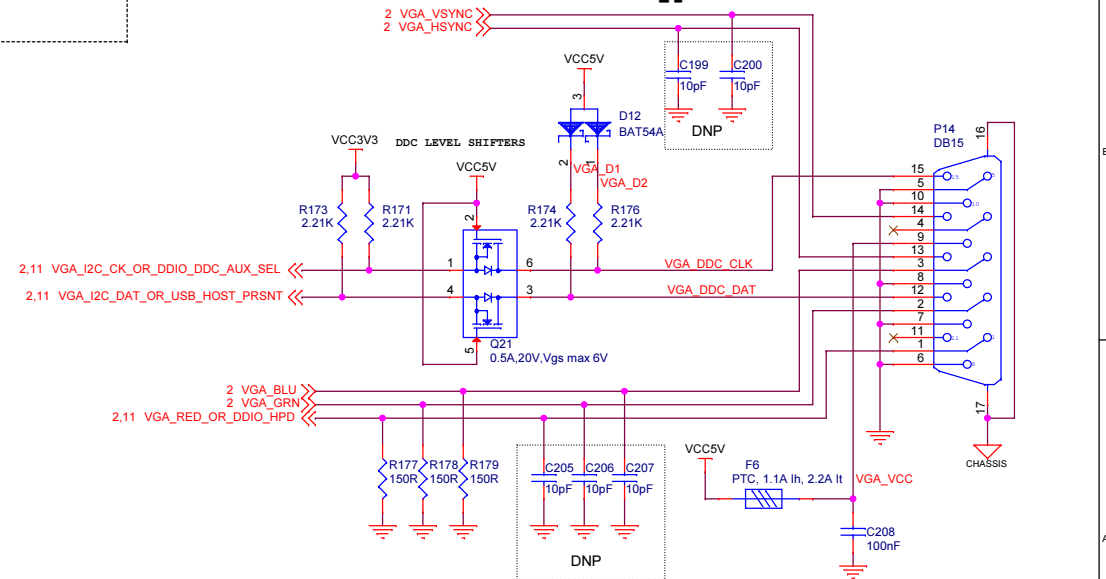


## eDP via DP connector



## VGA

## DNP in Type-10

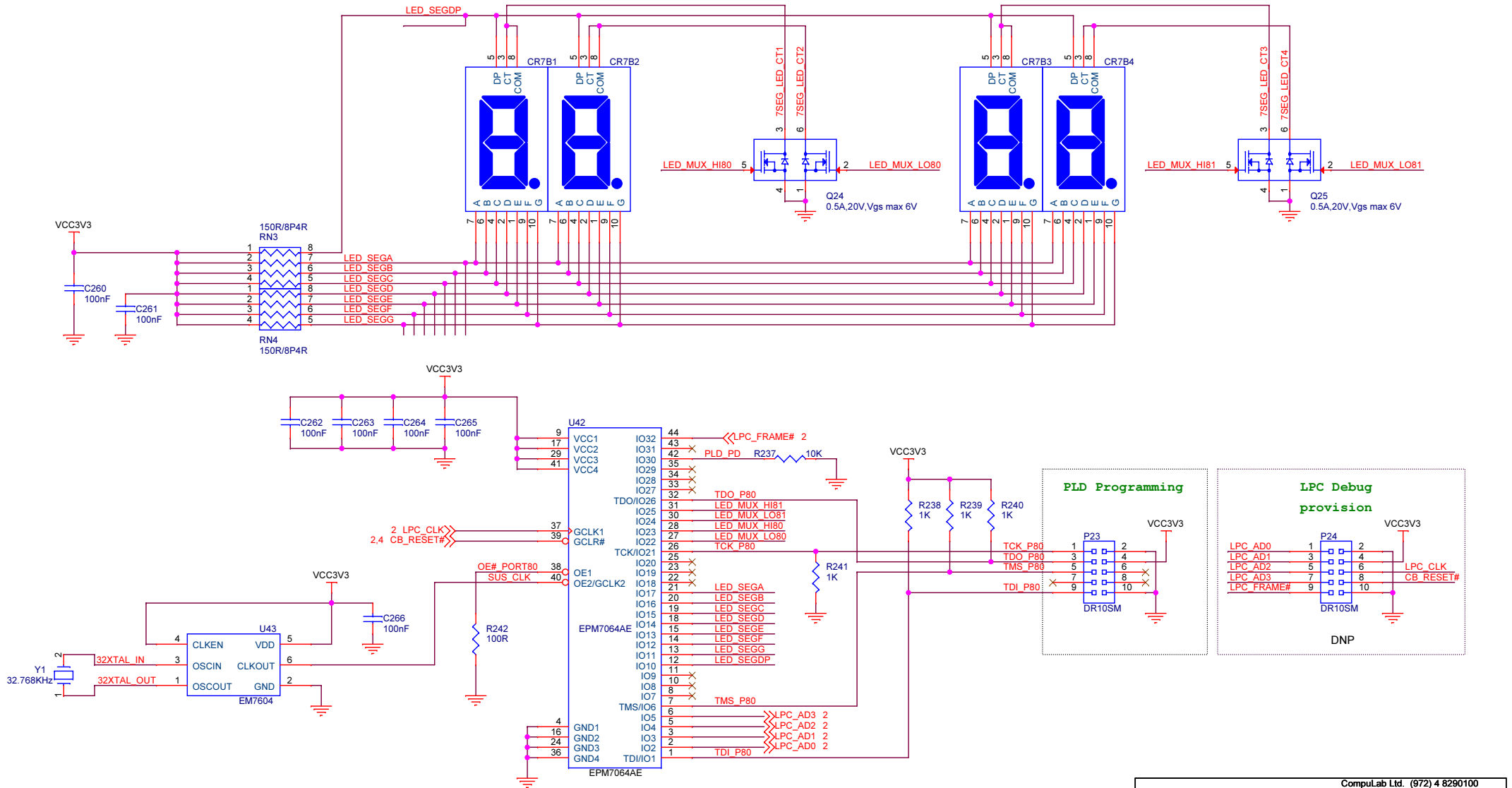




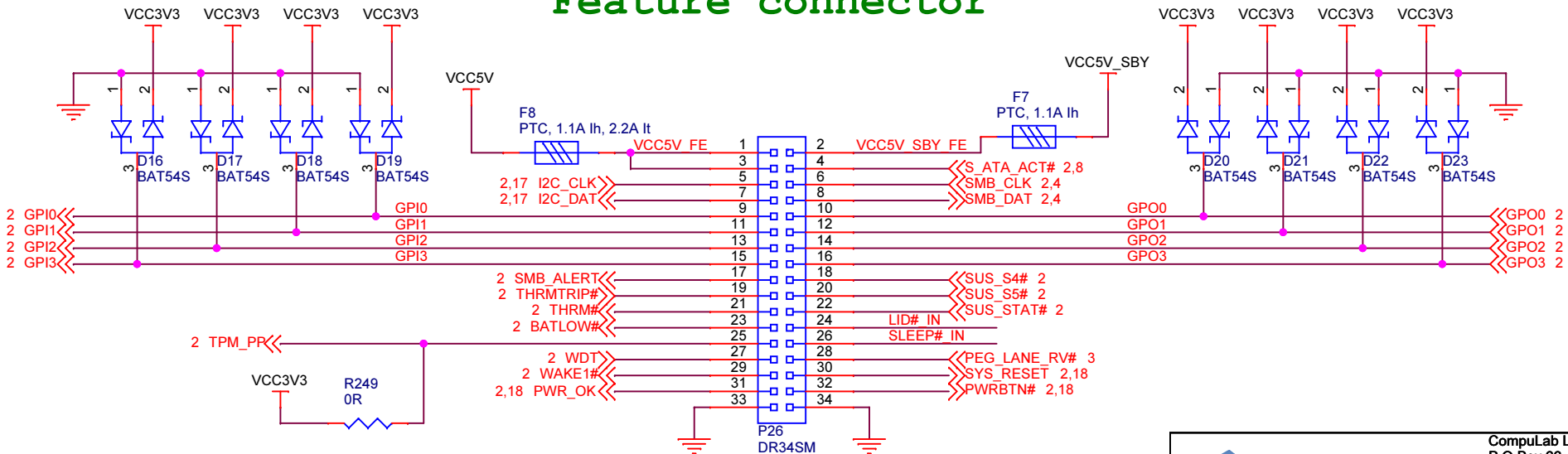
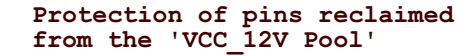


**BITS: 0-7**

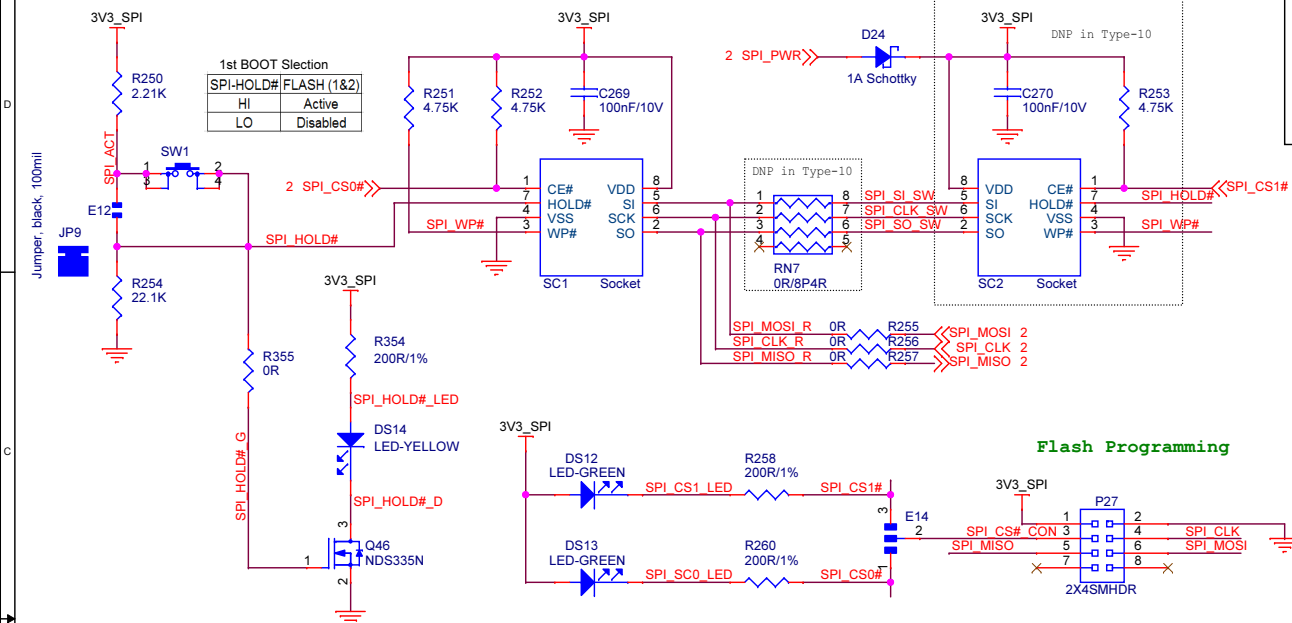
**BITS: 8-15**



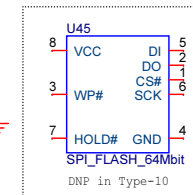
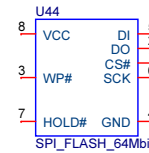
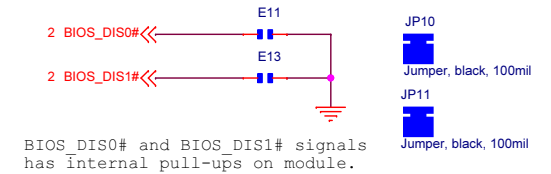
## Feature connector



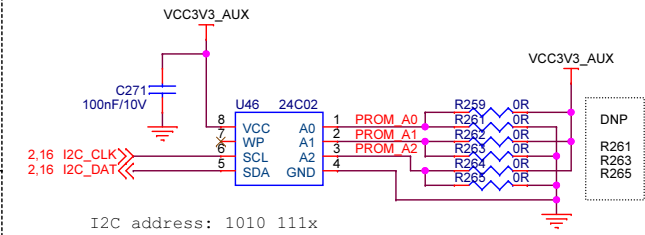
SPI FLASH 0,1



BIOS_DIS1#	BIOS_DIS0#	BIOS ENTRY / SPI_CS#
OFF	OFF	on-module firmware (default)
OFF	ON	carrier FWH (not supported)
ON	OFF	carrier firmware from SPI
ON	ON	on-module firmware, carrier SPI contains management data

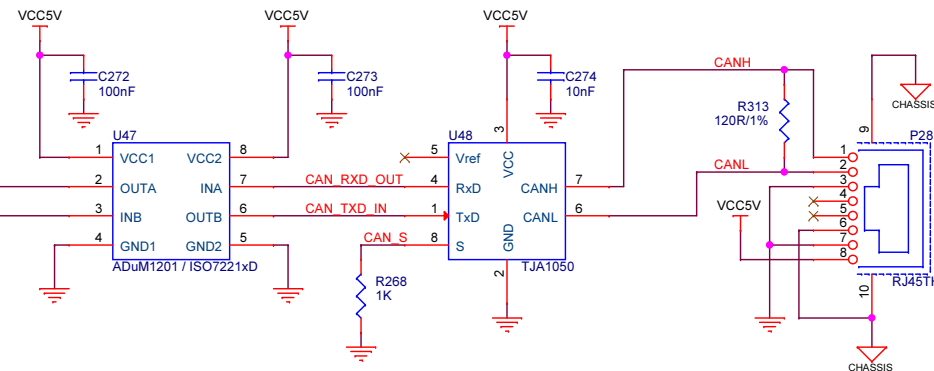
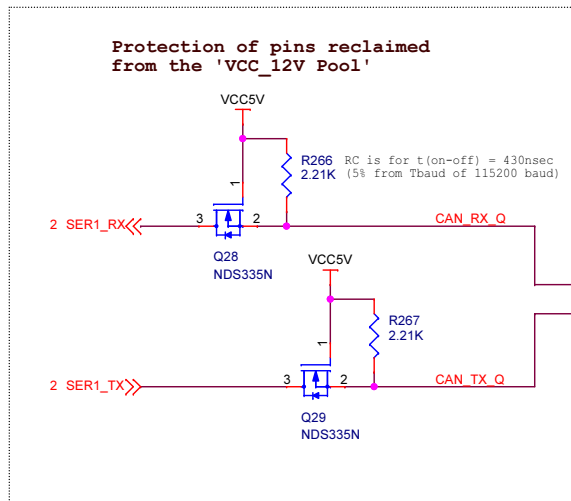


## I2C EEPROM

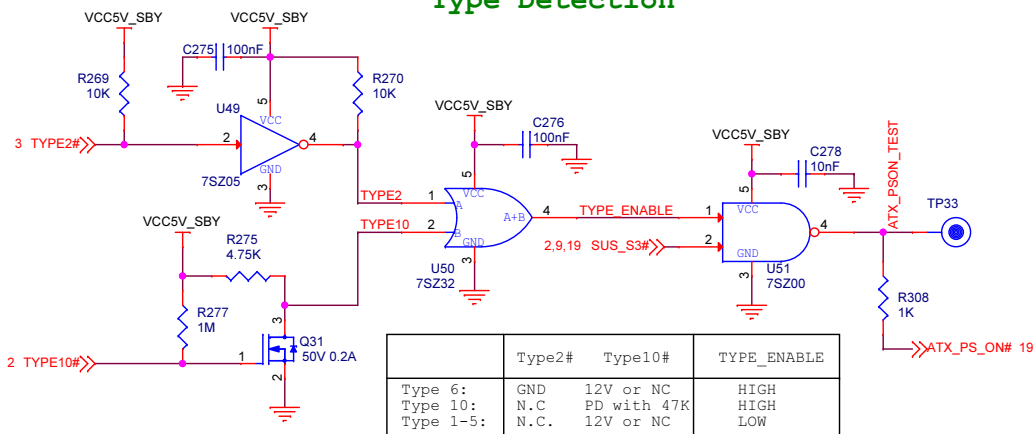


According to the COM Express specification, the I2C address lines A2, A1 and A0 of the system configuration EEPROM must be pulled high.

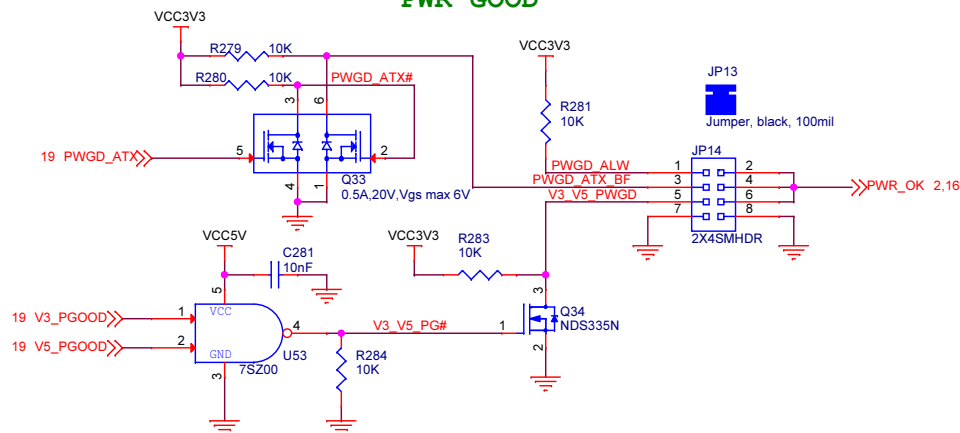
## CAN BUS



## Type Detection



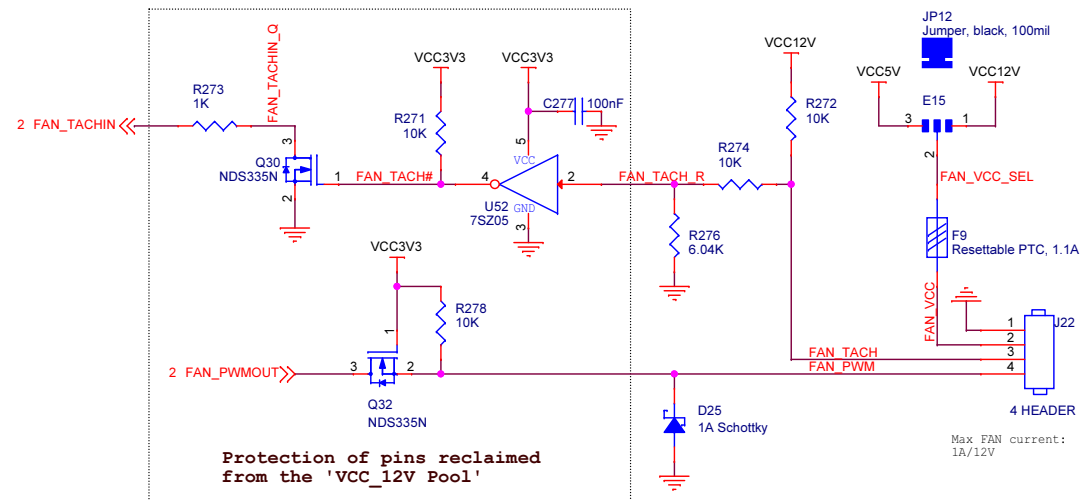
## PWR GOOD



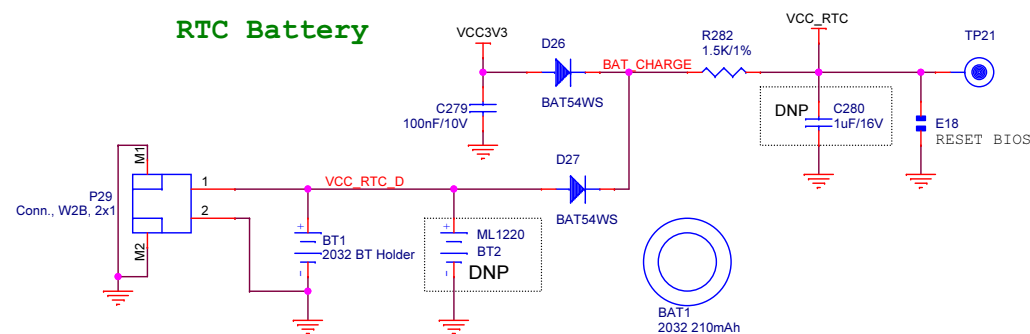
## PWR LED



## CPU FAN



## RTC Battery



## PC BEEPER

