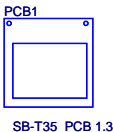
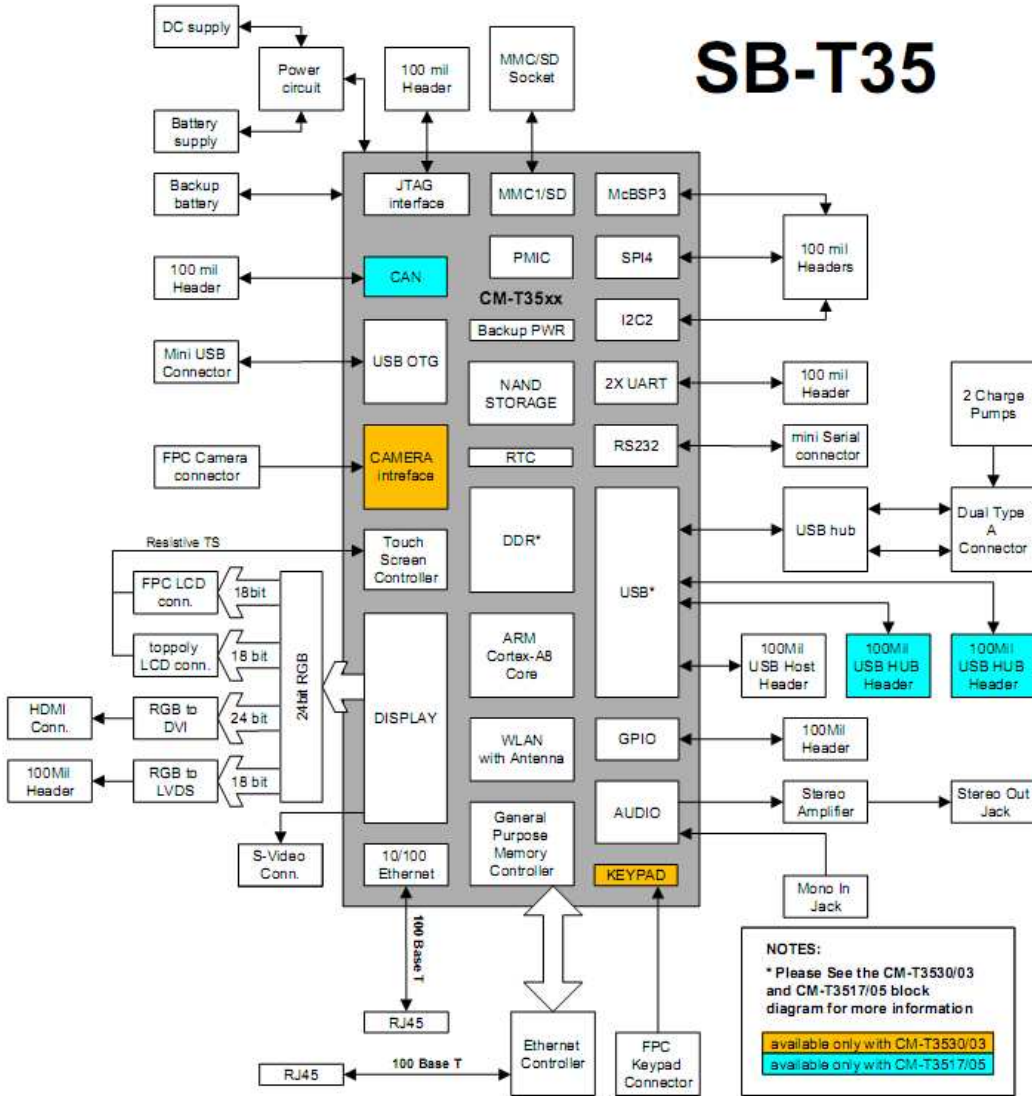


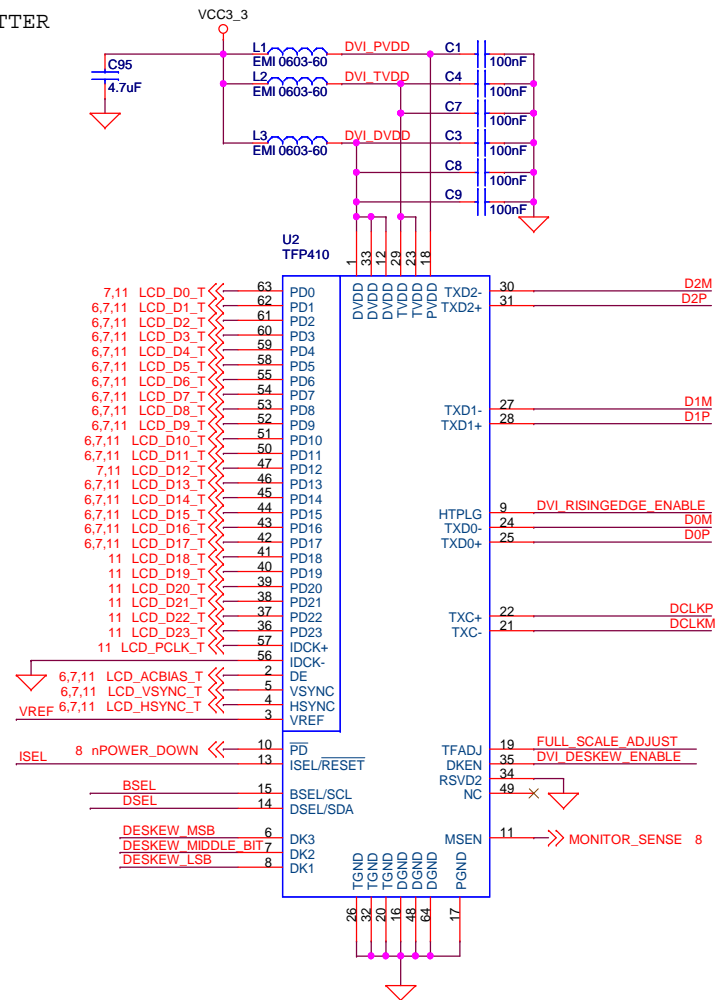
SB-T35 REV 1.3

PAGE	FUNCTION
1	INDEX
2	CM-T35 INTERFACE
3	DVI & LVDS
4	ETHERNET
5	POWER
6	CONNECTORS - 1
7	CONNECTORS - 2
8	LEVEL SHIFTERS
9	USB HUB
10	MISC
11	JUMPERS & MISC
12	MECHANICS

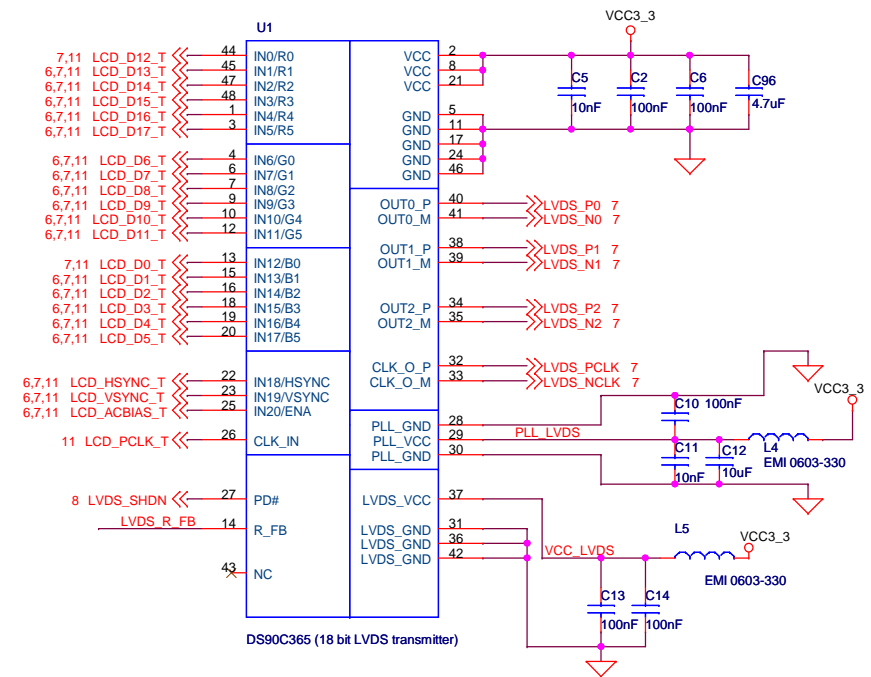


ZZ1
P/N = 188Z10030
PARSER VERSION 1.0

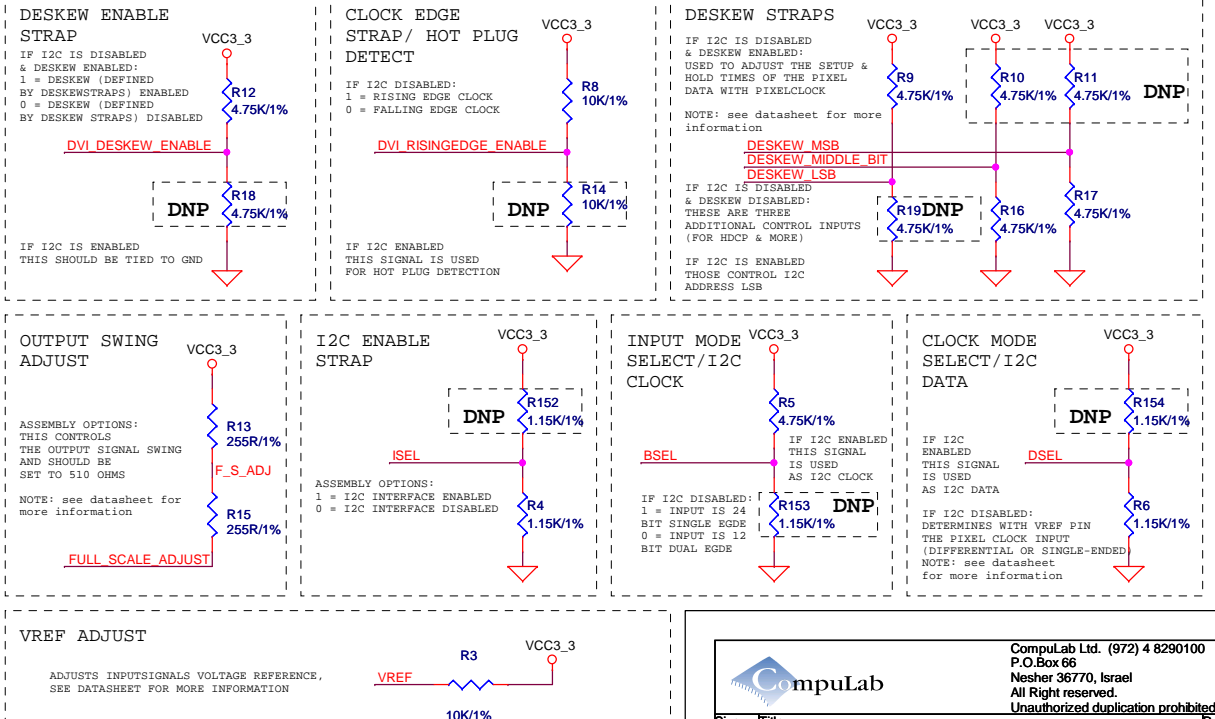
DVI TRANSMITTER



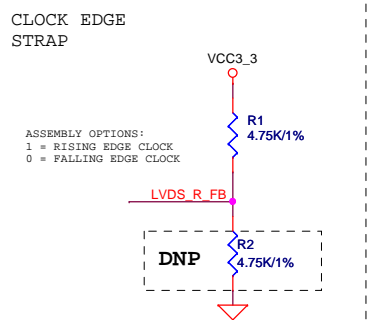
LVDS TRANSMITTER



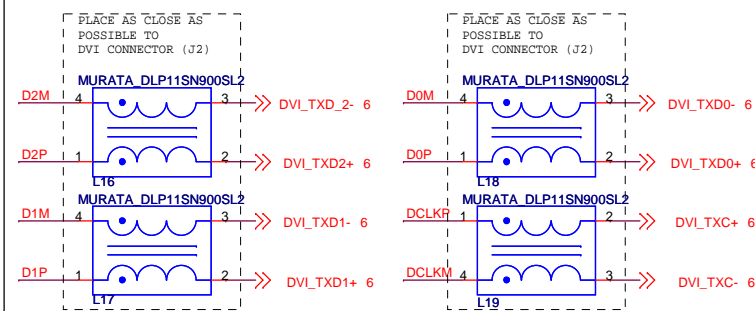
DVI HARDWARE CONFIGURATION & CONTROL

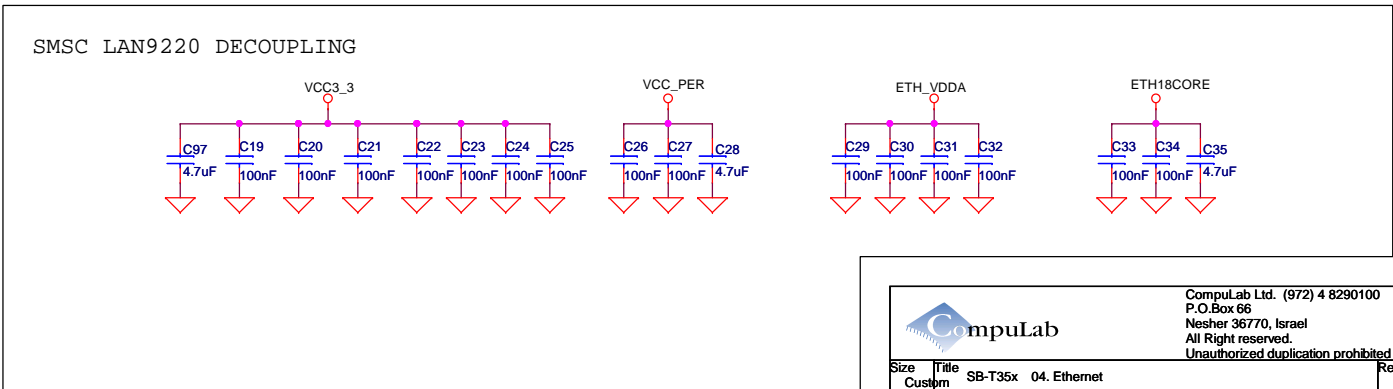
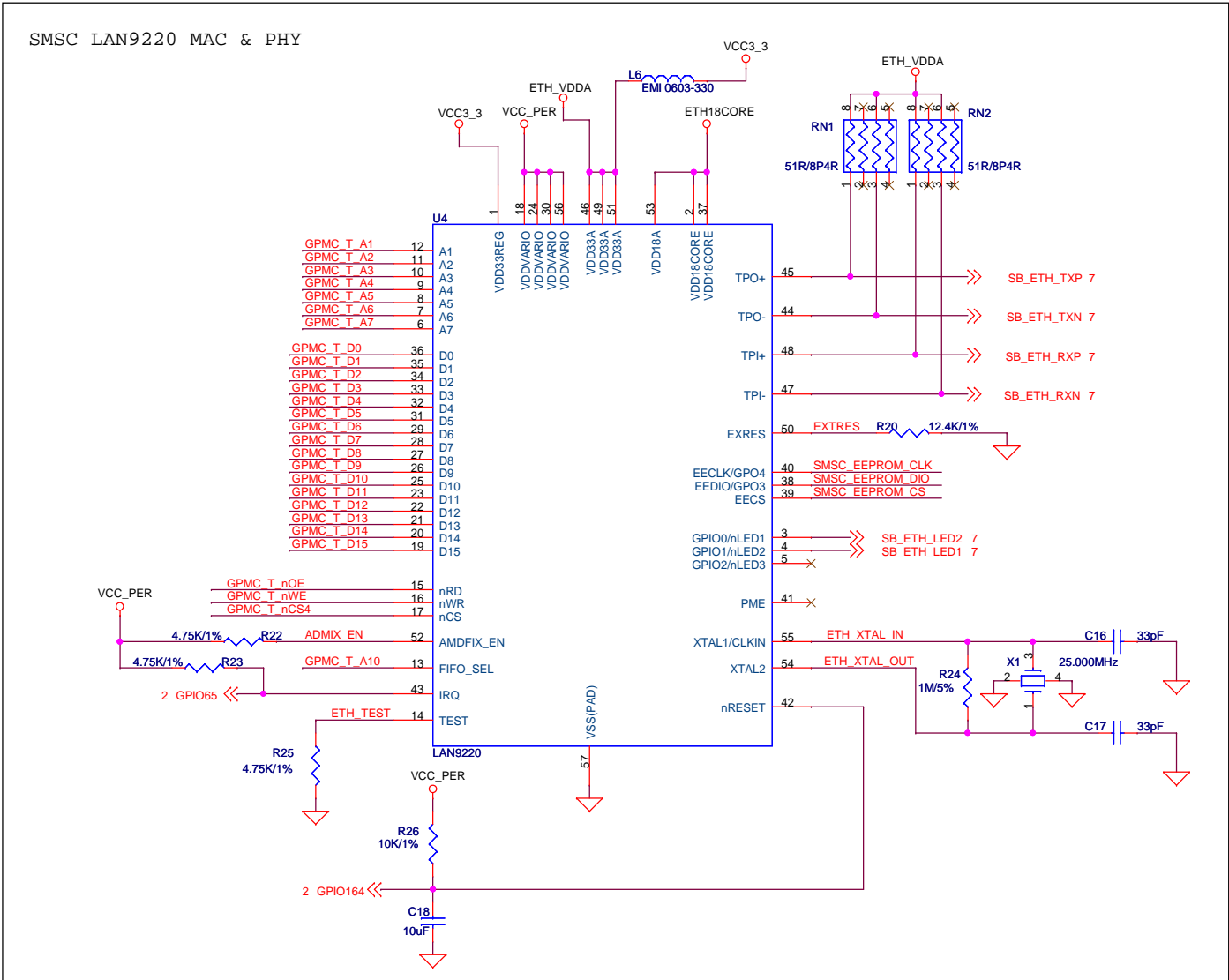
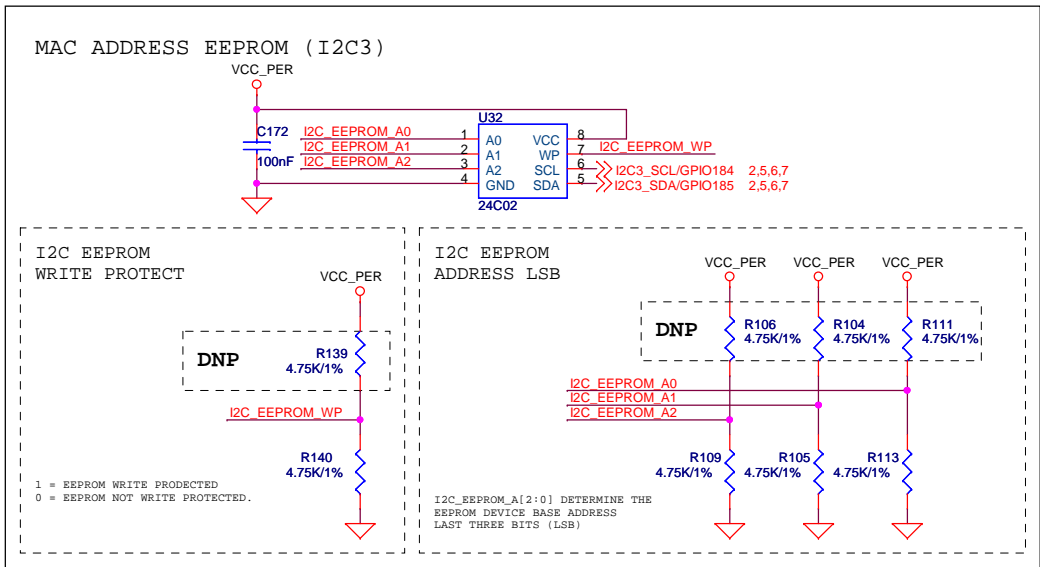
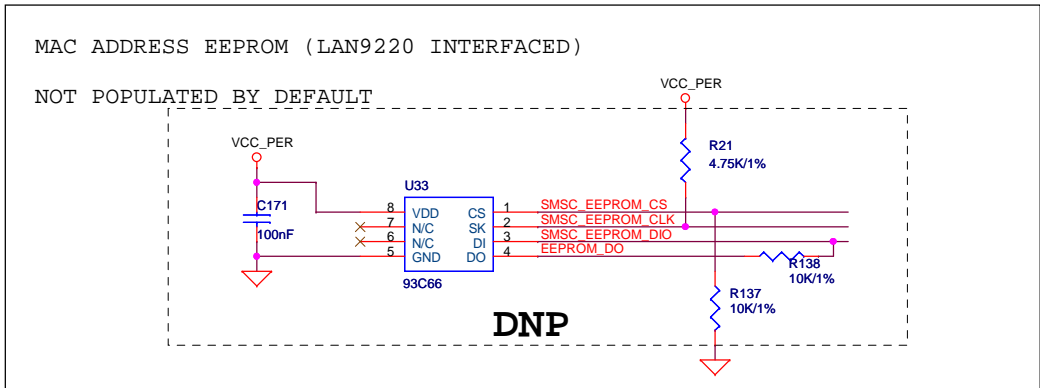
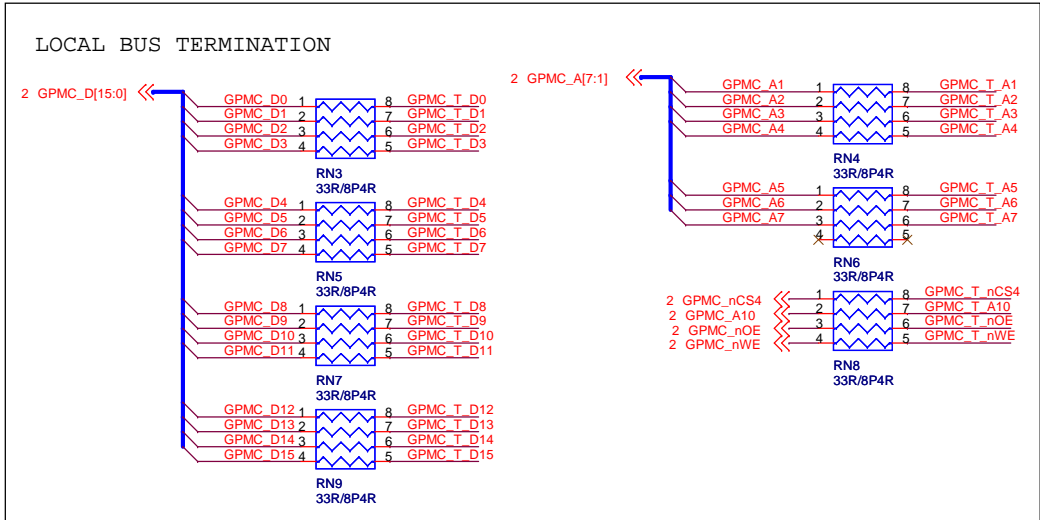


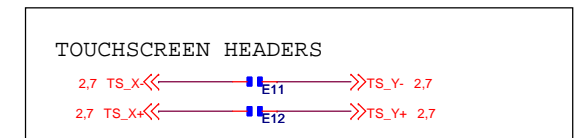
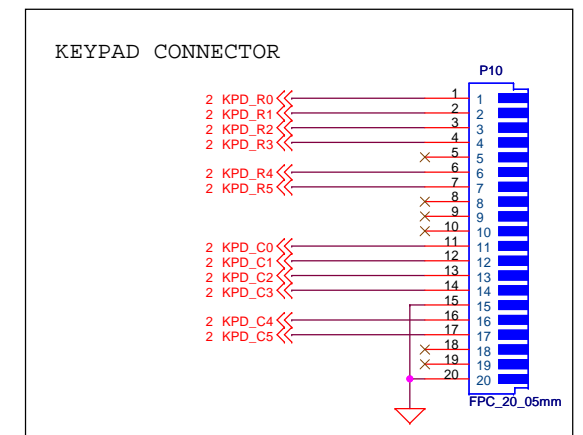
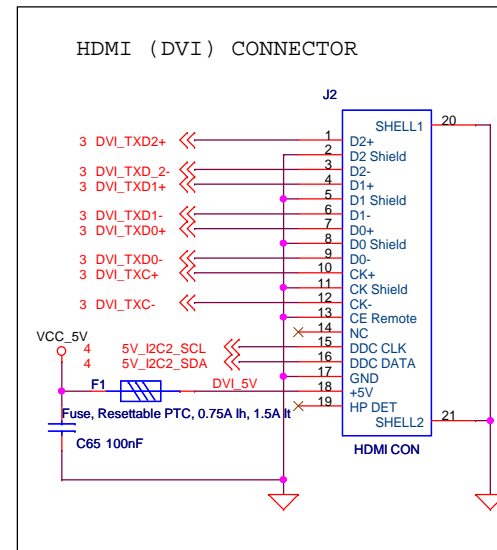
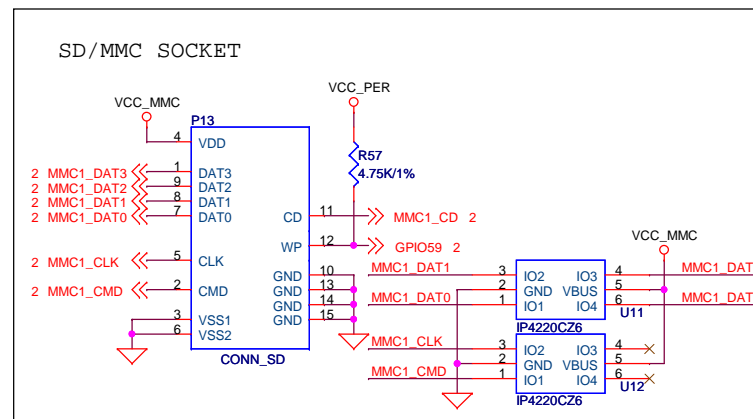
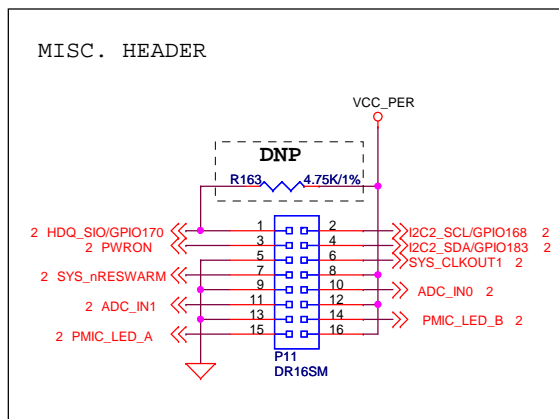
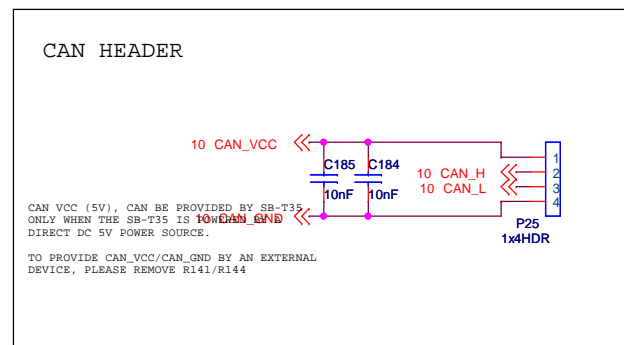
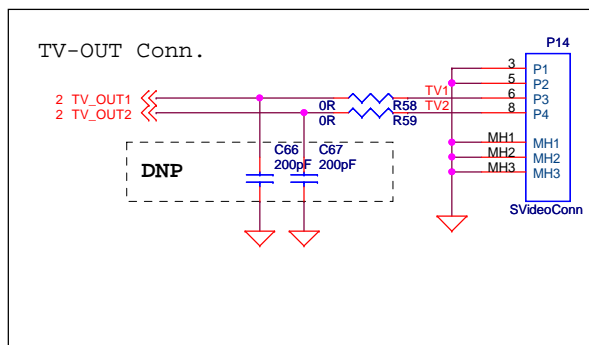
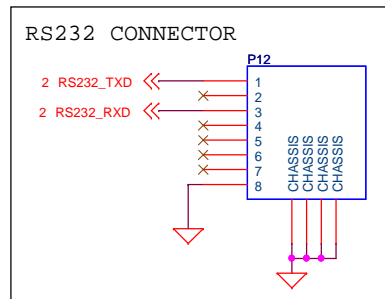
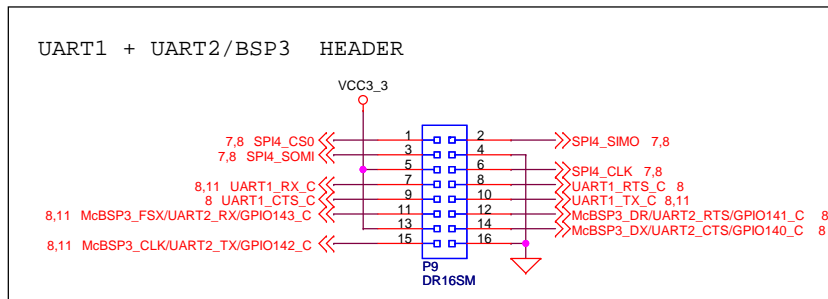
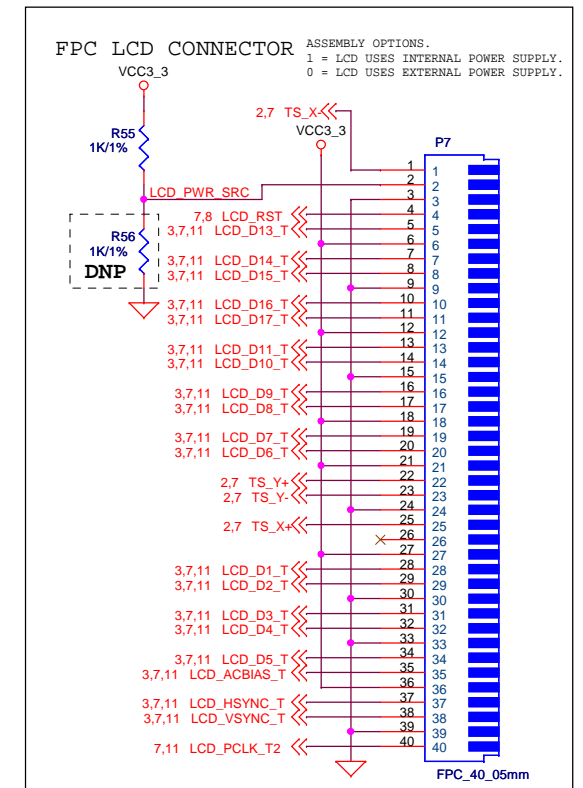
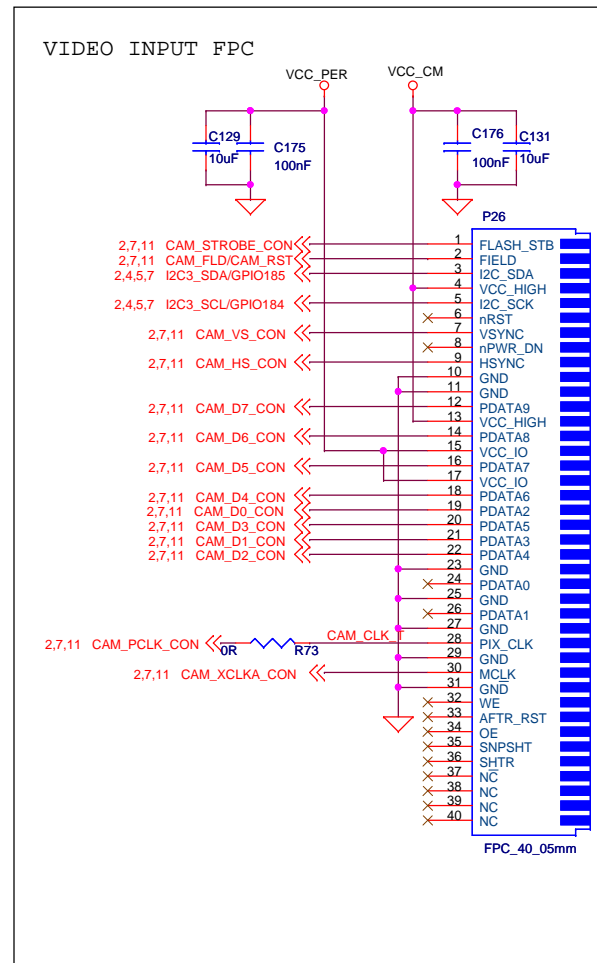
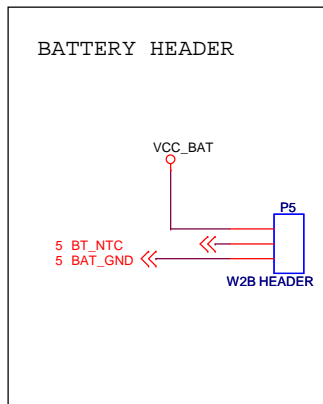
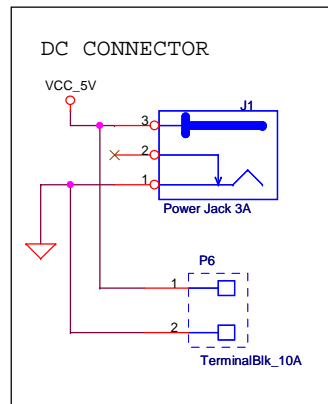
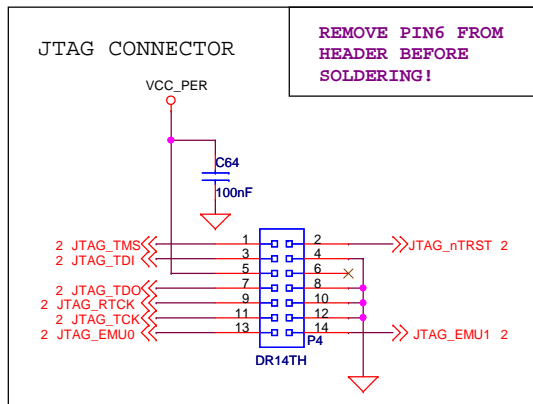
LVDS TRANSMITTER STRAPS

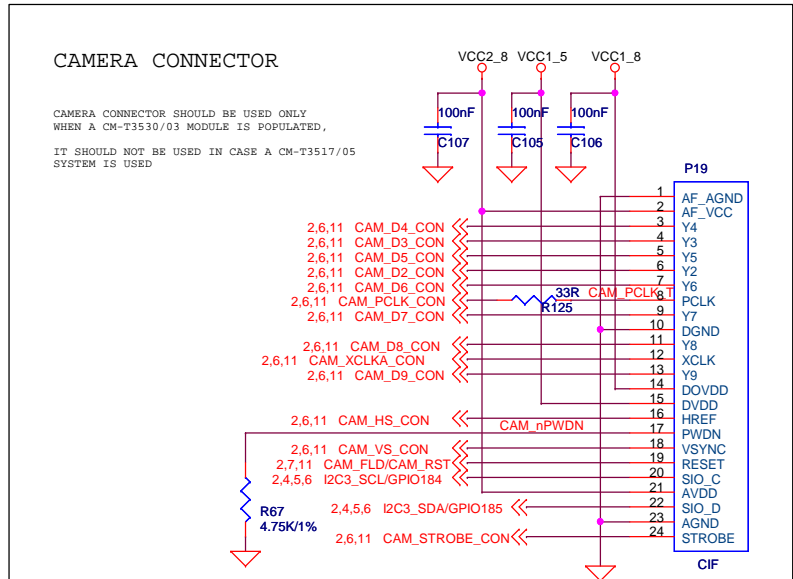
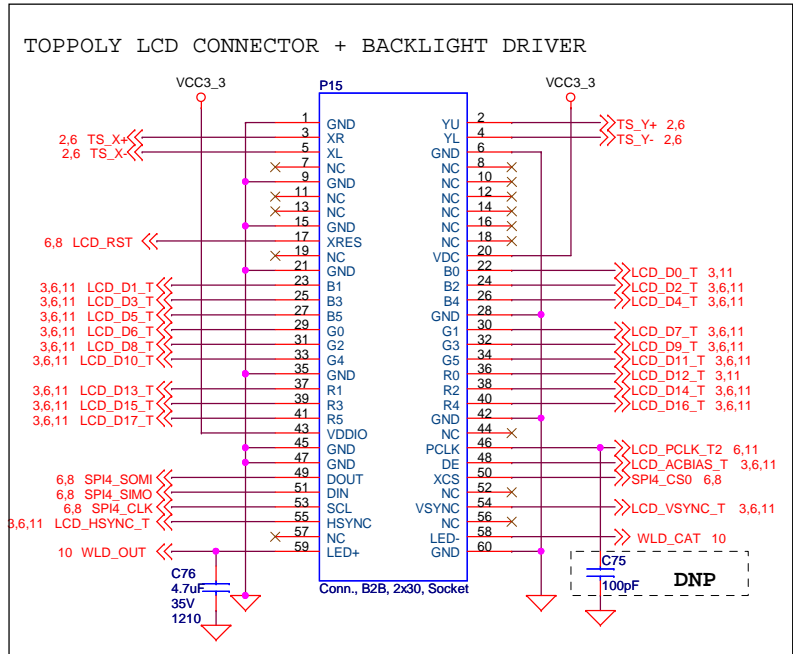
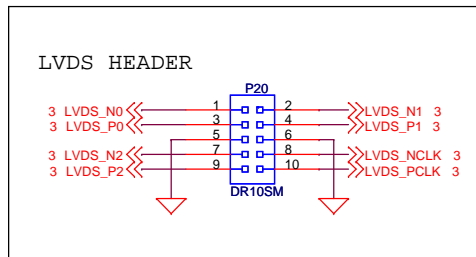
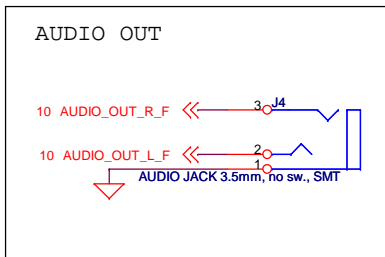
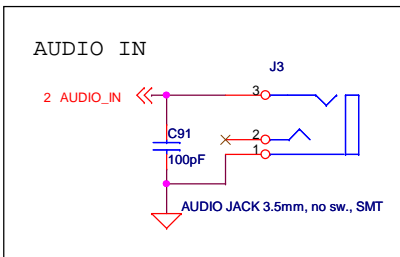
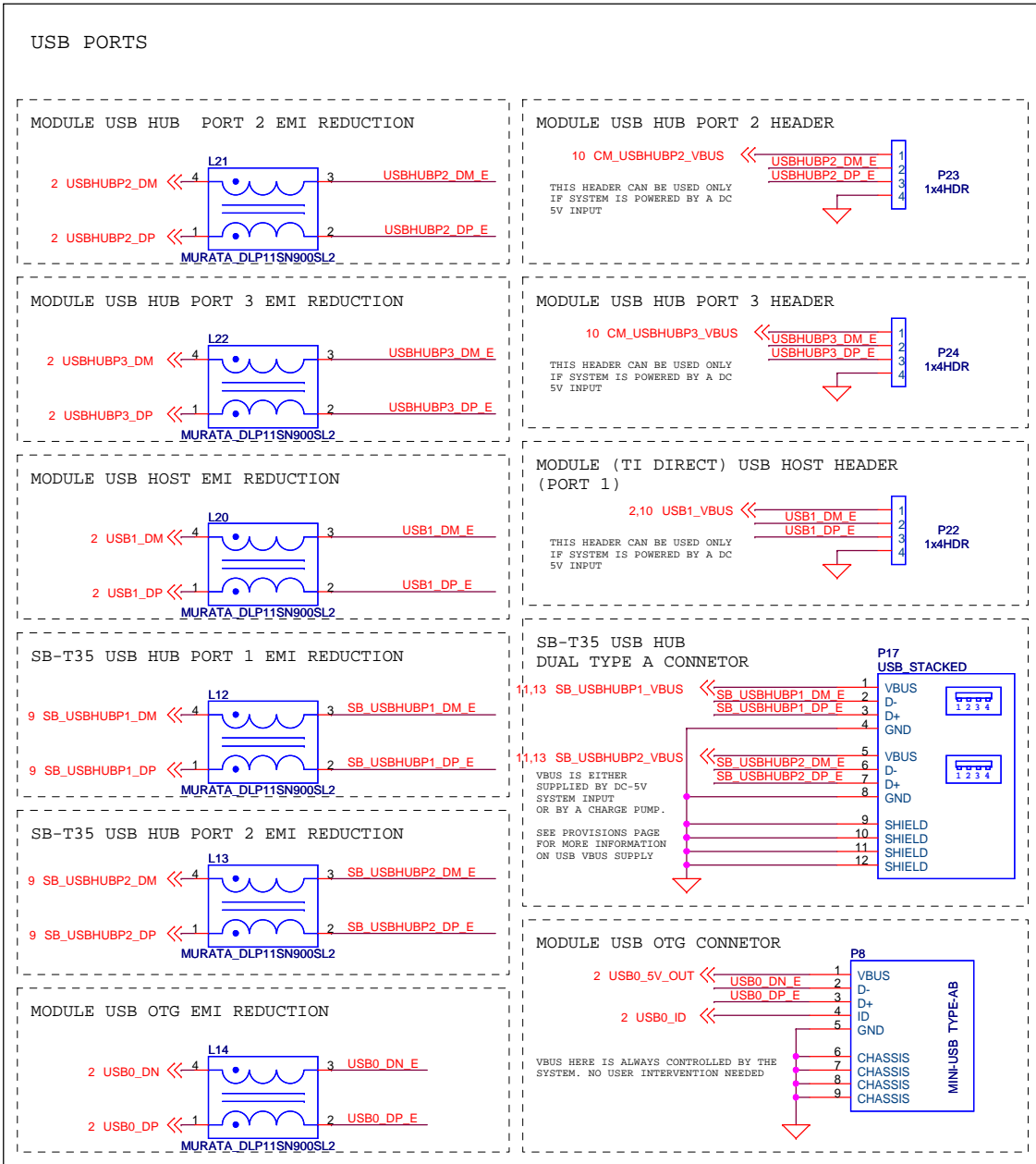
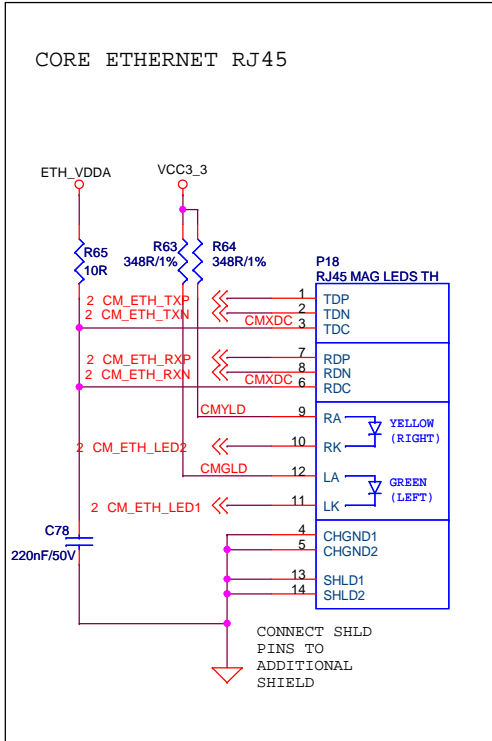
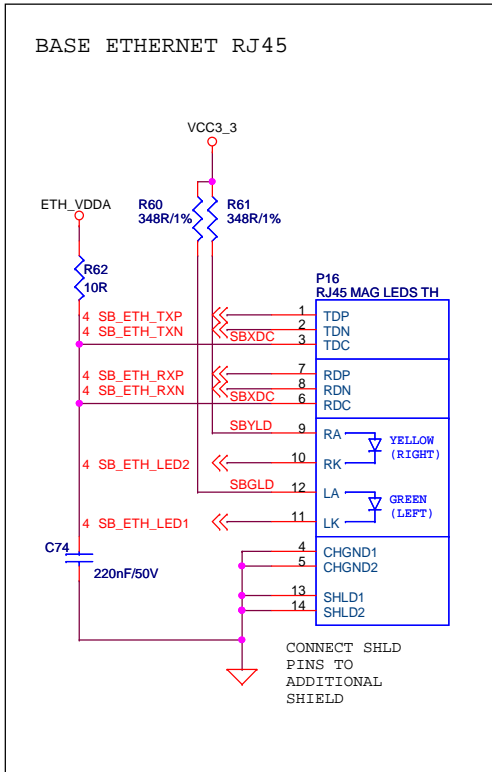


DVI OUTPUT EMI REDUCTION

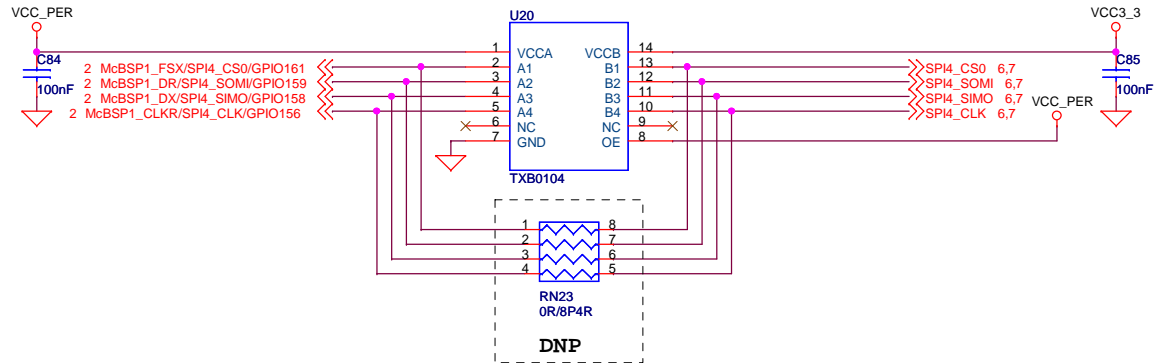
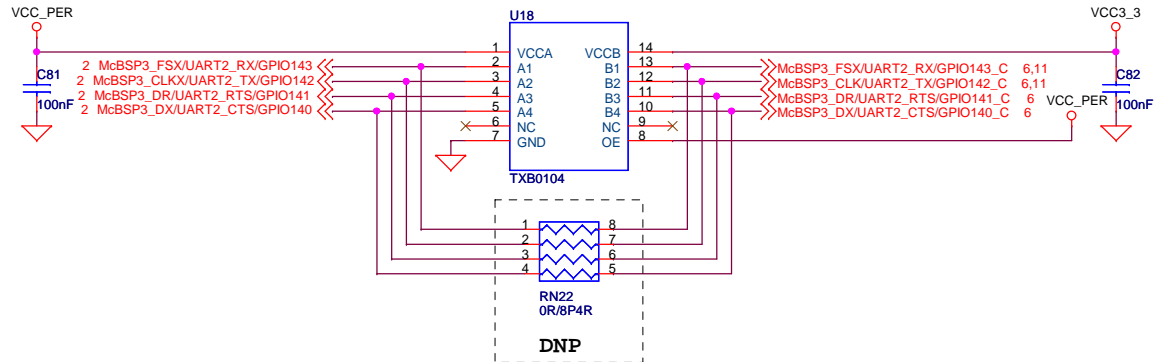
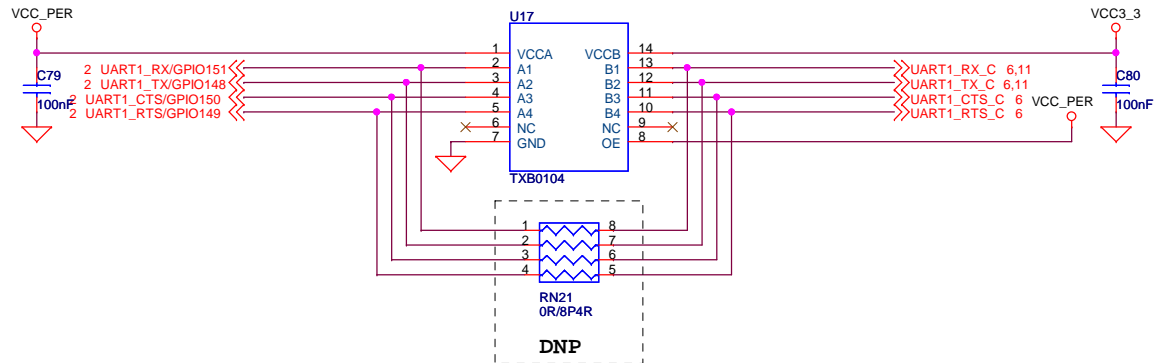




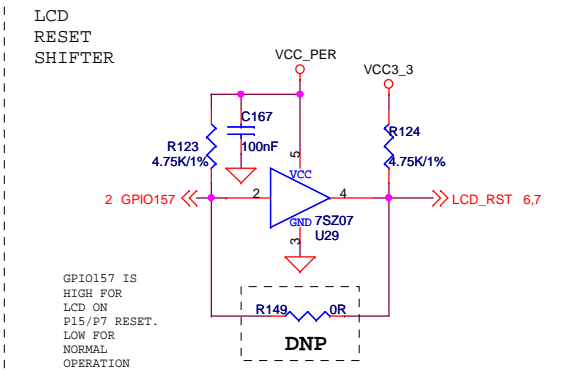
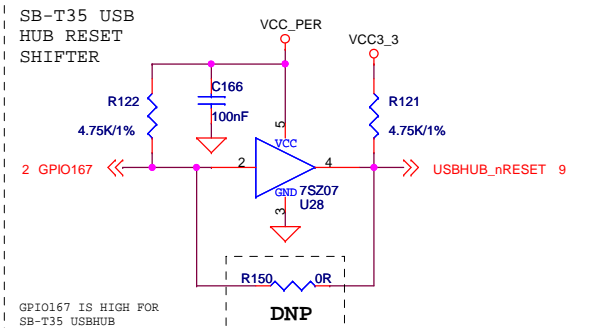
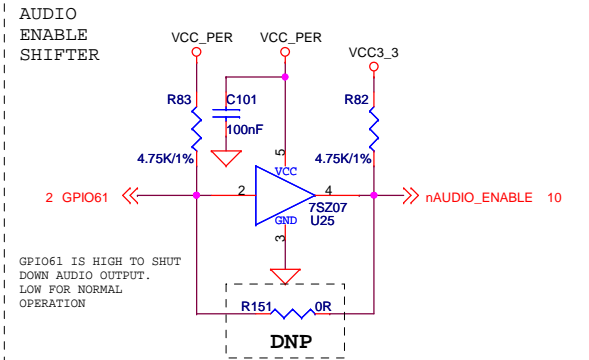
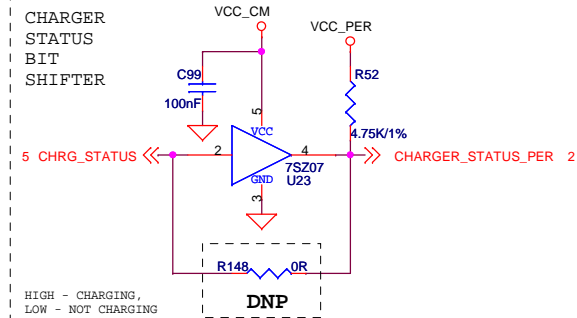
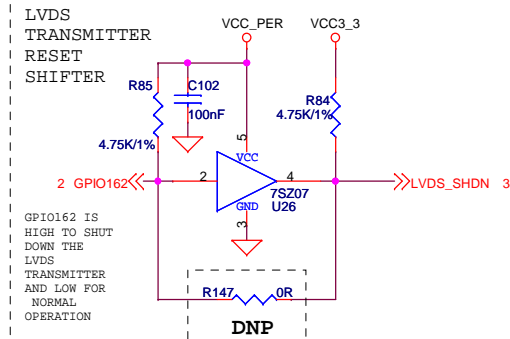
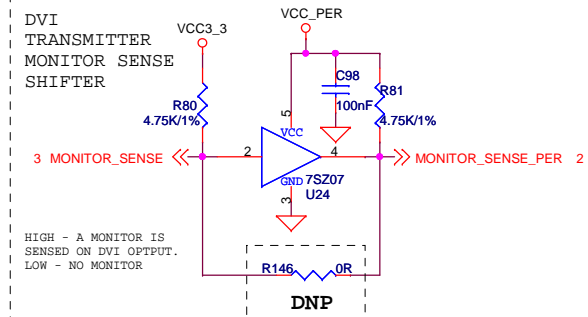
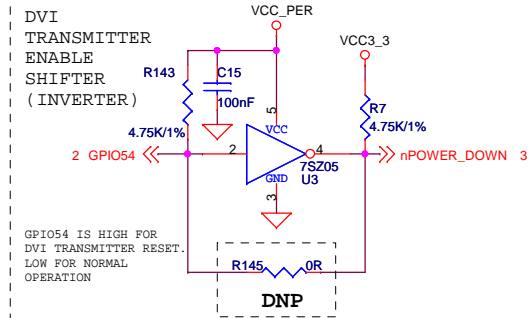




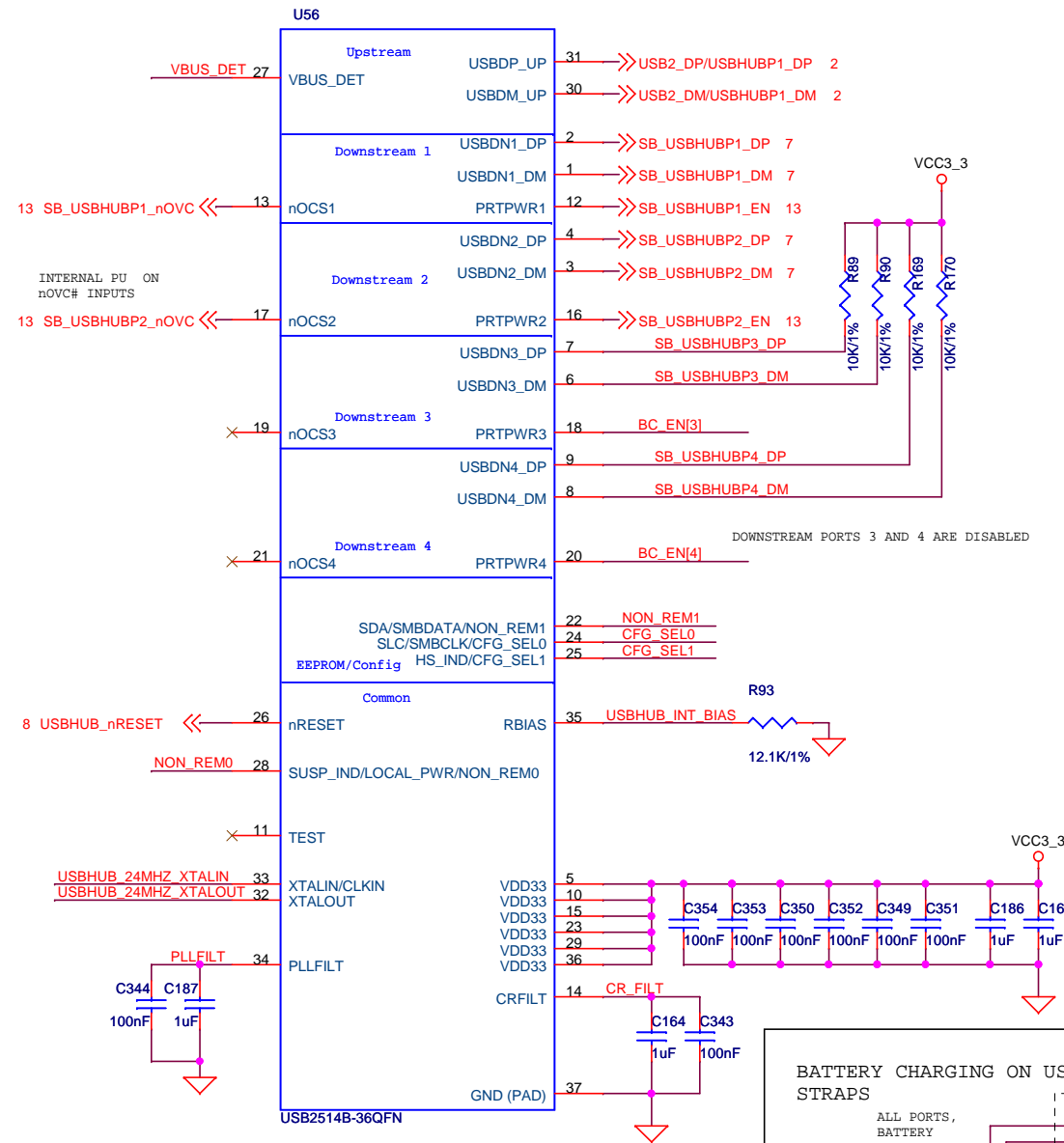
BIDIRECTIONAL LEVEL SHIFTERS & BYPASS



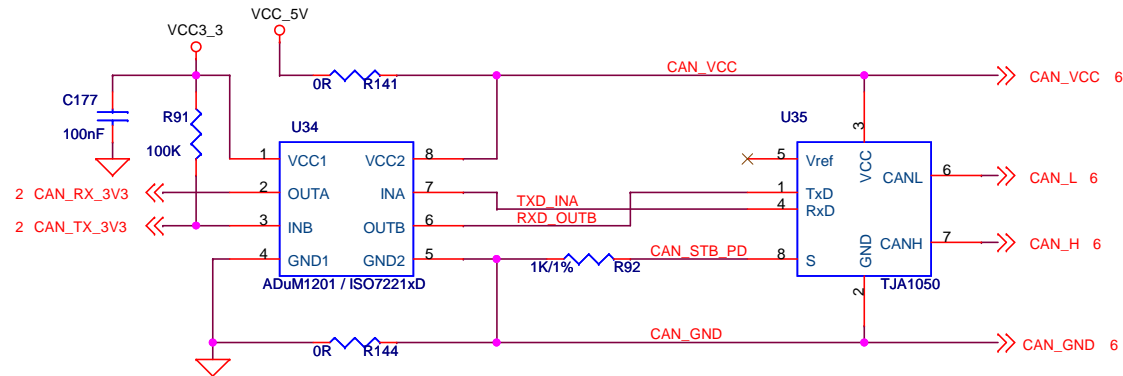
OPEN DRAIN LEVEL SHIFTERS



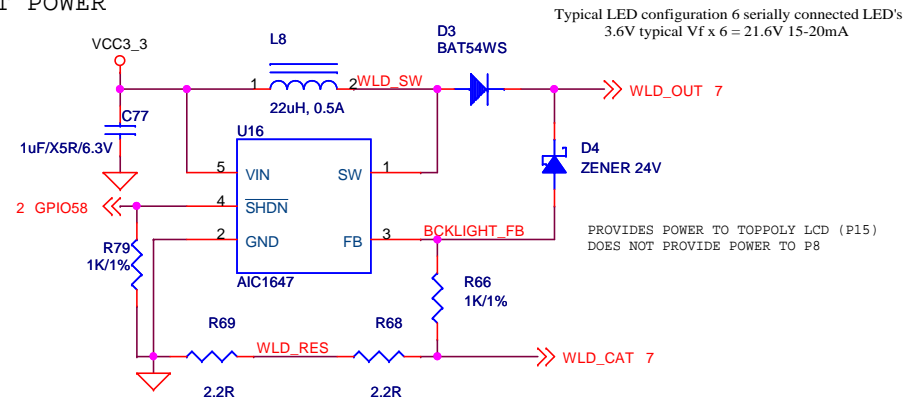
USB HUB



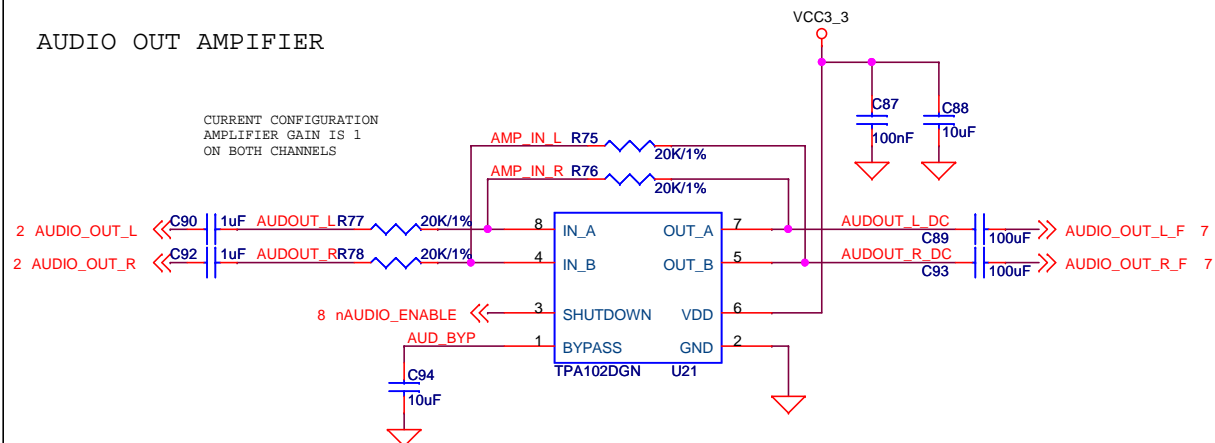
CAN ISOLATION, POWER & PHY



LCD BACKLIGHT POWER

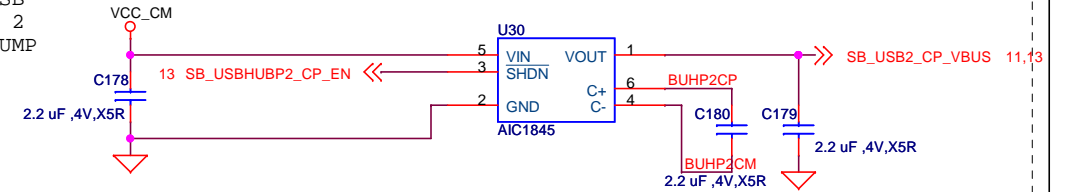


AUDIO OUT AMPIIFIER

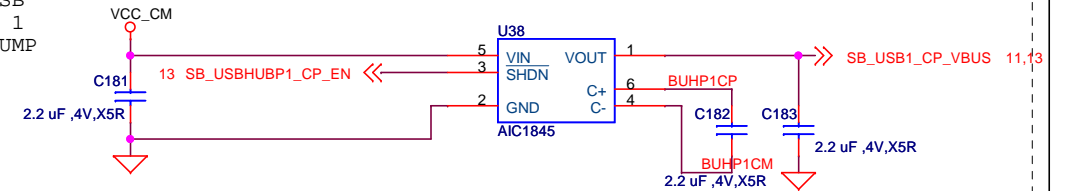


USB POWER

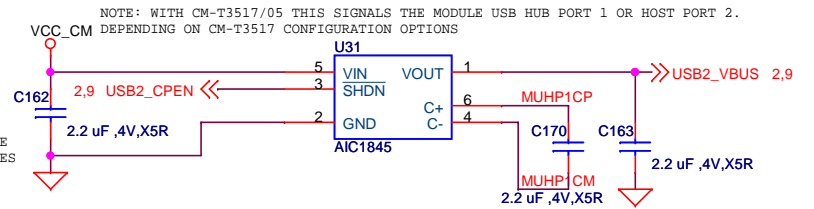
SB-T35 USB
HUB PORT 2
CHARGE PUMP
POWER



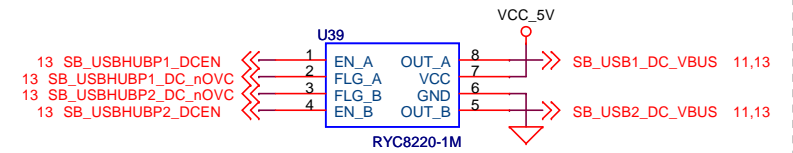
SB-T35 USB
HUB PORT 1
CHARGE PUMP
POWER



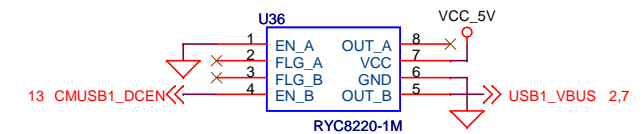
MODULE USB PORT 2
VBUS SIGNAL



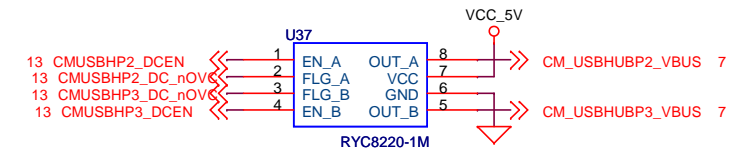
SB-T35 USB HUB
PORTS 1 & 2
DIRECT DC 5V
POWER



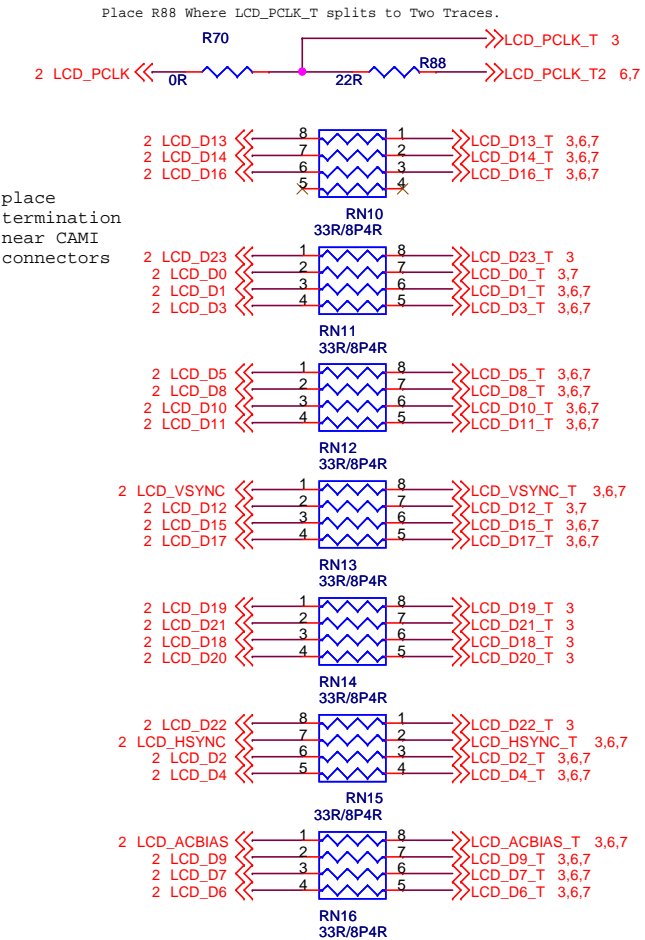
MODULE (TI DIRECT) USB
HOST (PORT 1)
DIRECT DC 5V POWER



MODULE USB HUB
PORTS 2 & 3
DIRECT DC 5V
POWER

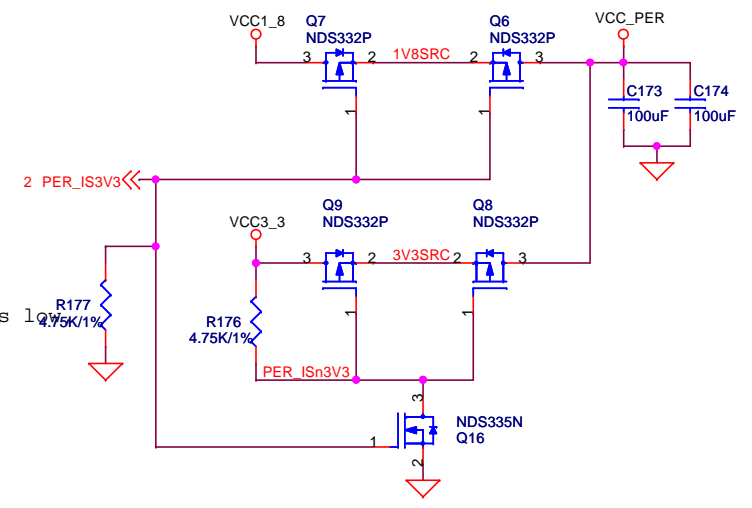


LCD TERMINATION

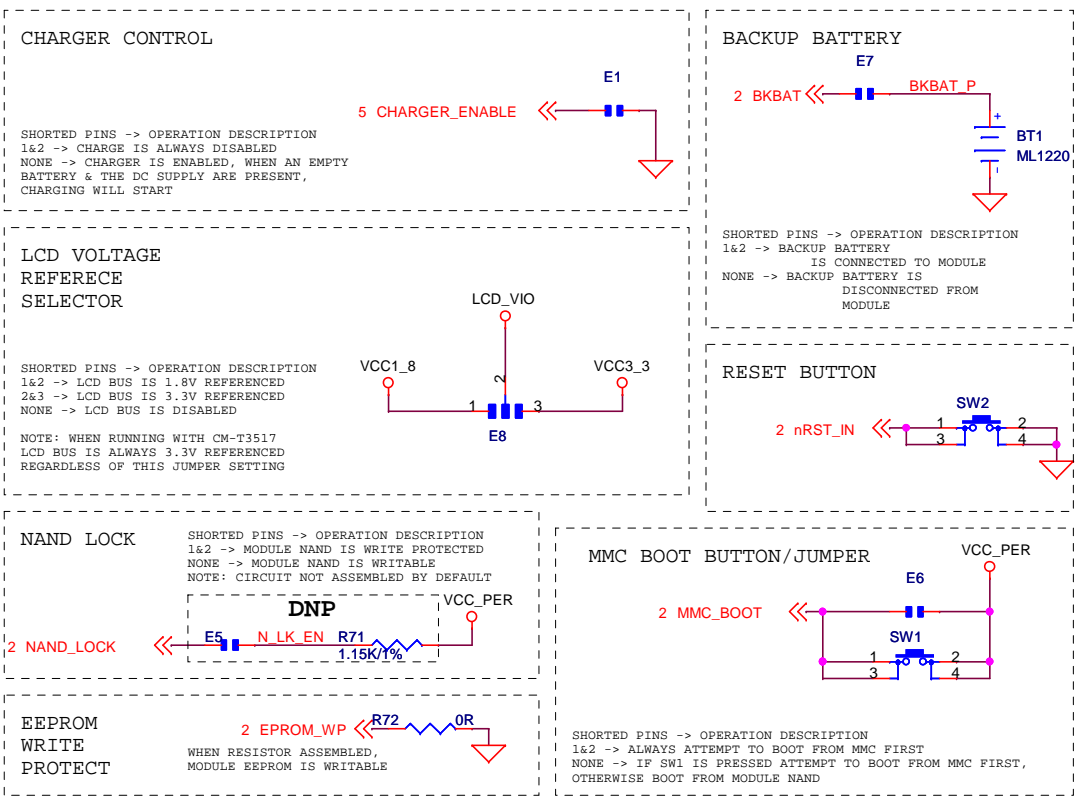


MODULE IO VOLTAGE AUTO-SELECTOR

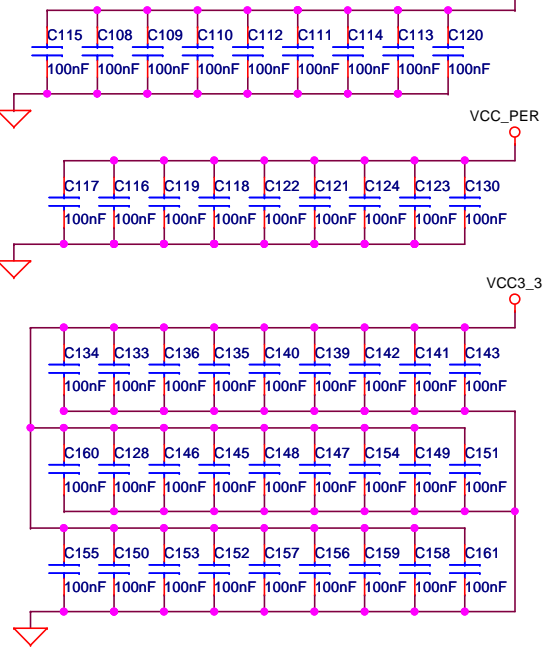
OPERATION DESCRIPTION:
The signal PER_IS3V3 is connected to the CM-T3XXX module through connectors P1 & P2.
When CM-T3517 is populated --> PER_IS3V3 is driven high therefore Transistors Q9 & Q8 are open, and transistors Q6 & Q7 are closed. --> VCC_PER is 3.3V
When CM-T3X30 is populated --> PER_IS3V3 is not driven, so it's low therefore Transistors Q9 & Q8 are closed, and transistors Q6 & Q7 are open. --> VCC_PER is 1.8V



JUMPERS, BUTTONS & SETTINGS



DECOUPLING CAPS



STANDOFFS

STANDOFF1



Spacer,(Standoff) Hexagonal+ext, M3/L12mm+M3/L6mm, Brass, Nickel coating

STANDOFF2



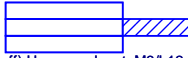
Spacer,(Standoff) Hexagonal+ext, M3/L12mm+M3/L6mm, Brass, Nickel coating

STANDOFF3



Spacer,(Standoff) Hexagonal+ext, M3/L12mm+M3/L6mm, Brass, Nickel coating

STANDOFF4



Spacer,(Standoff) Hexagonal+ext, M3/L12mm+M3/L6mm, Brass, Nickel coating

STANDOFF5



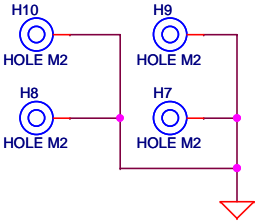
Spacer,(Standoff) Hexagonal+ext, M3/L12mm+M3/L6mm, Brass, Nickel coating

STANDOFF6

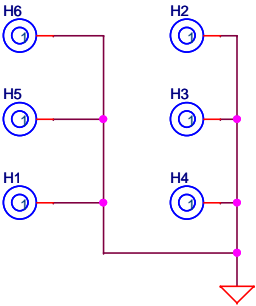


Spacer,(Standoff) Hexagonal+ext, M3/L12mm+M3/L6mm, Brass, Nickel coating

5mm MECH.
HOLES



6mm MECH.
HOLES



3.5mm SCREWS



SCREW1
SCREW, M2, 3.5mm, PAN HEAD



SCREW2
SCREW, M2, 3.5mm, PAN HEAD

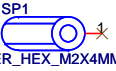


SCREW3
SCREW, M2, 3.5mm, PAN HEAD

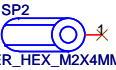


SCREW4
SCREW, M2, 3.5mm, PAN HEAD

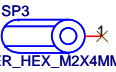
4mm SPACERS



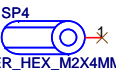
SP1
SPACER_HEX_M2X4MM



SP2
SPACER_HEX_M2X4MM



SP3
SPACER_HEX_M2X4MM



SP4
SPACER_HEX_M2X4MM

JUMPERS

JP1



Jumper, black, 100mil

JP7



Jumper, black, 100mil

JP2



Jumper, black, 100mil

JP8



Jumper, black, 100mil

JP3



Jumper, black, 100mil

JP4



Jumper, black, 100mil

JP5



Jumper, black, 100mil

JP6



Jumper, black, 100mil

JP9



Jumper, black, 100mil

MECHANICAL NUTS

NUT1



Nut, Hex, M3, Steel, Zinc plating, 2.4mm thick, 5.5mm Width

NUT2



Nut, Hex, M3, Steel, Zinc plating, 2.4mm thick, 5.5mm Width

NUT3



Nut, Hex, M3, Steel, Zinc plating, 2.4mm thick, 5.5mm Width

NUT4



Nut, Hex, M3, Steel, Zinc plating, 2.4mm thick, 5.5mm Width

NUT5



Nut, Hex, M3, Steel, Zinc plating, 2.4mm thick, 5.5mm Width

NUT6



Nut, Hex, M3, Steel, Zinc plating, 2.4mm thick, 5.5mm Width

TEST POINTS

2 MIC_BIAS << TP7

THESE TEST POINTS AVAILABLE WHEN EITHER A
CM-T351705 OR THE CM-3530/03 IS ASSEMBLED

THESE TEST POINTS ARE
AVAILABLE ONLY WITH
CM-T3517/05 MODULE.
THEY ARE NOT
CONNECTED WHEN THE
CM-3530/03 IS
ASSEMBLED

2 PWR_GOOD << TP1

2 ETH_BOOT_PU << TP2

2 CMUSBP1_nOVC << TP3

2 SYS_nIRQ/GPIO0 << TP4

2 3517_AUDIO_R_IN << TP5



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Nesher 36770, Israel
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Date: Monday, November 01, 2010	Document Number: 8000351003	
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