

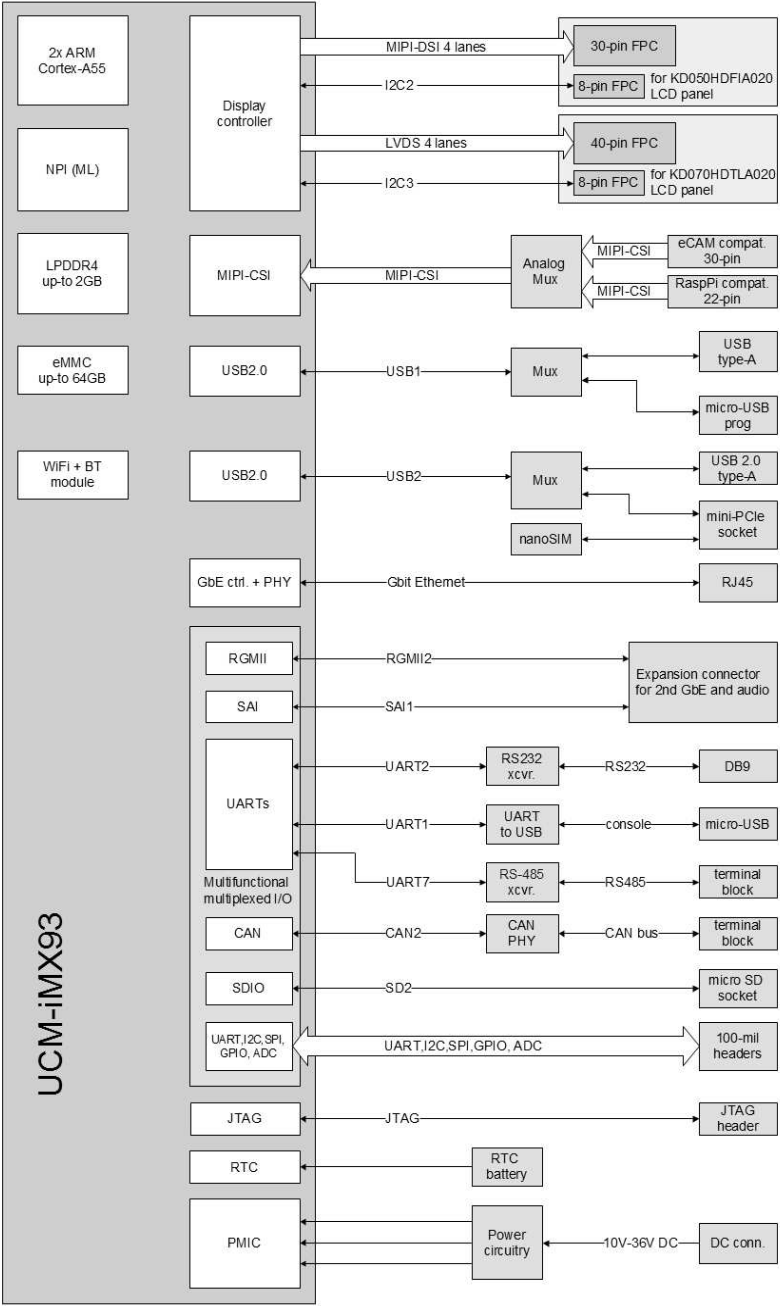
SB-UCMIMX93

BOARD REVISION: 1.0

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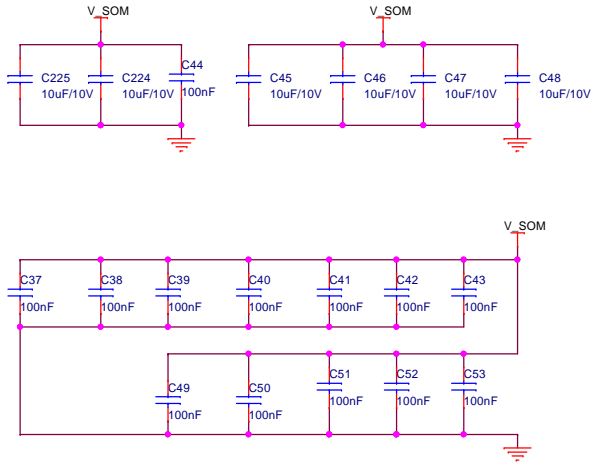
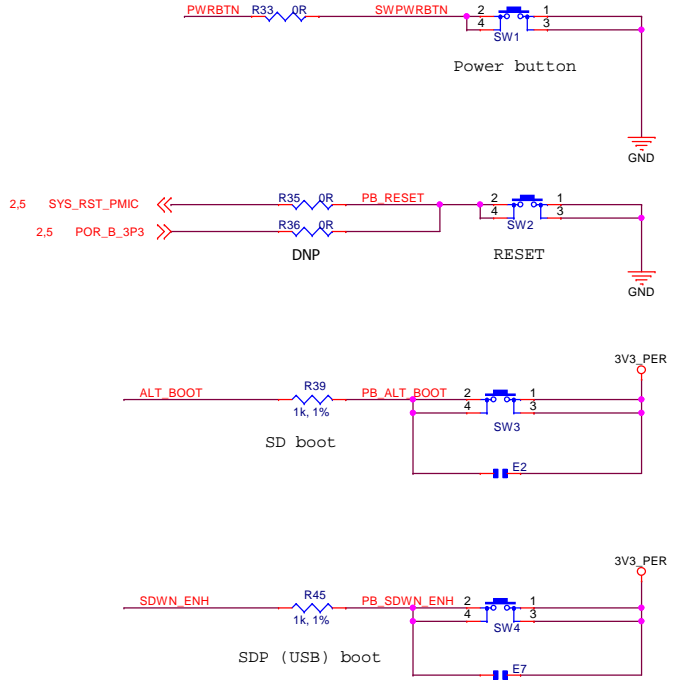
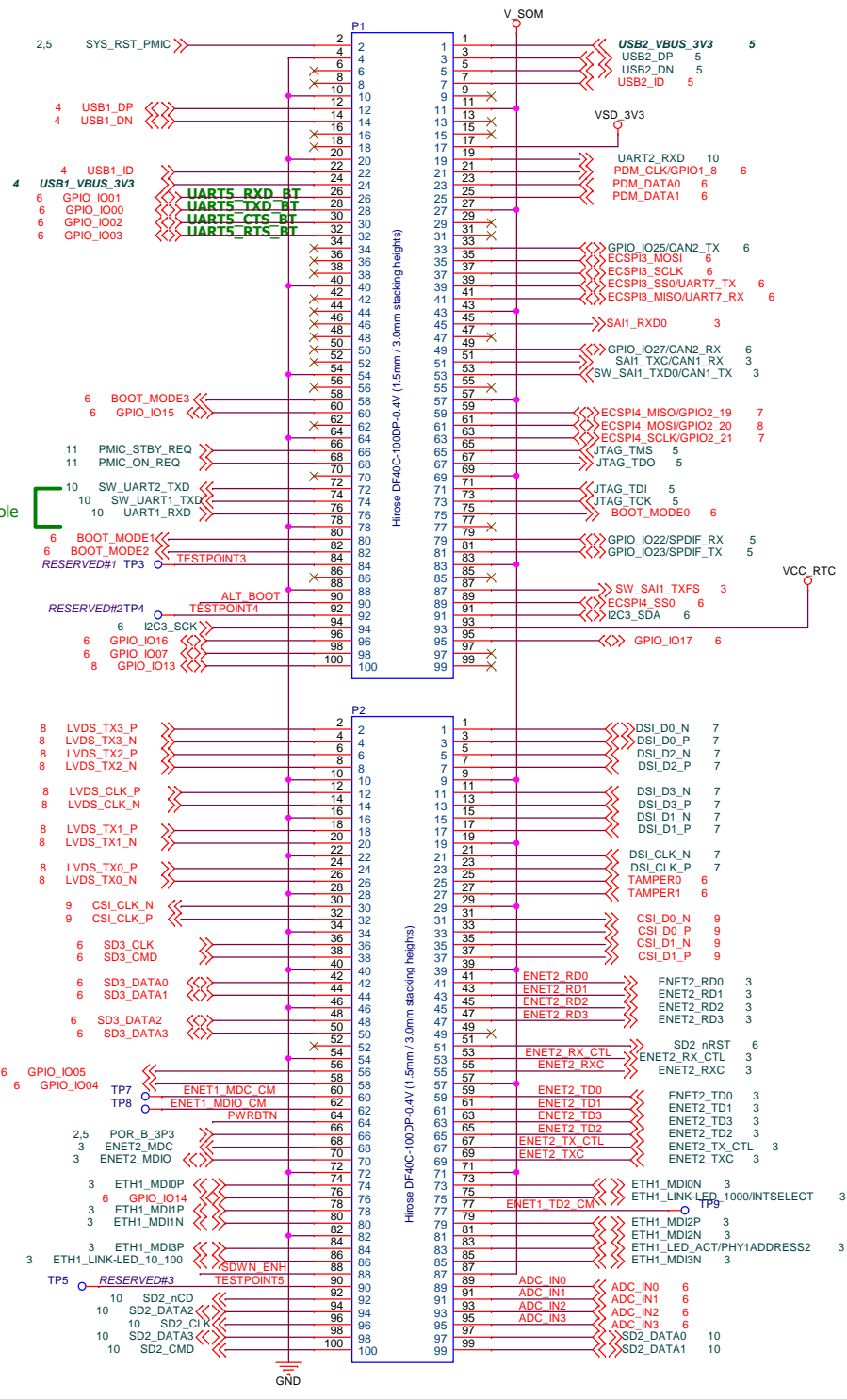
PCB1
PCB, SB-UCMIMX93, Rev 1.0
P/N = 188C04390

ZZ1
PARSER_VERSION_1.0

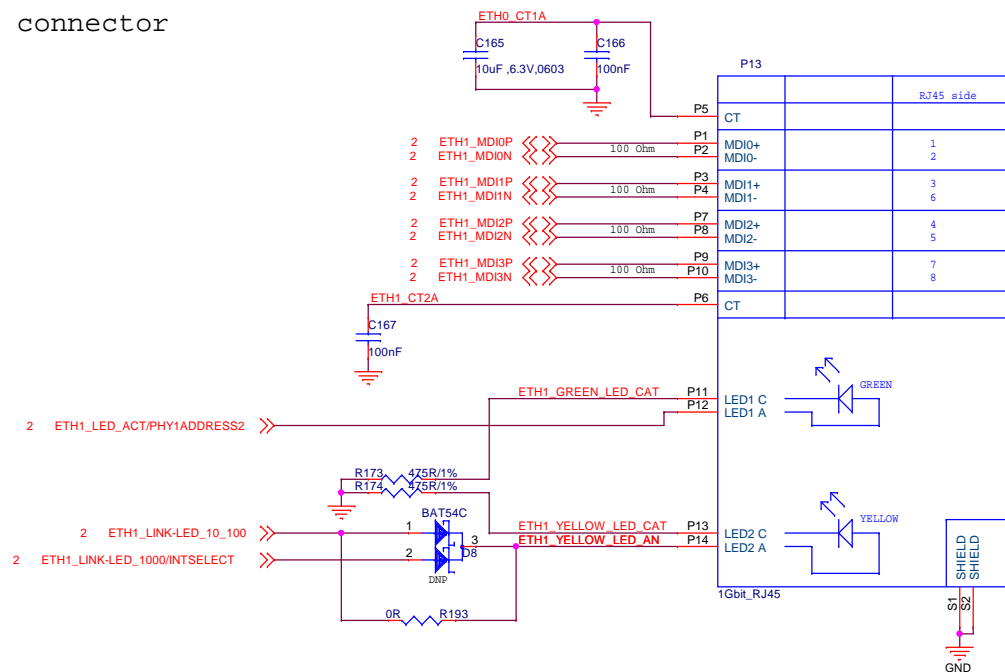


SOM connectors

A53 core console

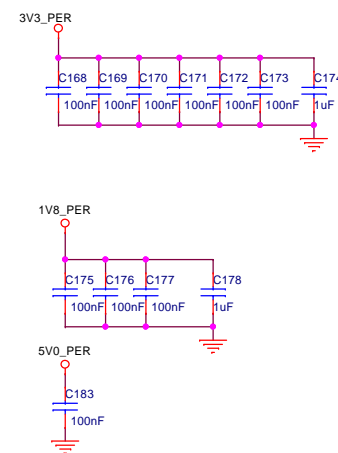
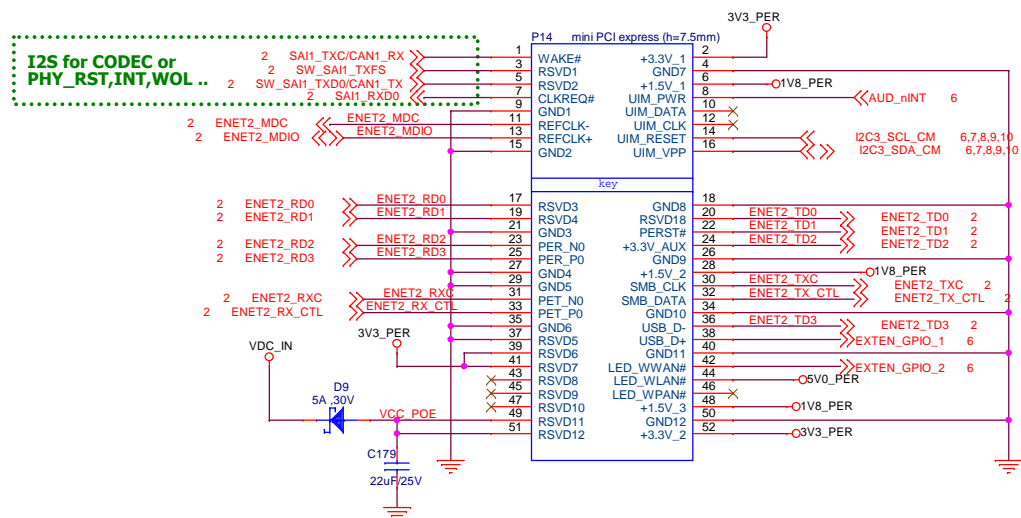


Ethernet connector



Expansion connector

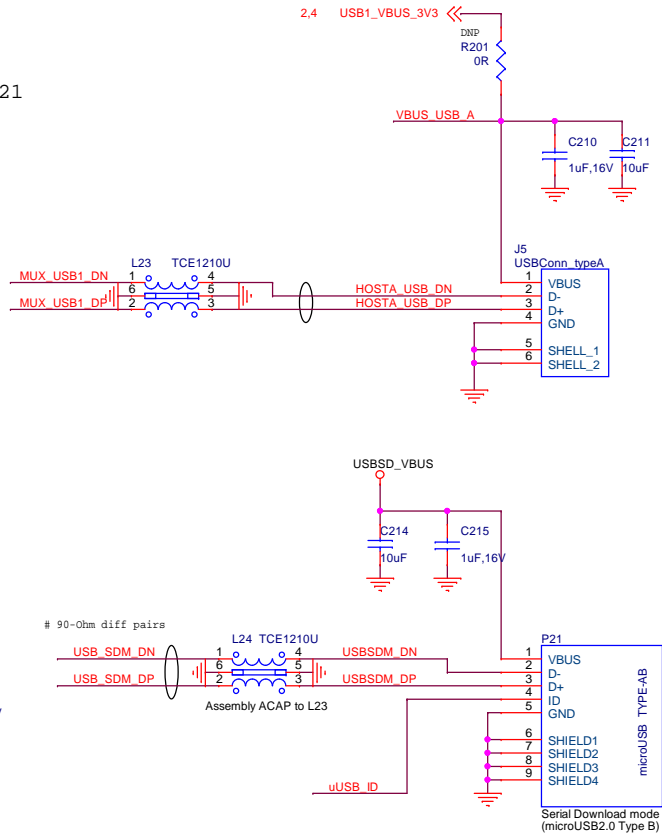
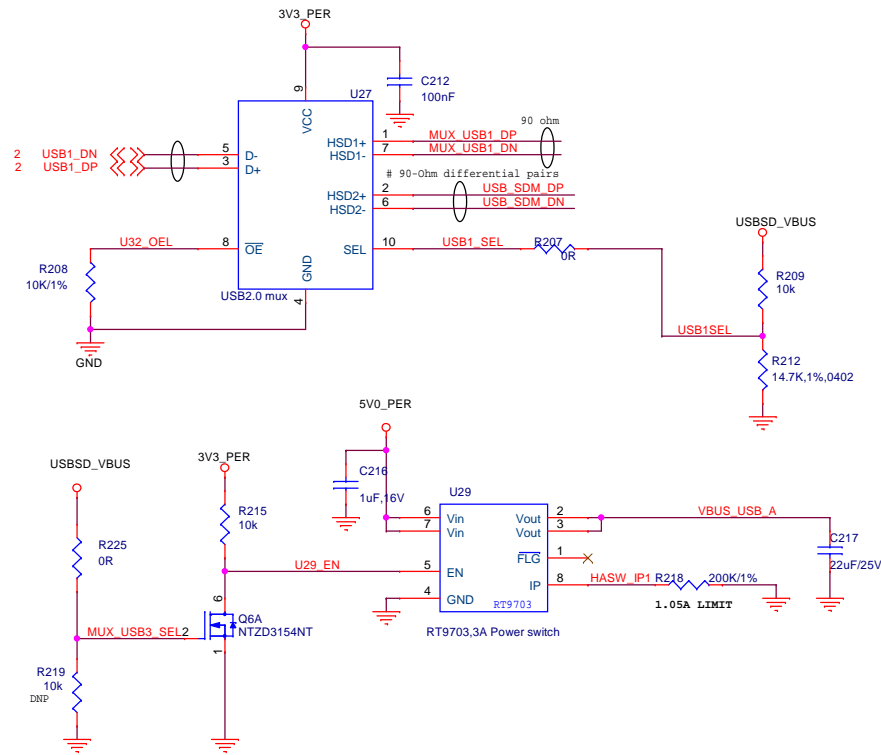
For 2nd Ethernet and RGMII



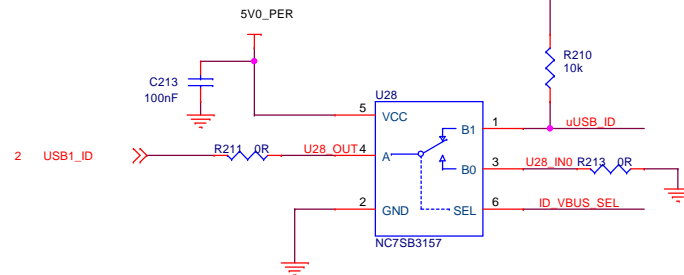
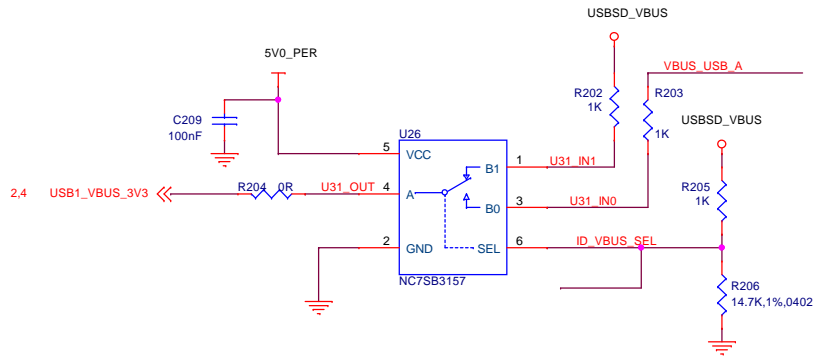
USB1

Host with mux for SDP mode

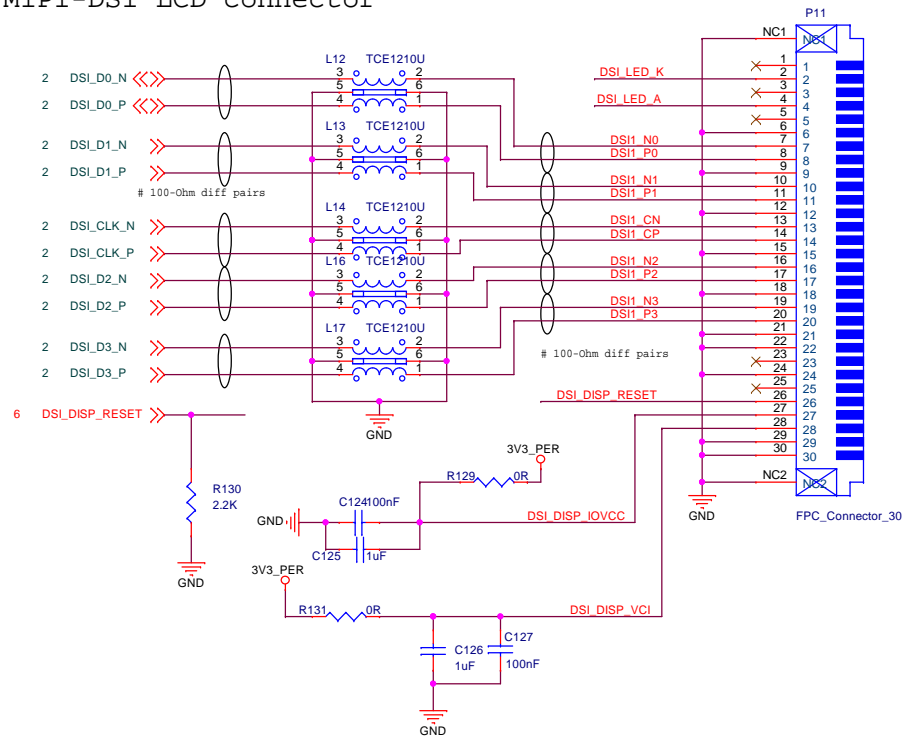
NOTE: USB1 multiplexing is controlled by presence of VBUS on connector P21



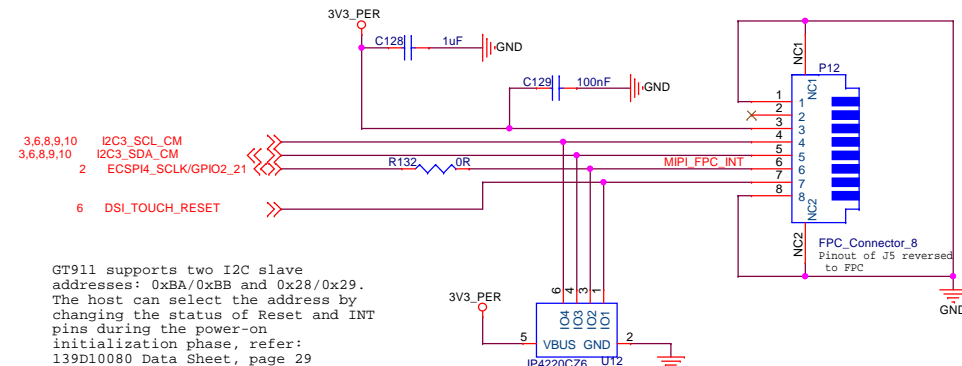
Mux for USB1 ID and VBUS



MIPI-DSI LCD connector

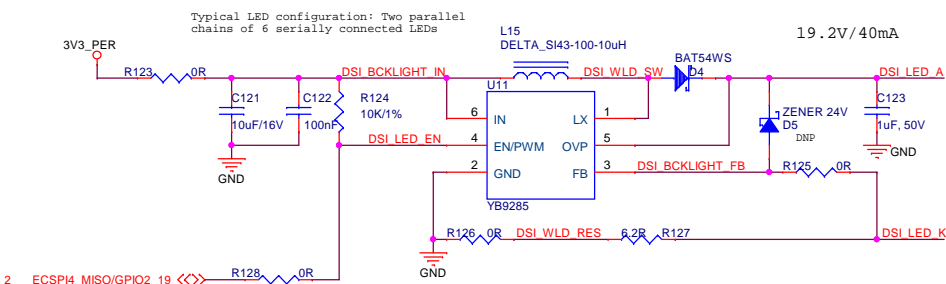


MIPI LCD touch-panel connector

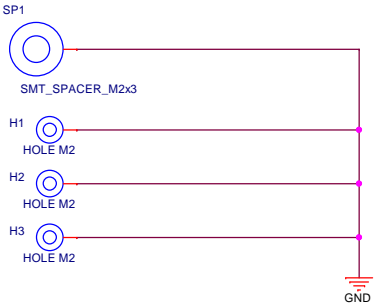


GT911 supports two I2C slave addresses: 0xBA/0xBB and 0x28/0x29. The host can select the address by changing the status of Reset and INT pins during the power-on initialization phase, refer: 139D10080 Data Sheet, page 29

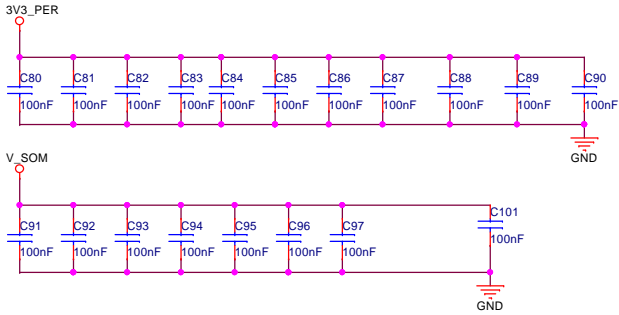
MIPI LCD back-light power



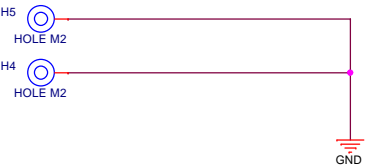
For PCIe module



Stitching capacitors



For Extension Board



12 Jumper Plugs

