

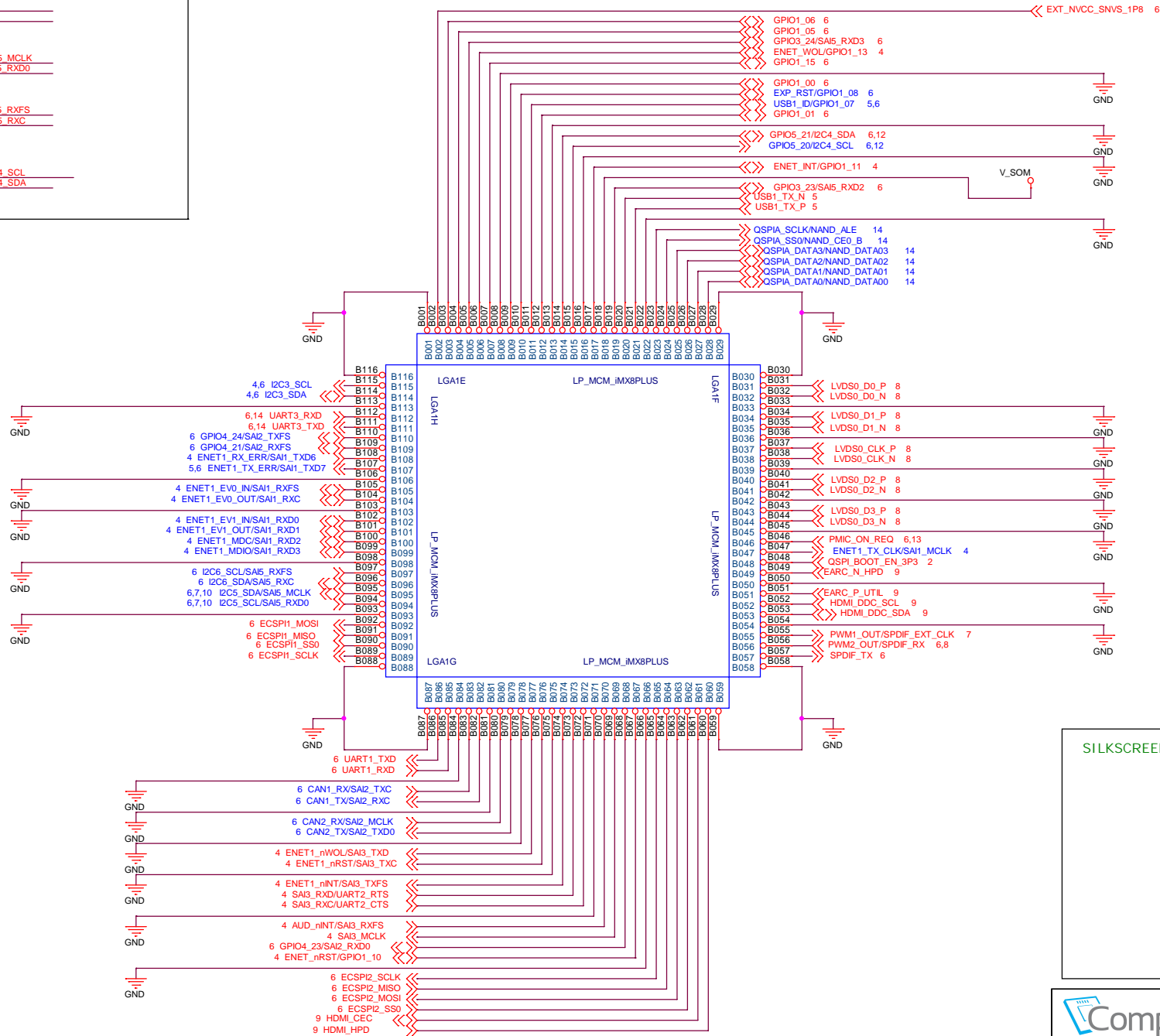
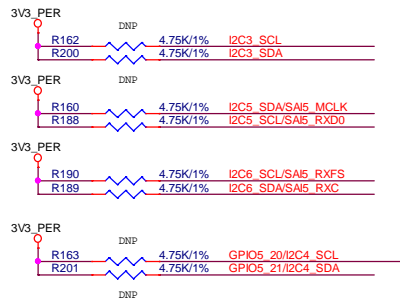
BOARD REVISION: 1.0

D

CompLab
SBC-MCM8PLUS, Rev. 1.0
188C04400

PCB, SBC-MCM8PLUS, Rev. 1.0
188C04400

I2C4/5/6 PU provision



SILKSCREEN ON BOTTOM SIDE:

B001 ->

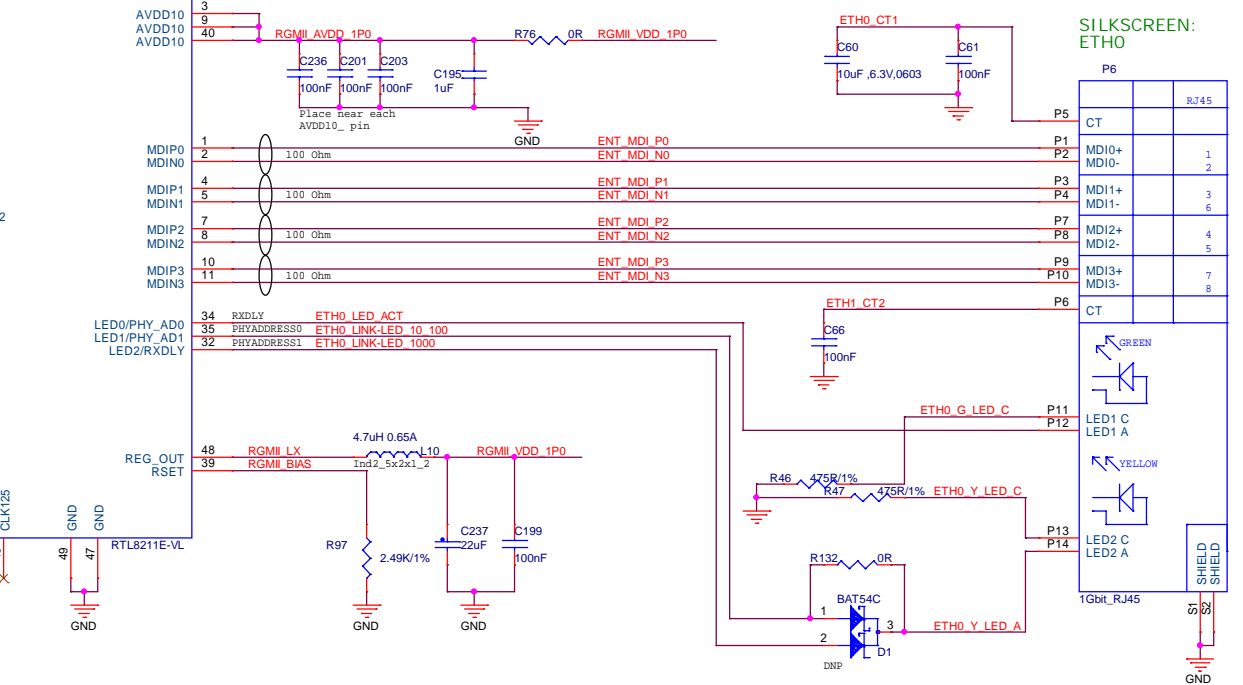
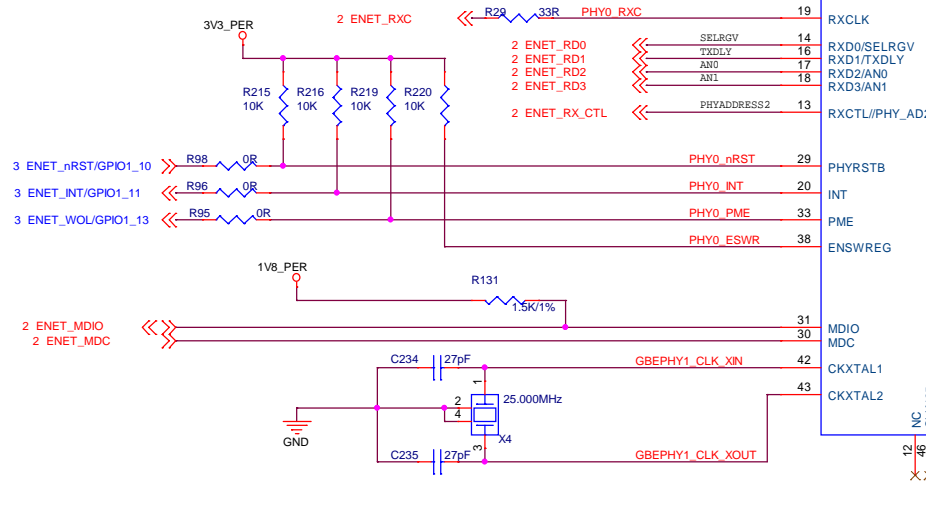
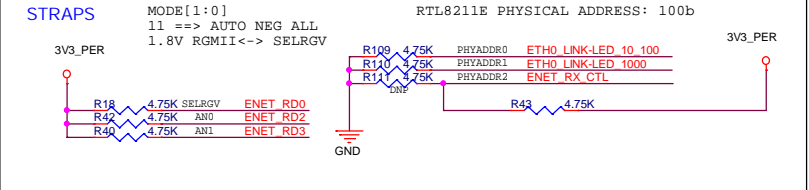
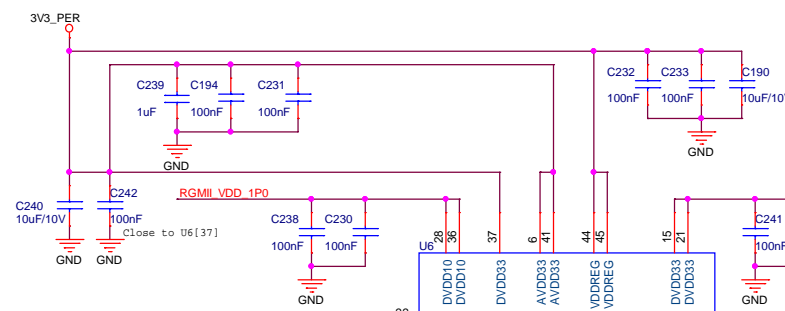
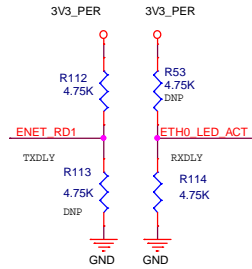
B030 ->

-> B088

<- B059

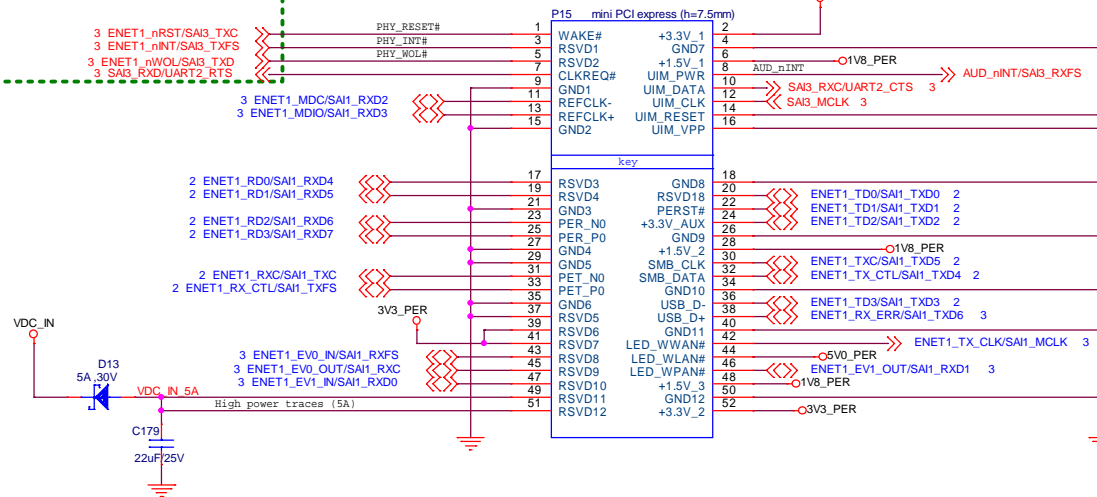
Enable internal delays

```
TX delay is ENABLE
RX delay is disable
```

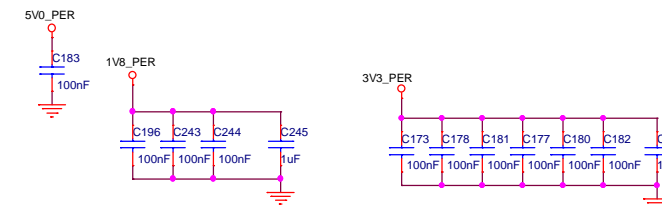


- I2S for Audio CODEC or PHY_RST#/_INT#/_WOL#

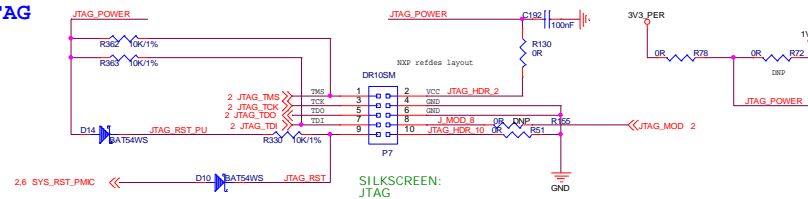
SILKSCREEN: RGMII / I2S Extender



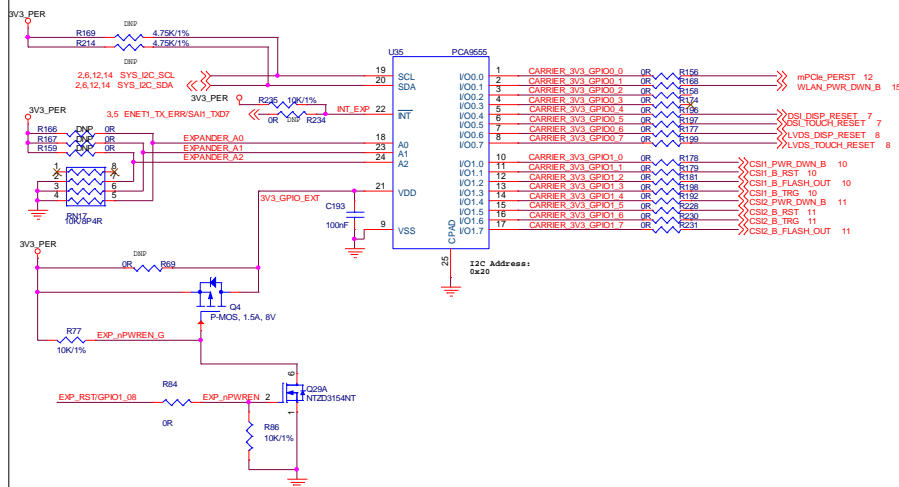
ENET1, RGMII frontend



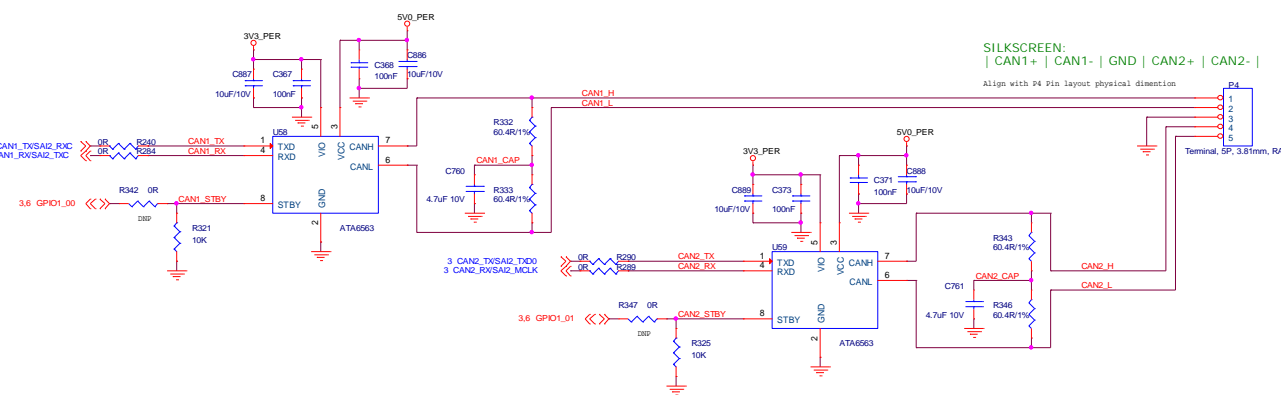
JTAG



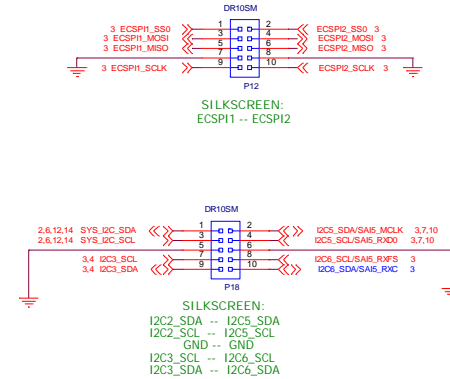
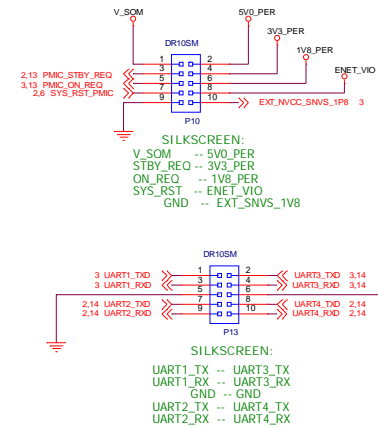
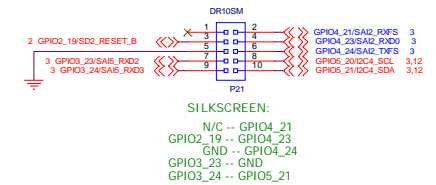
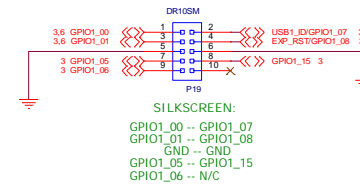
I2C to GPIO expander



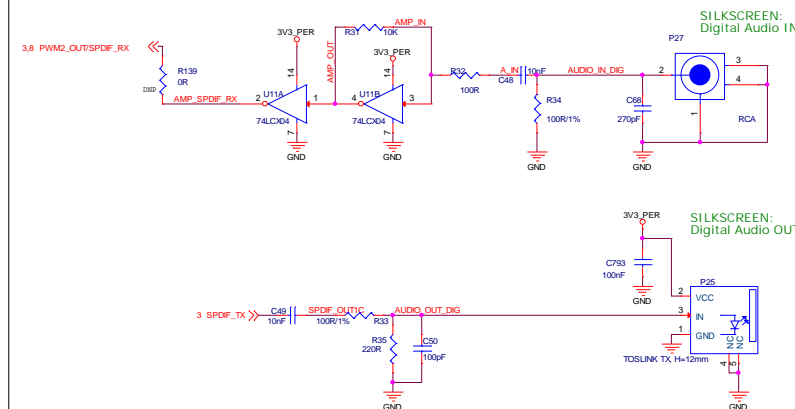
CAN Buses



FlexIO connectors



SPDIF



3V3_PER

E18

CSH_SELECTOR

R328 10K/1%

SILKSCREEN:
Enable 30-pin conn.

3V3_PER

1 6 13 20

VDD_1 VDD_2 VDD_3

C159 100nF C105 100nF C170 100nF C160 100nF

U63

2 3 4 5 7 8 9 10 11 12 14 15 16 17

COM0+ COM0- COM1+ COM1- COM2+ COM2- COM3+ COM3- COM4+ COM4- COM5+ COM5- SEL1 SEL2

42 41 40 39 38 37 36 35 25 24 23 22 34 33 32 31 30 29 28 27 21 20 19 18

NC0+ NC0- NC1+ NC1- NC2+ NC2- NC3+ NC3- NC4+ NC4- NC5+ NC5- NO0+ NO0- NO1+ NO1- NO2+ NO2- NO3+ NO3- NO4+ NO4- NO5+ NO5-

CSH P49 DP3 CSH P49 DN3 CSH P49 CKP CSH P49 CKN CSH P49 DP0 CSH P49 DN0 CSH P49 DP1 CSH P49 DN1 CSH P49 DP2 CSH P49 DN2 CSH P48 DP3 CSH P48 DN3 CSH P48 CKP CSH P48 CKN CSH P48 DP0 CSH P48 DN0 CSH P48 DP1 CSH P48 DN1 CSH P48 DP2 CSH P48 DN2

MAX14998ET0+T

Tpad

1V8_PER

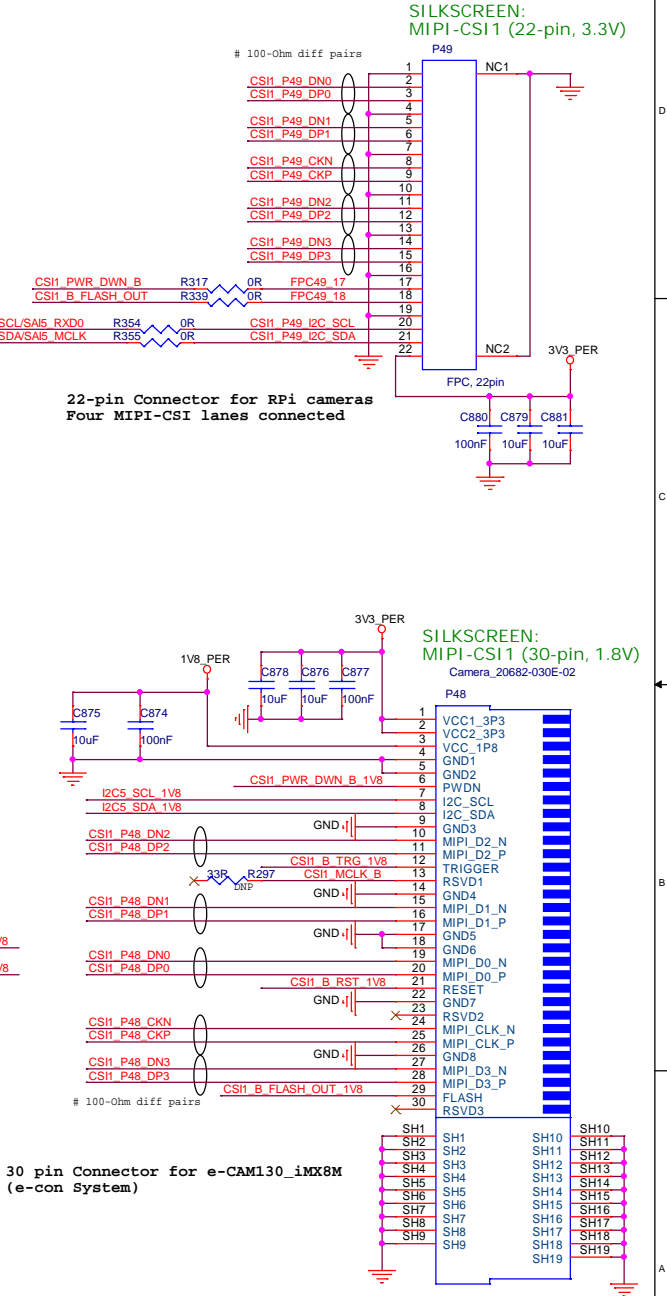
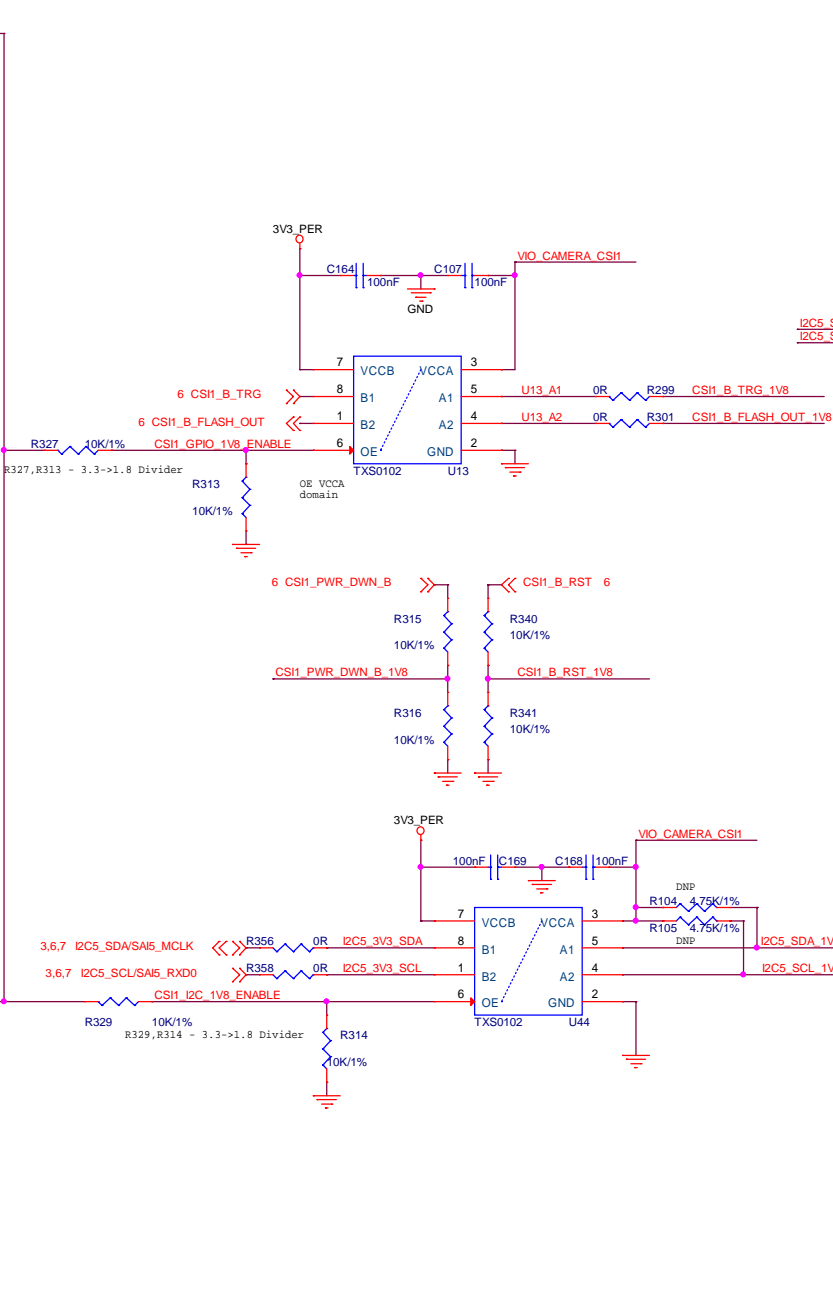
3V3_PER

R302 0R R303 0R

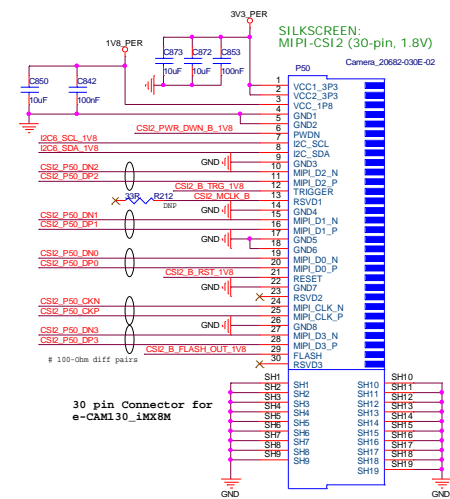
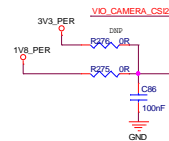
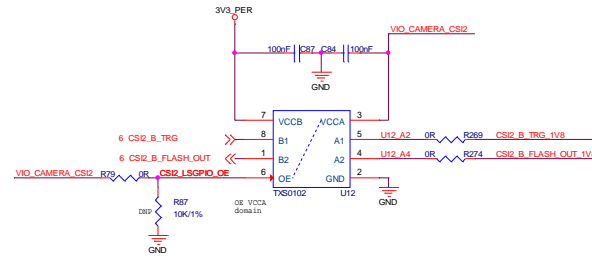
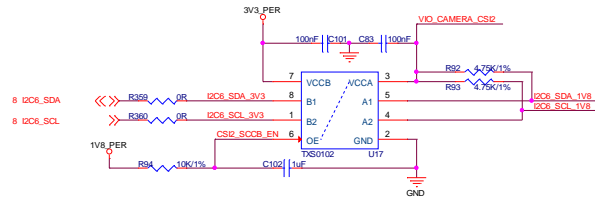
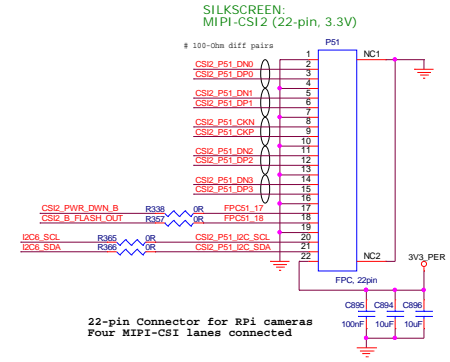
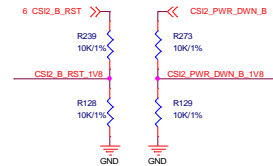
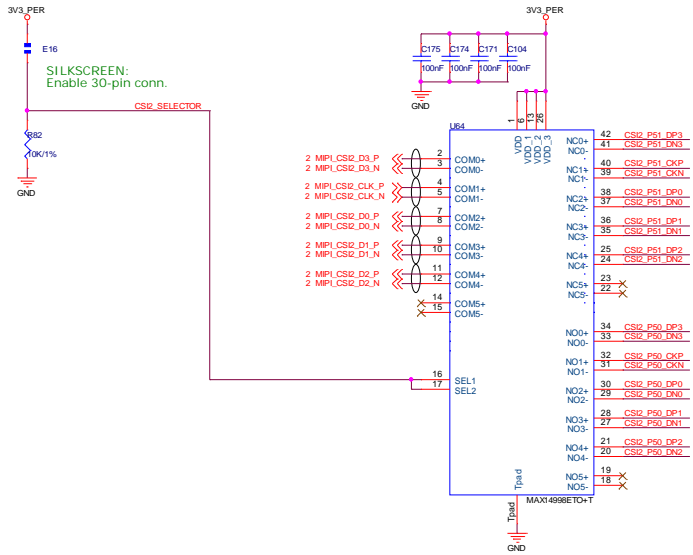
C106 100nF

VIO_CAMERA_CSH1

DNP

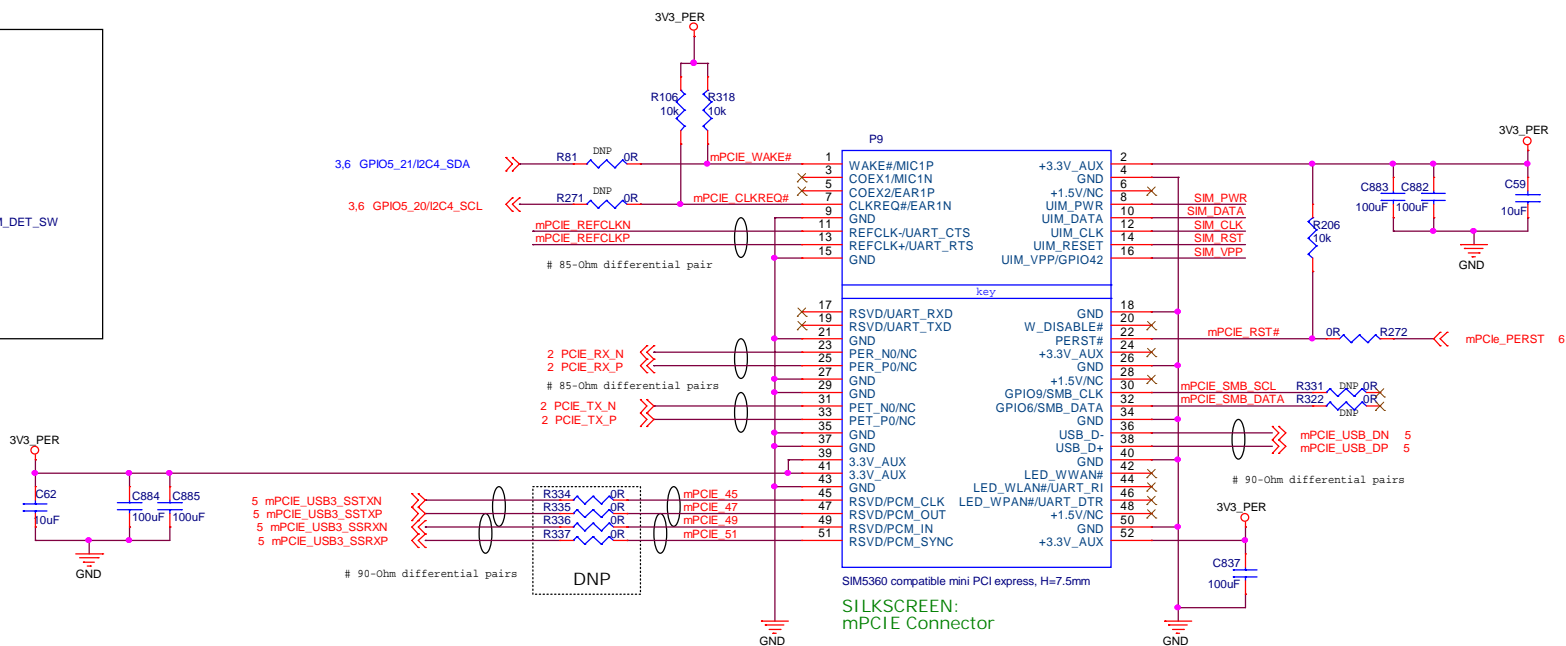
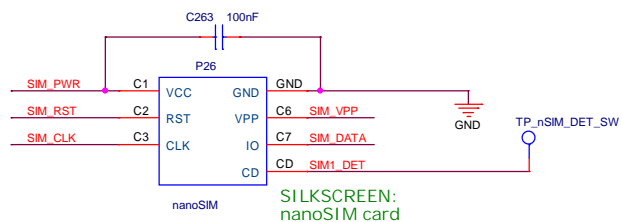


MIPI-CSI2 Connectors

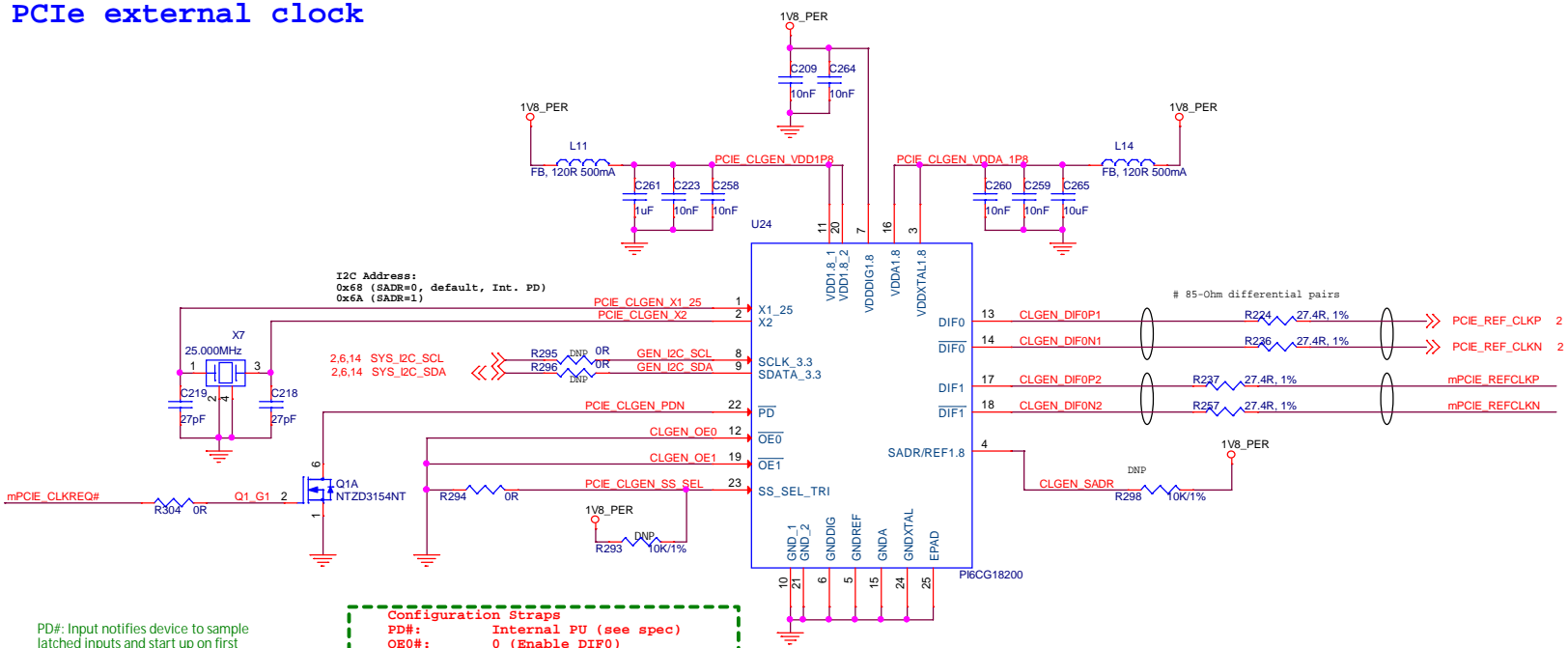


mPCIe Connector

nanoSIM



PCIe external clock



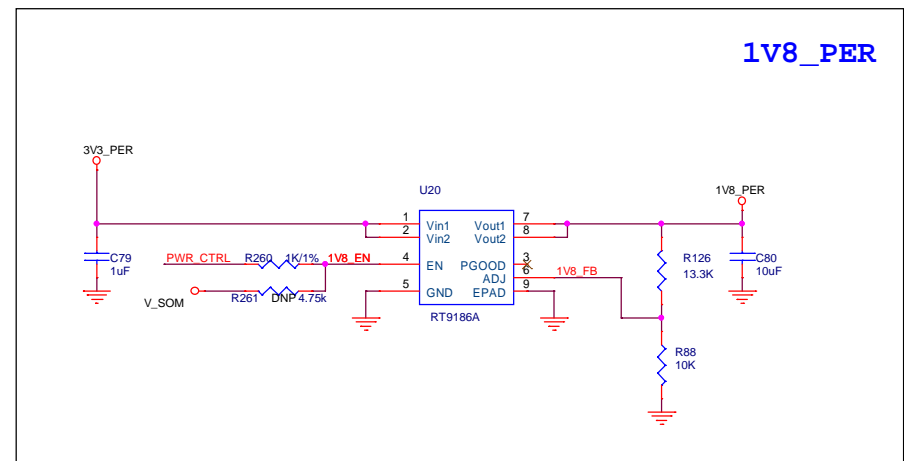
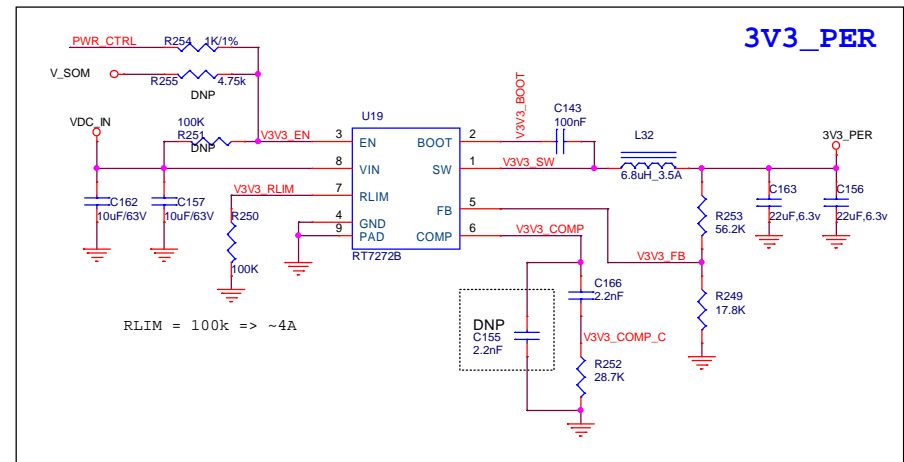
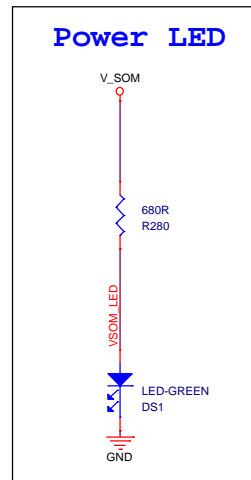
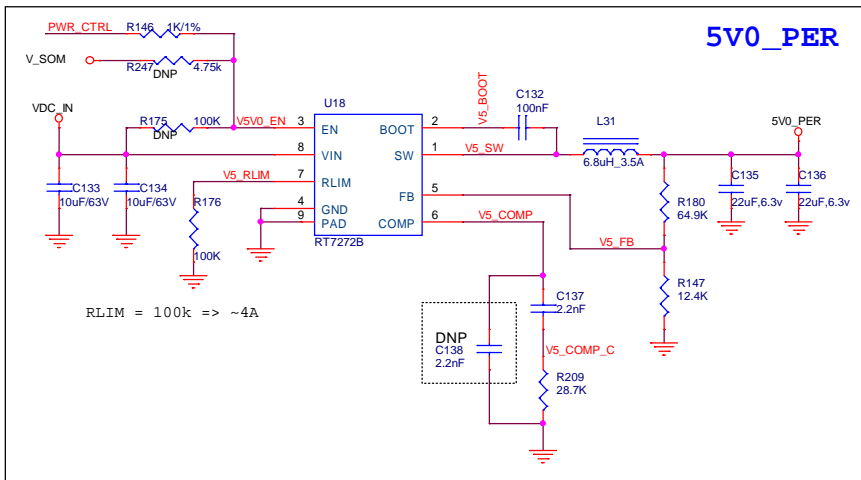
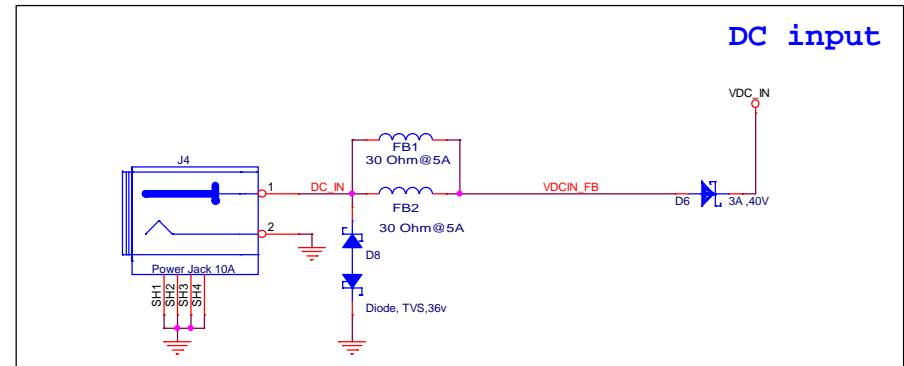
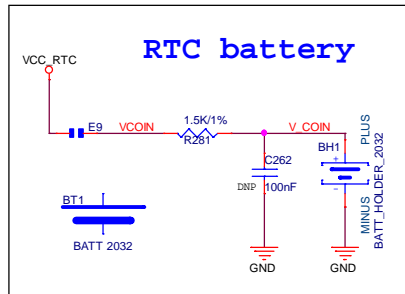
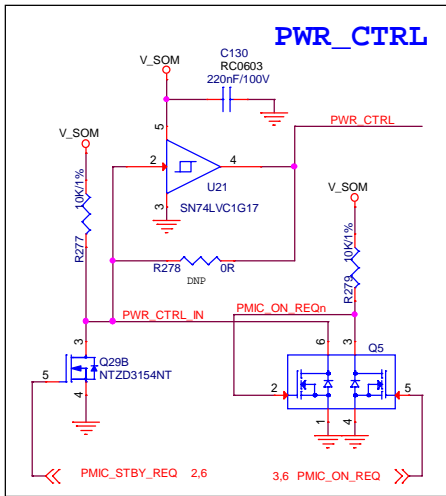
PD#: Input notifies device to sample latched inputs and start up on first high assertion. Low enters Power Down Mode, subsequent high assertions exit Power Down Mode. This pin has internal pull-up resistor.

```

Configuration Straps
PD#:          Internal PU (see spec)
OE0#:         0 (Enable DIF0)
OE1#:         0 (Enable DIF1)
SS_SEL TRI:   0 (SpreadSpect off)

```

3



V_SOM (3.7V, 3A)

VDC_IN

R173 100K

VSOM_EN

U16

3 EN

8 VIN

2 BOOT

1 VSOM_SW

5 FB

6 VSOM_COMP

4 GND PAD

9 COMP

RT2722B

C144 10uF/63V

C150 10uF/63V

R164 100K

VSOM_RLIM

RLIM

C142 100nF

L26 6.8uH, 2.4A

V_SOM_L

C256 22uF, 6.3V

C257 22uF, 6.3V

E8

R1 0.002R, 1%

V_SOM

C151 22uF, 6.3V

C153 22uF, 6.3V

R154 64.9K

R161 17.8K

C152 2.2nF

VSOM_COMP_C

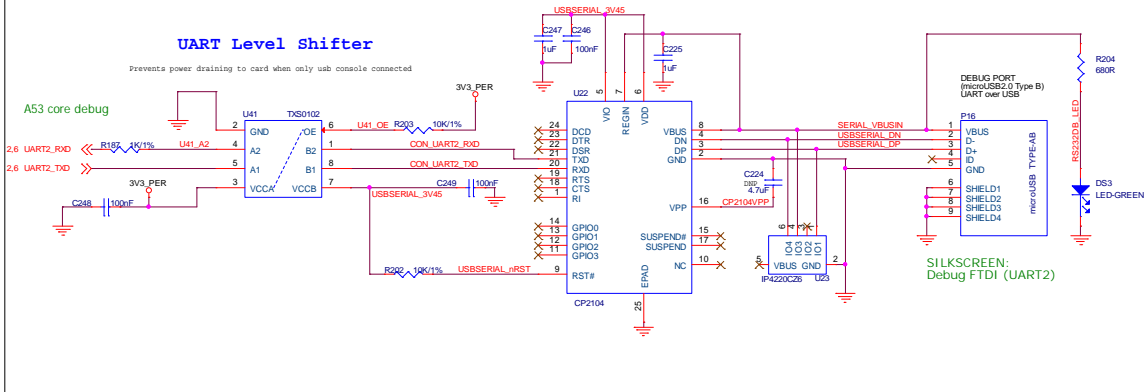
R165 28.7K

DNP C154 2.2nF

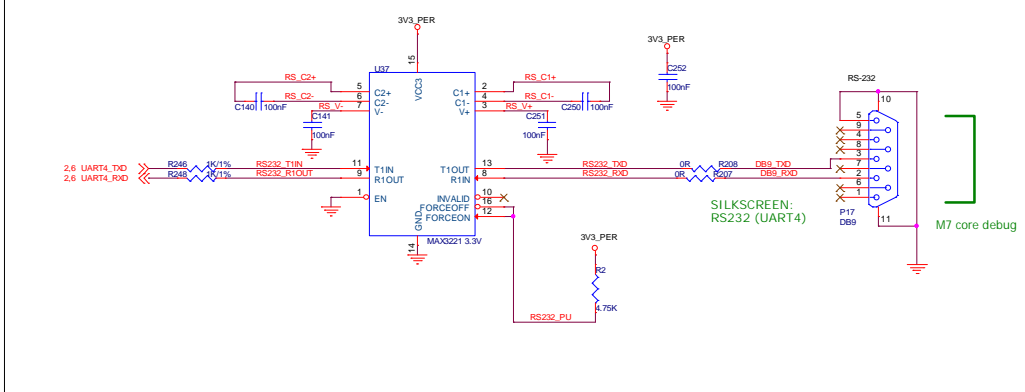
Current measure

RLIM = 100k => ~4A

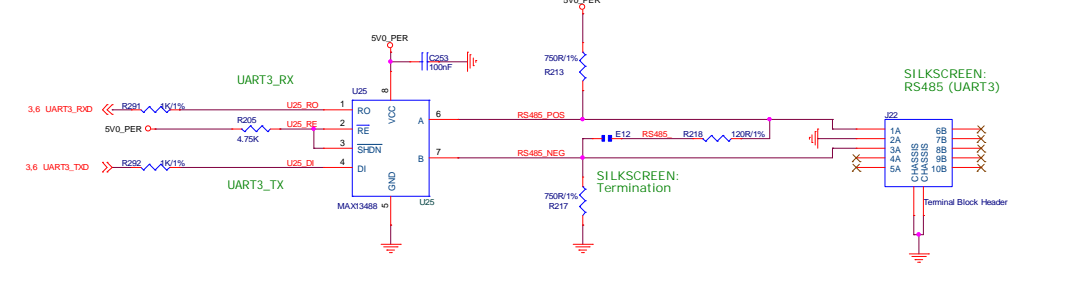
USB Serial Console (UART2)



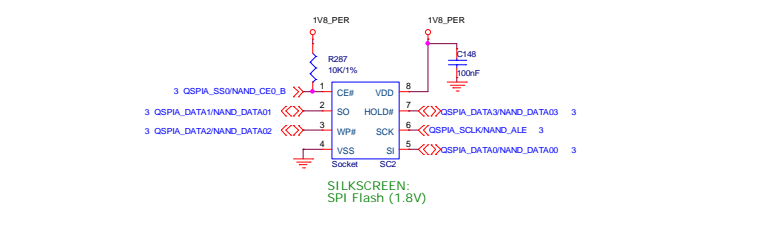
RS232 (UART4)



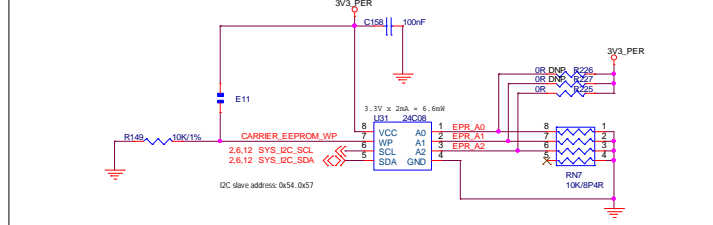
RS485 (UART3)



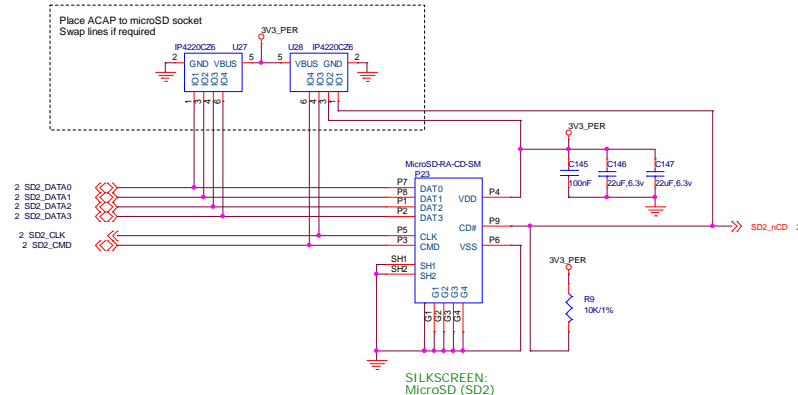
SPI NOR Flash Socket 1.8V



Carrier-board EEPROM



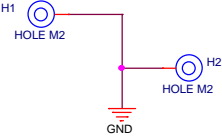
Micro-SD socket



For PCIe module



For LGA1 heatsink stand-offs



For P15 (RGMII/I2S Extender)

