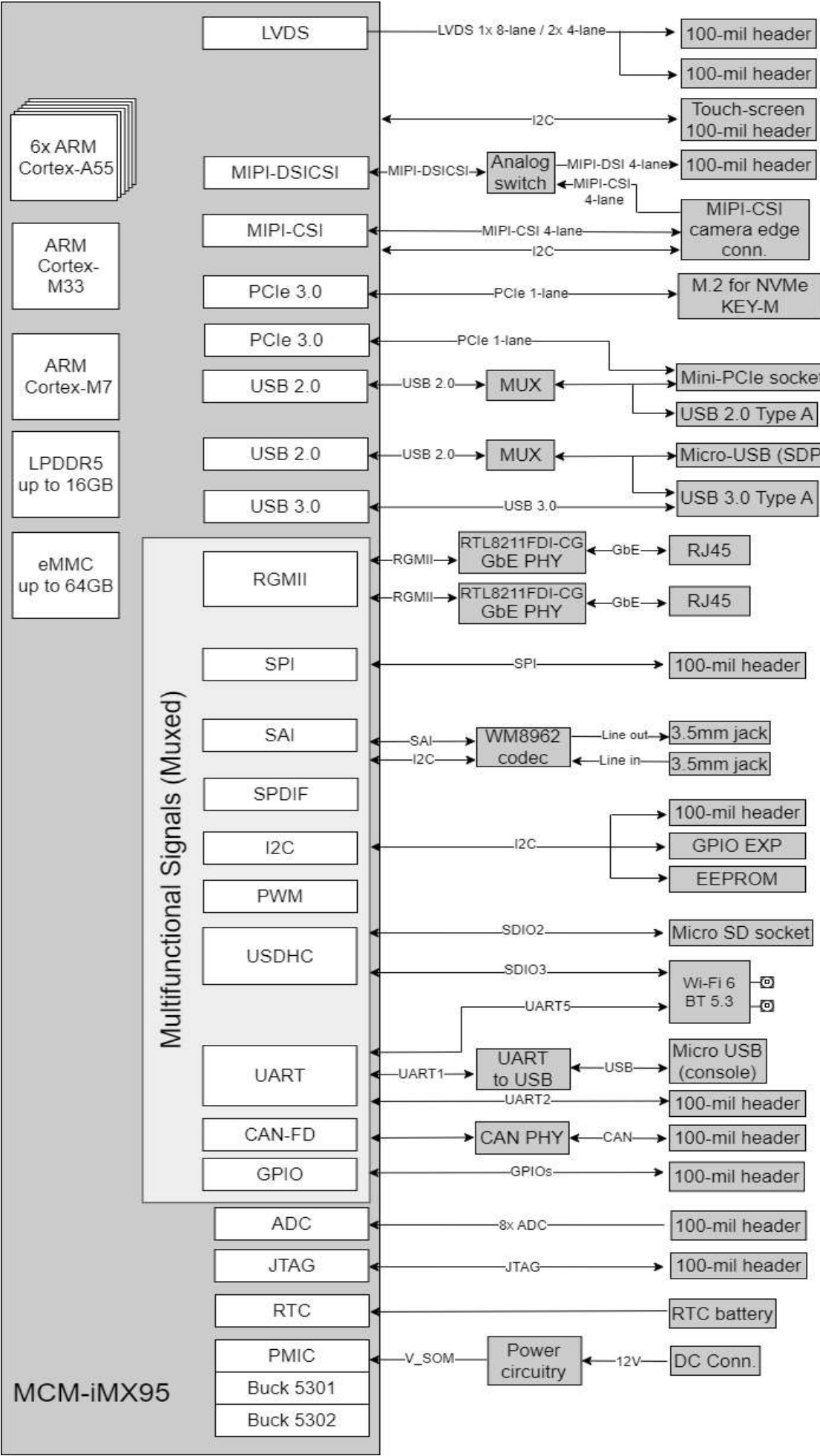


SBC-MCM95

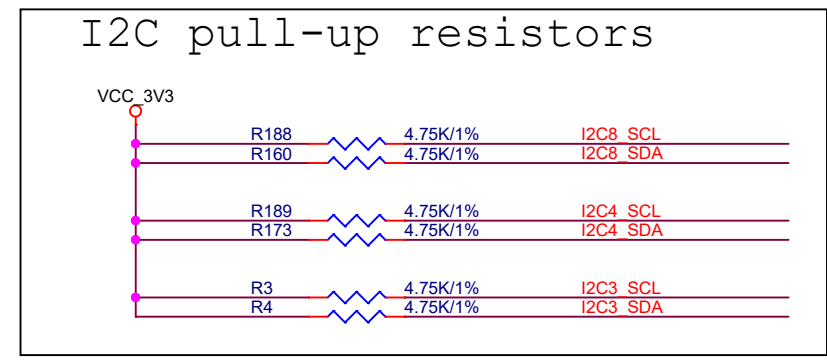
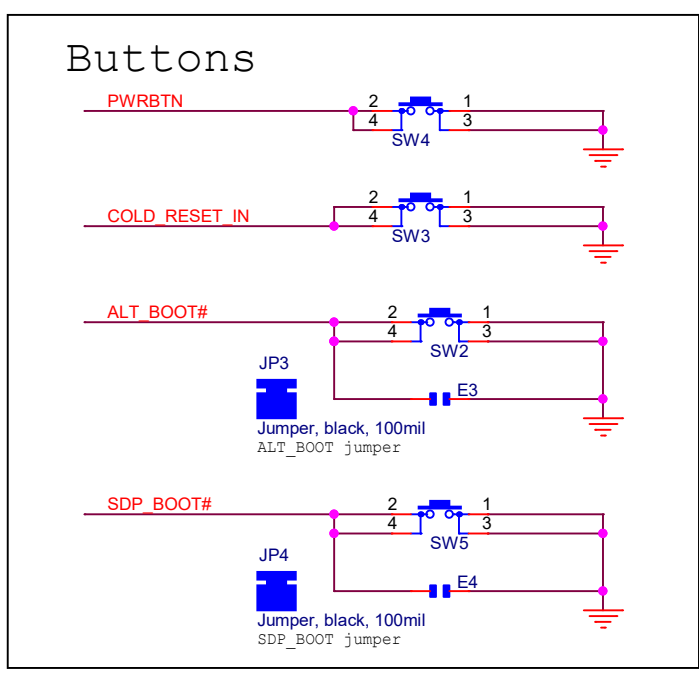
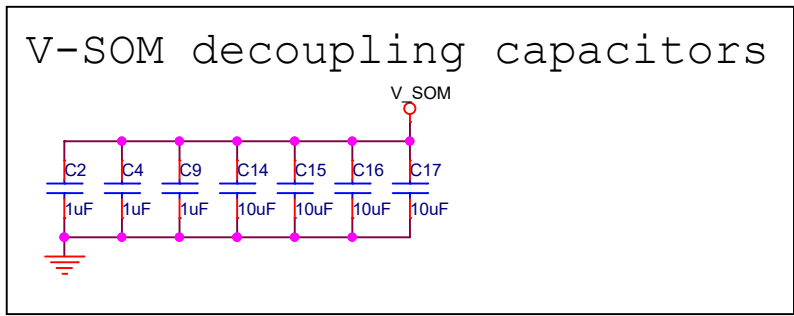
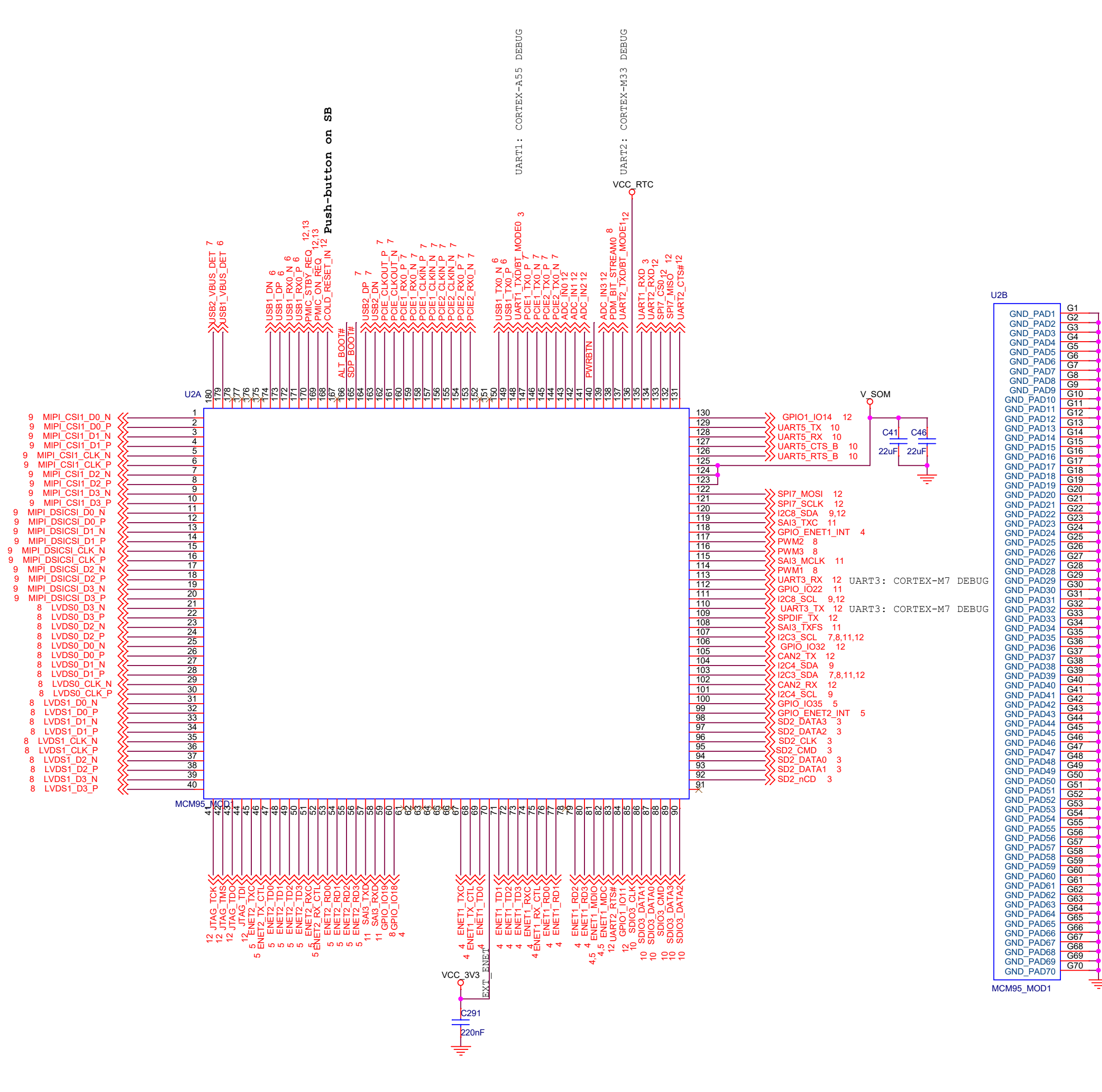
Page	Description
01	Index and Block Diagram
02	SOM connectors
03	Console, micro-SD
04	Carrier-board Ethernet
05	USB #1 and SDP
06	USB #2 and PCIe
07	LVDS and MPI-DSI
08	MIPI-CSI
09	Audio codec
10	Octal SPI flash
11	I/O headers, JTAG, GPIO exp.
12	LEDs
13	Power
14	DC input and spacers

CH #	I2C devices
I2C3	EEPROM, PCA9500, codec, touch,
I2C4	Camera
I2C8	Camera, header



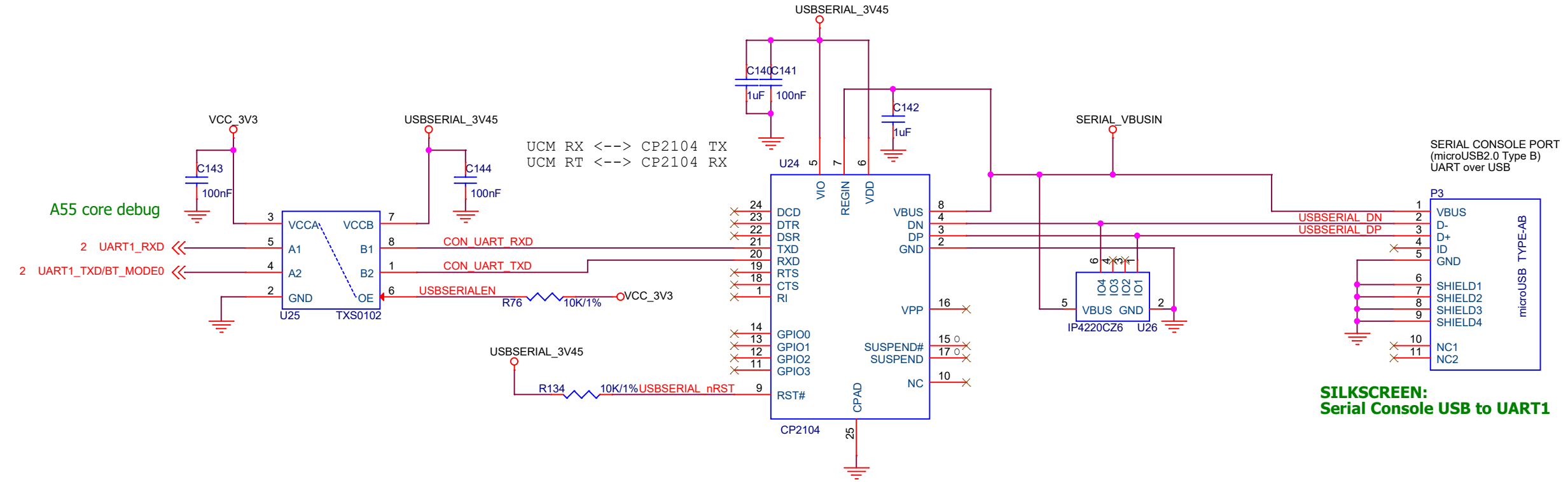
PCB1  
PCB, SBC-MCM95, Rev 1.0  
P/N = 188C04740

ZZ1  
PARSER\_VERSION\_1.0

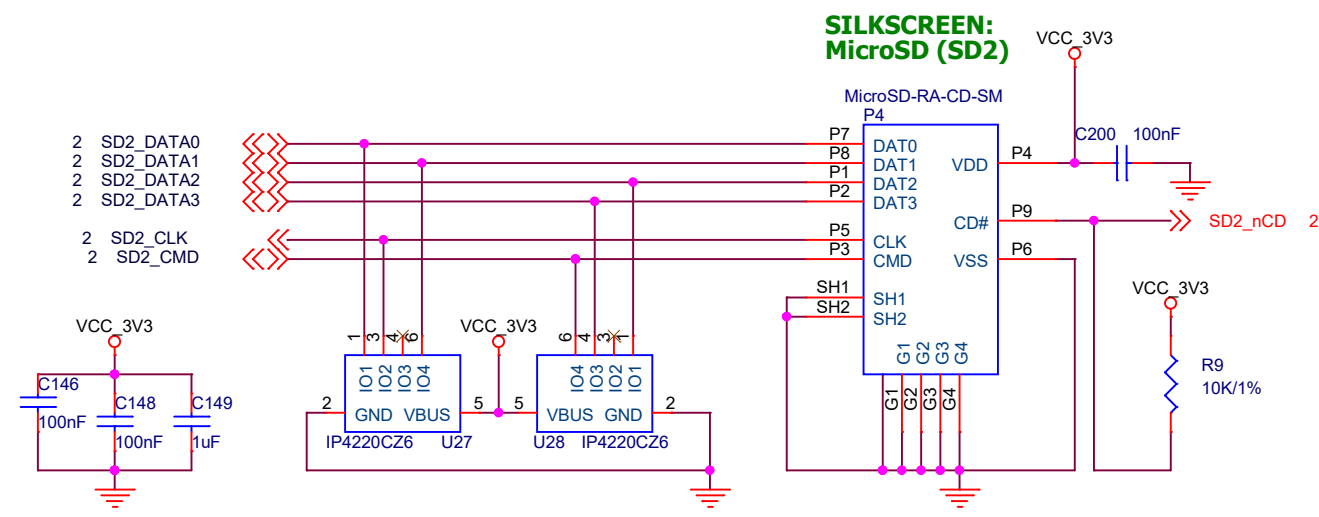


Title				
MCM interface				
Size	Document Number			Rev
A3	<Doc>			1.0
Date:	Monday, May 12, 2025	Sheet	2 of 14	

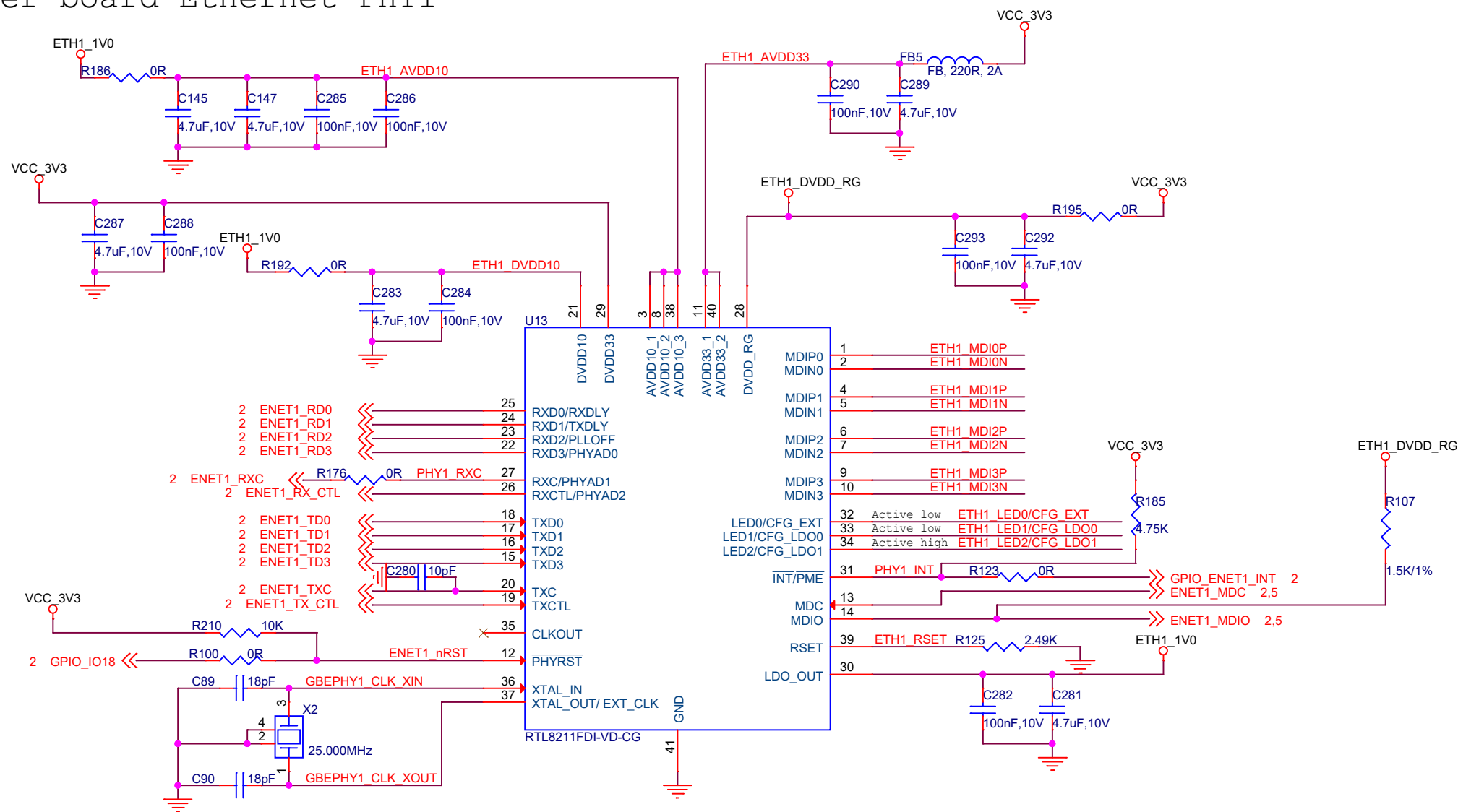
Serial debug console



Micro-SD socket (bootable)

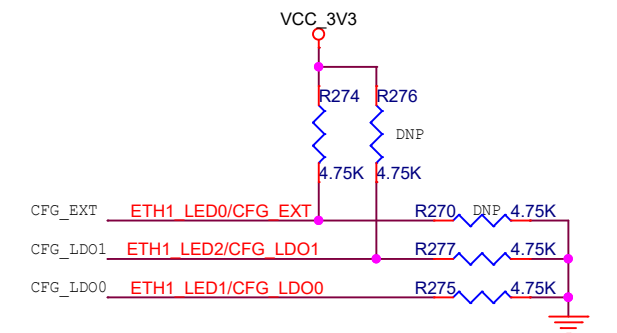


## Carrier-board Ethernet PHY1

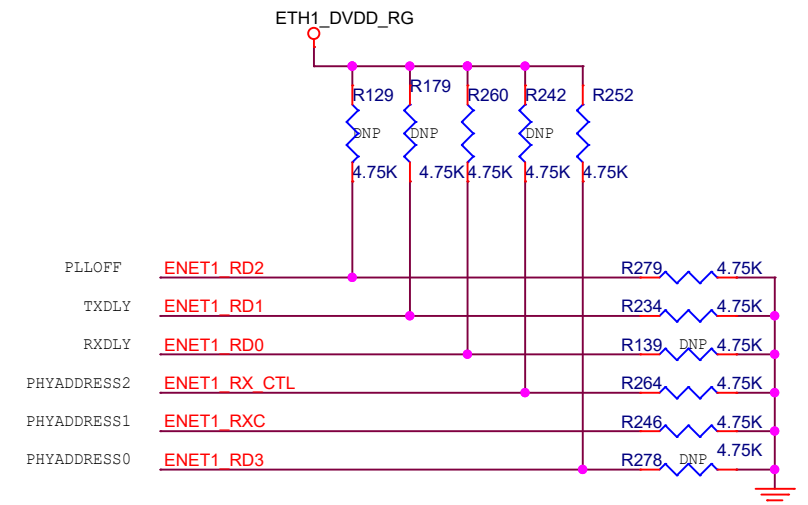
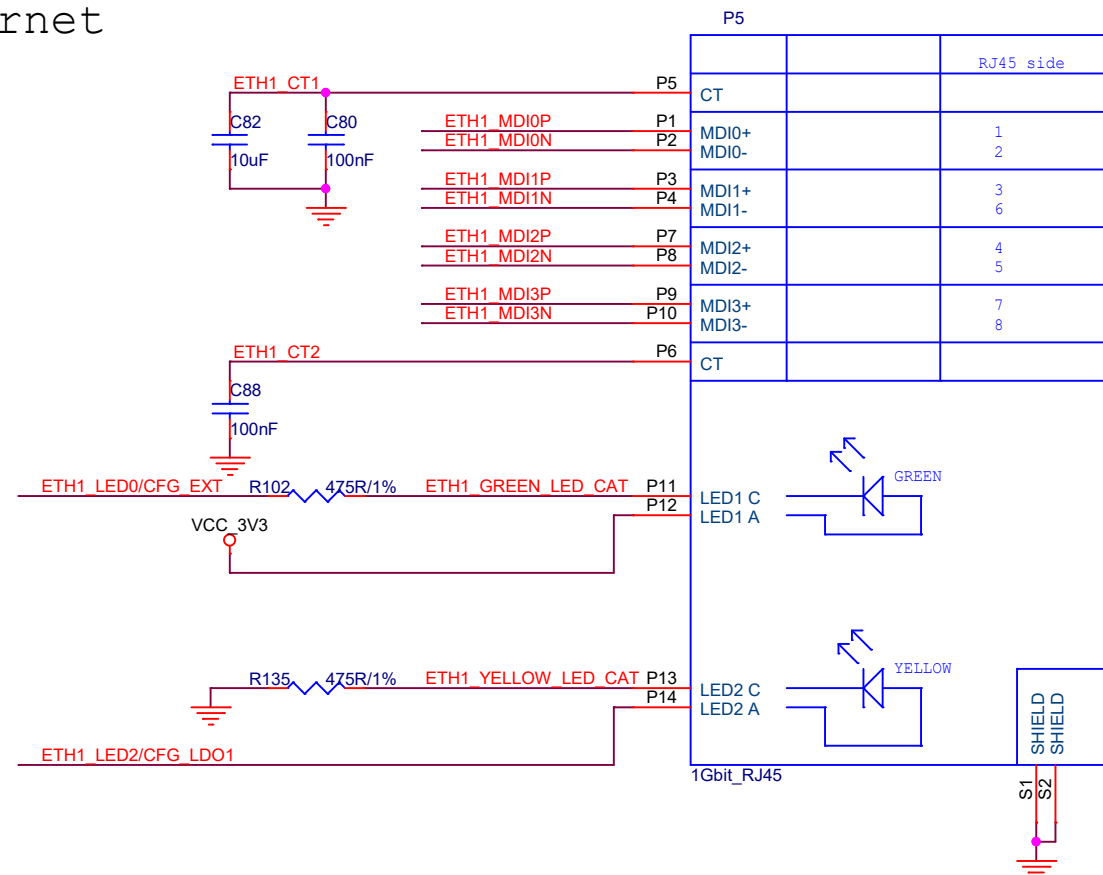


## Power-on strap pins CFG

RGMII Power Source	CFG_EXT	CFG_LDO[1:0]
<b>External 3.3V</b>	<b>1</b>	<b>00</b>
External 1.8V	1	10
Internal 1.8V	0	10



CONFIG PINS

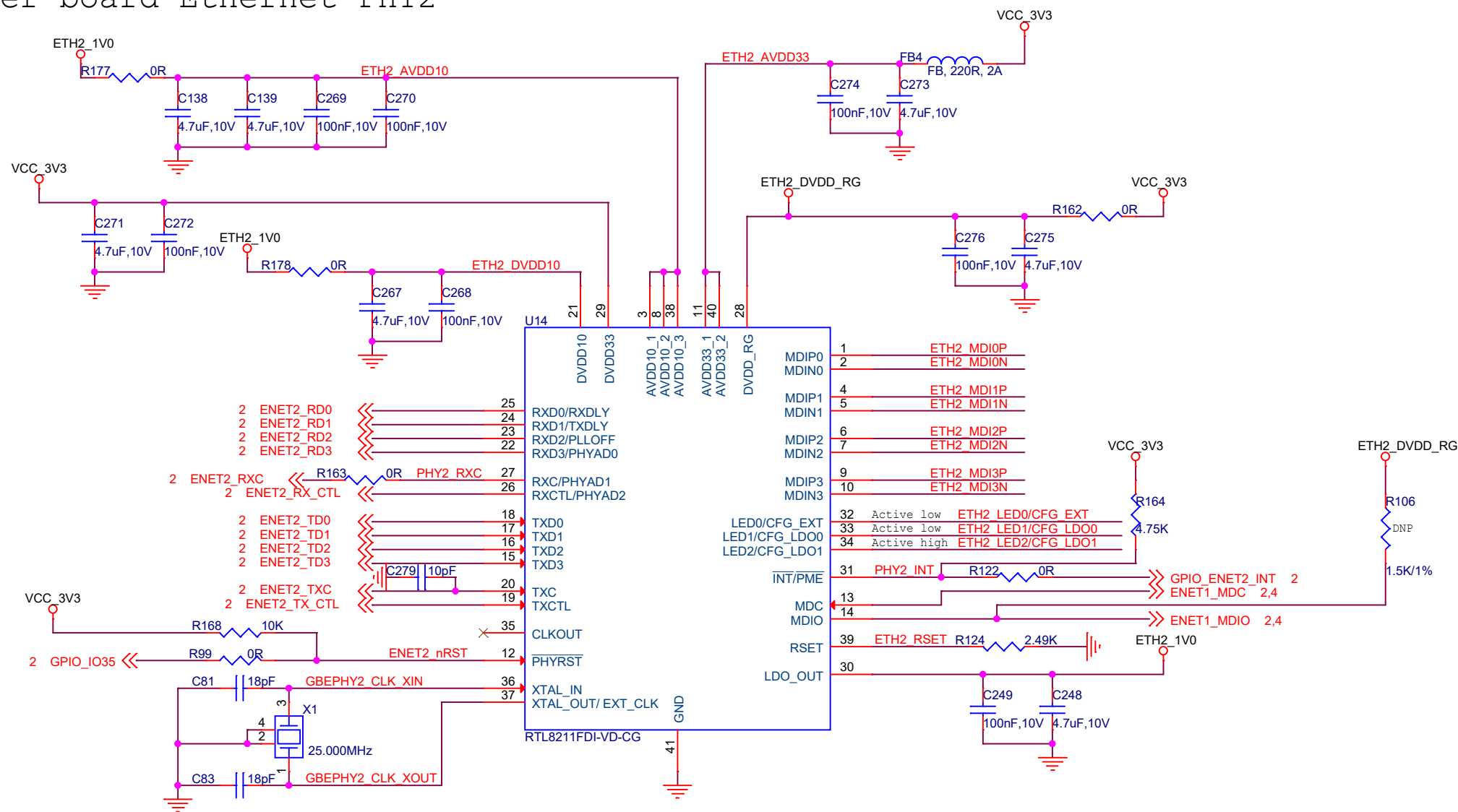
Carrier-board Ethernet  
connector

```
PHY ADDRESS = [0,0,1]
PHY support addresses from 00001 to 00111.
PHY address 0 is a broadcast from the MAC
```

```
Enable internal delays
TX delay is disabled
RX delay is enabled
```

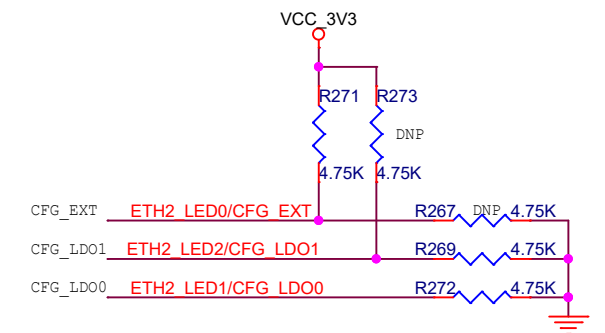
Title			
Carrier-board Ethernet1			
Size	Document Number		Rev
A3	<Doc>		1.0
Date:	Monday, May 12, 2025	Sheet	4 of 14

## Carrier-board Ethernet PHY2

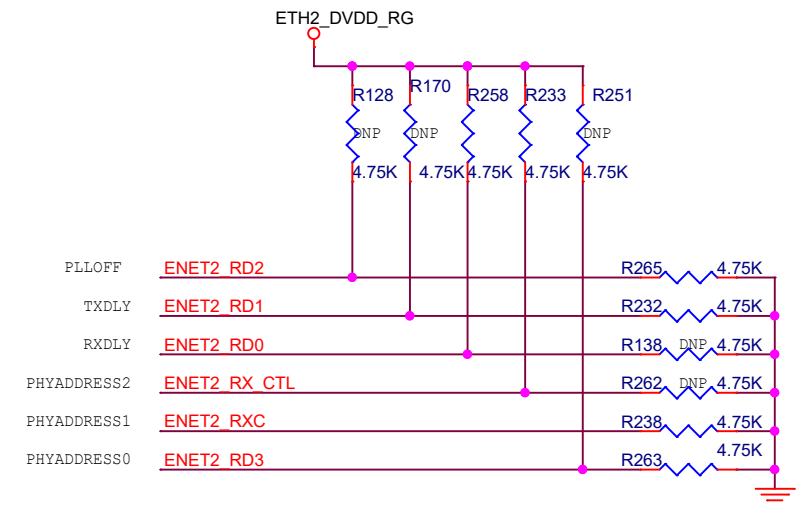
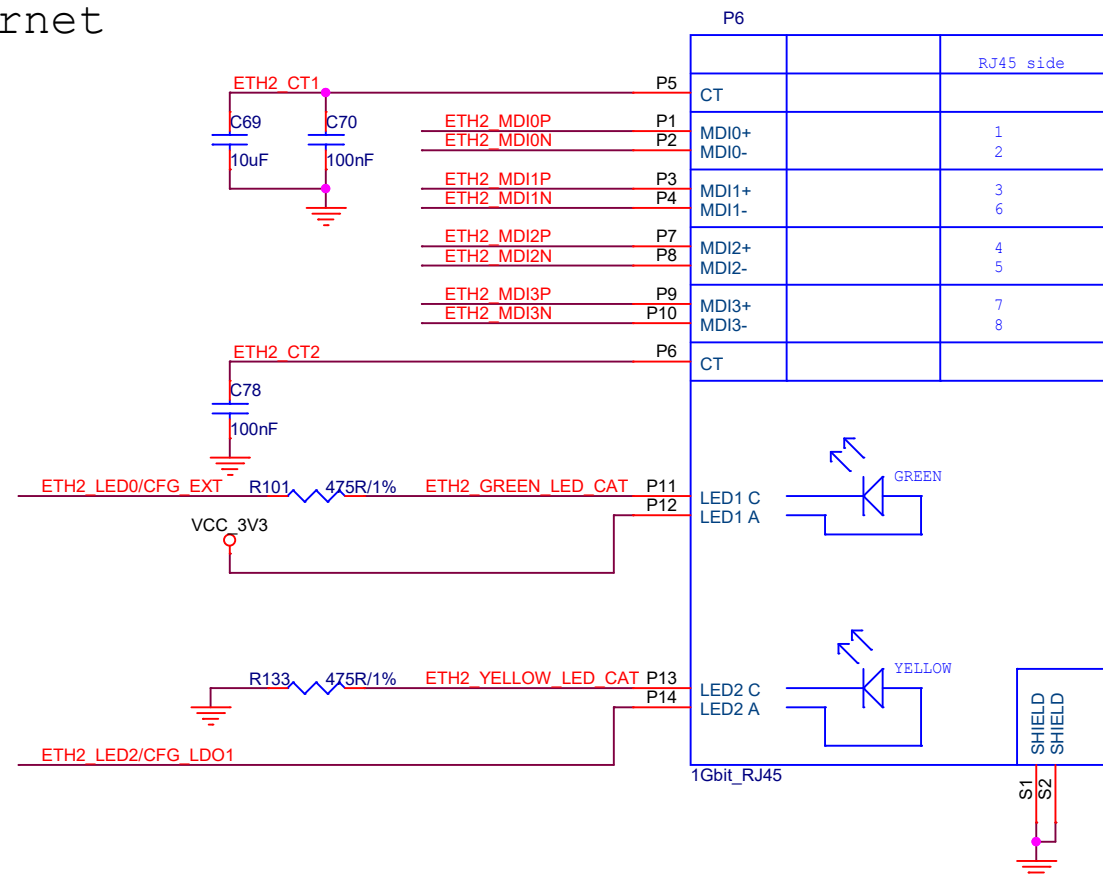


## Power-on strap pins CFG

RGMII Power Source	CFG_EXT	CFG_LDO[1:0]
<b>External 3.3V</b>	<b>1</b>	<b>00</b>
External 1.8V	1	10
Internal 1.8V	0	10



CONFIG PINS

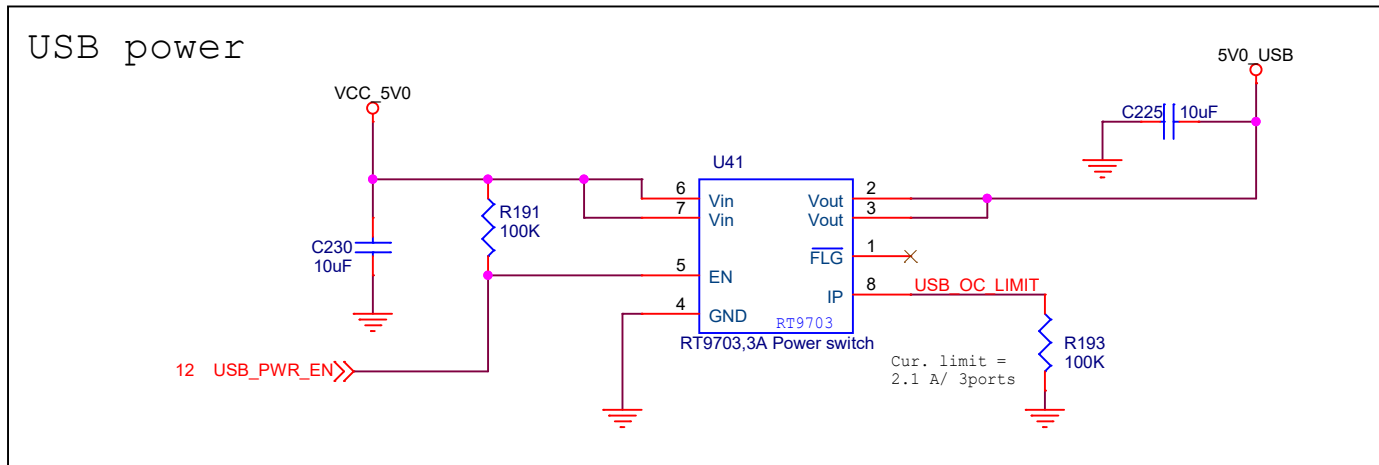
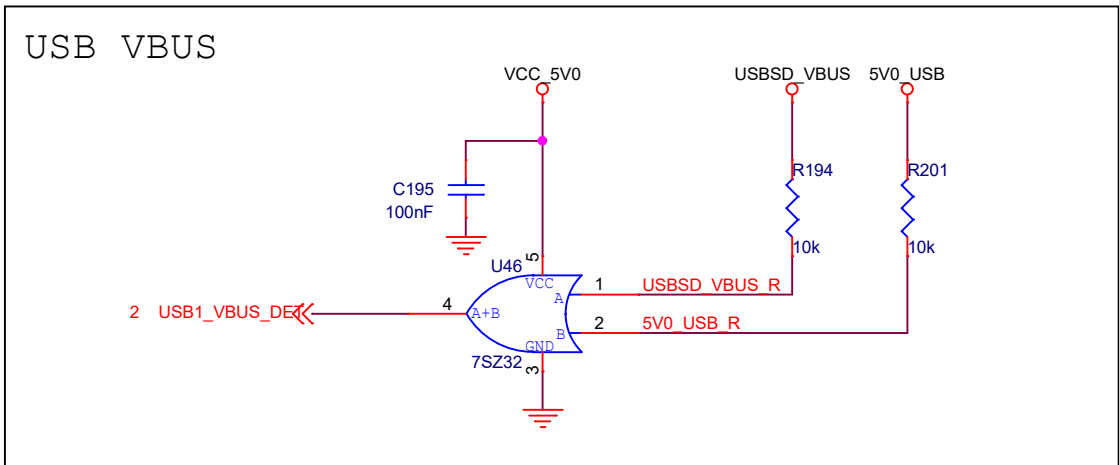
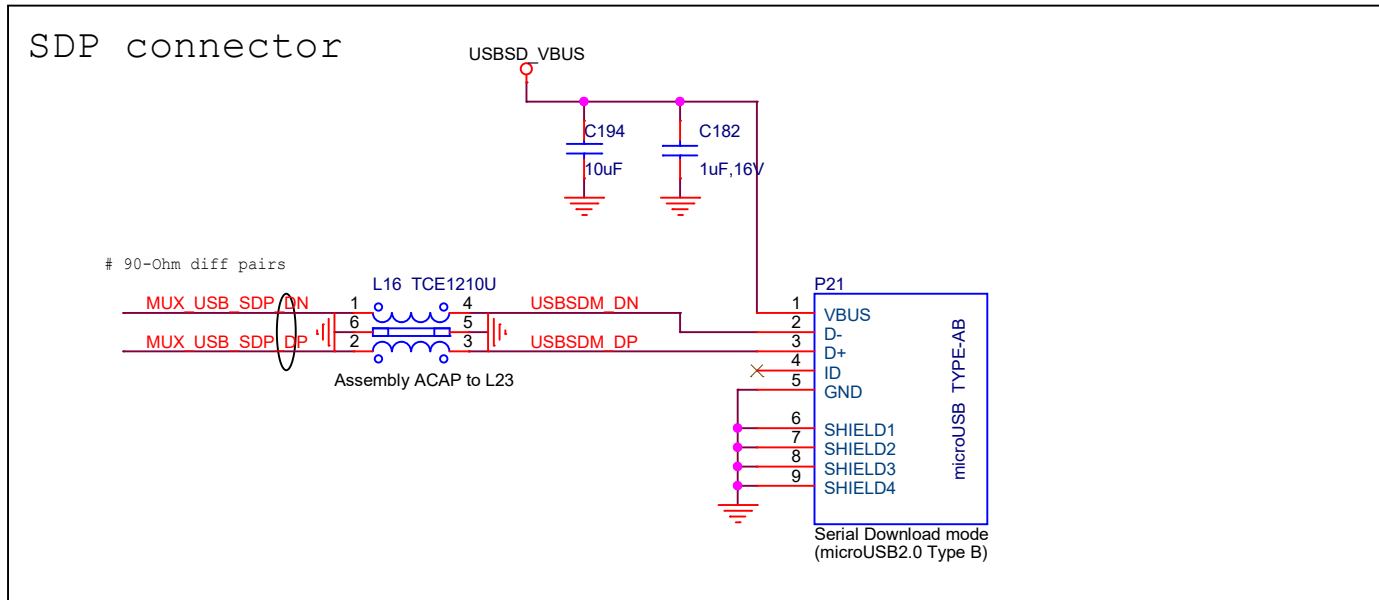
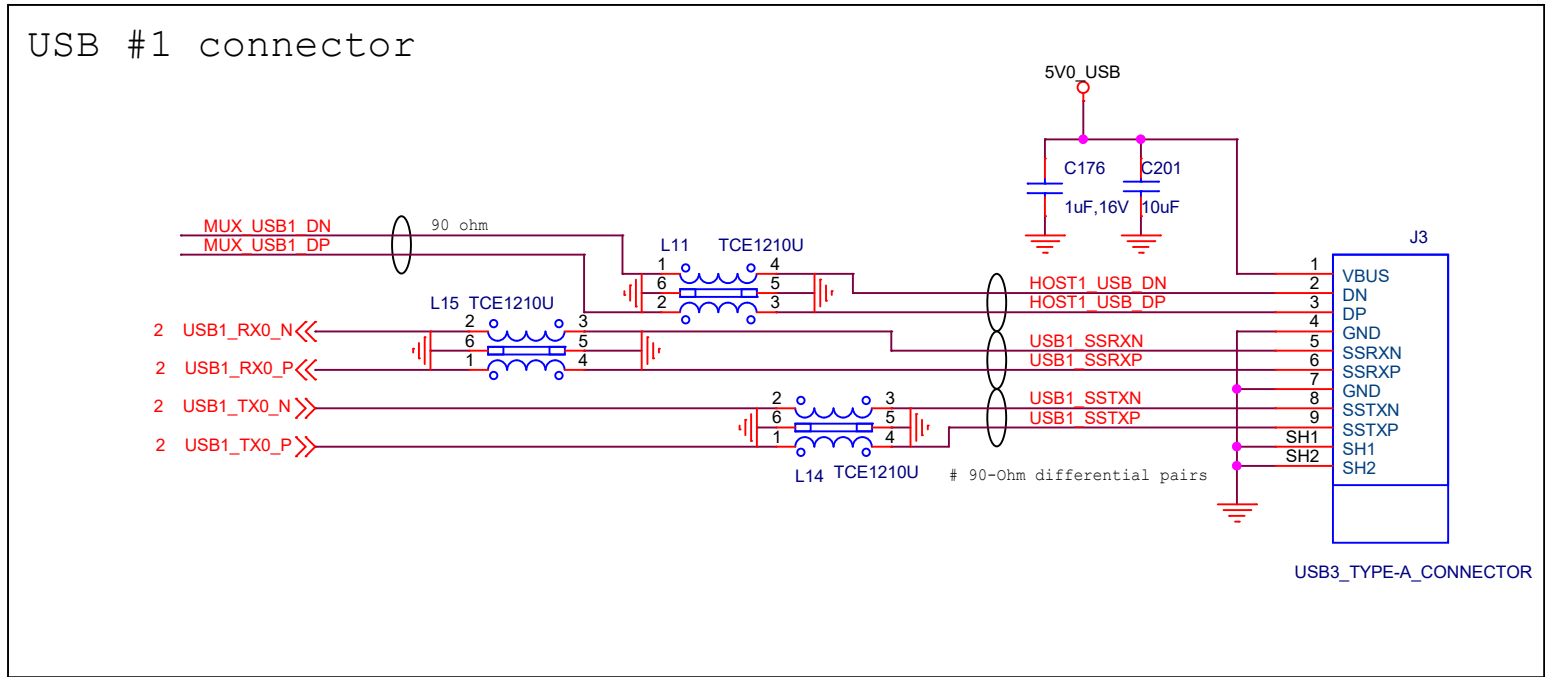
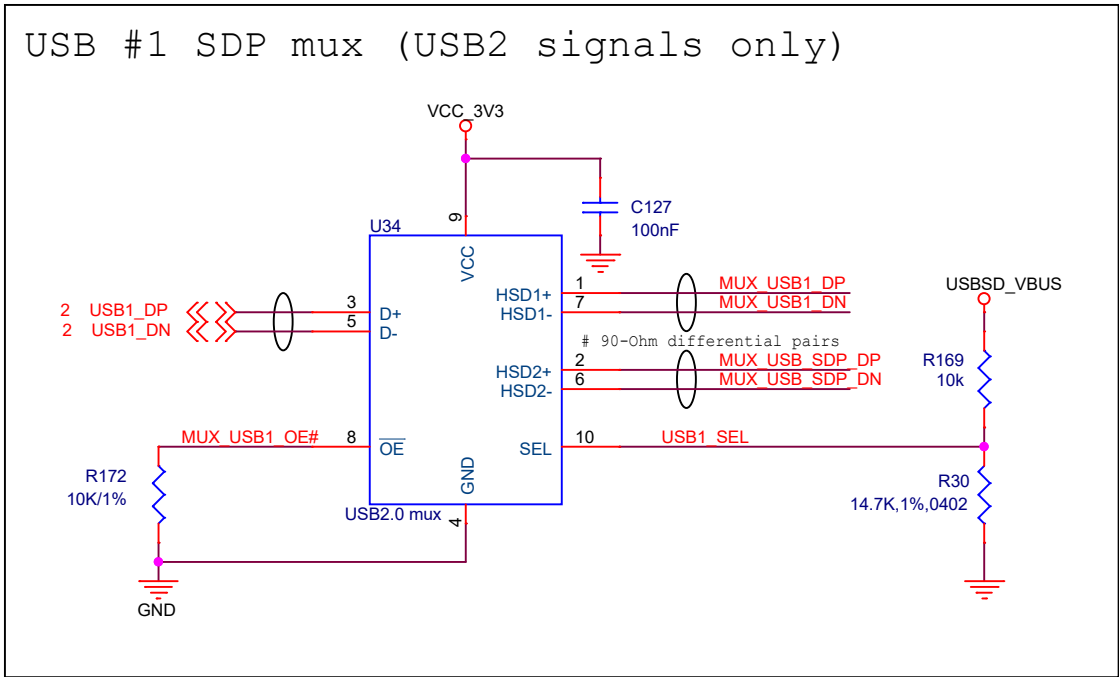
Carrier-board Ethernet  
connector

```
PHY ADDRESS = [1,0,0]
PHY support addresses from 00001 to 00111.
PHY address 0 is a broadcast from the MAC

PLL0FF: Pull-up to disable PLL @ ALDPS mo
```

```
Enable internal delays
TX delay is disabled
RX delay is enabled
```



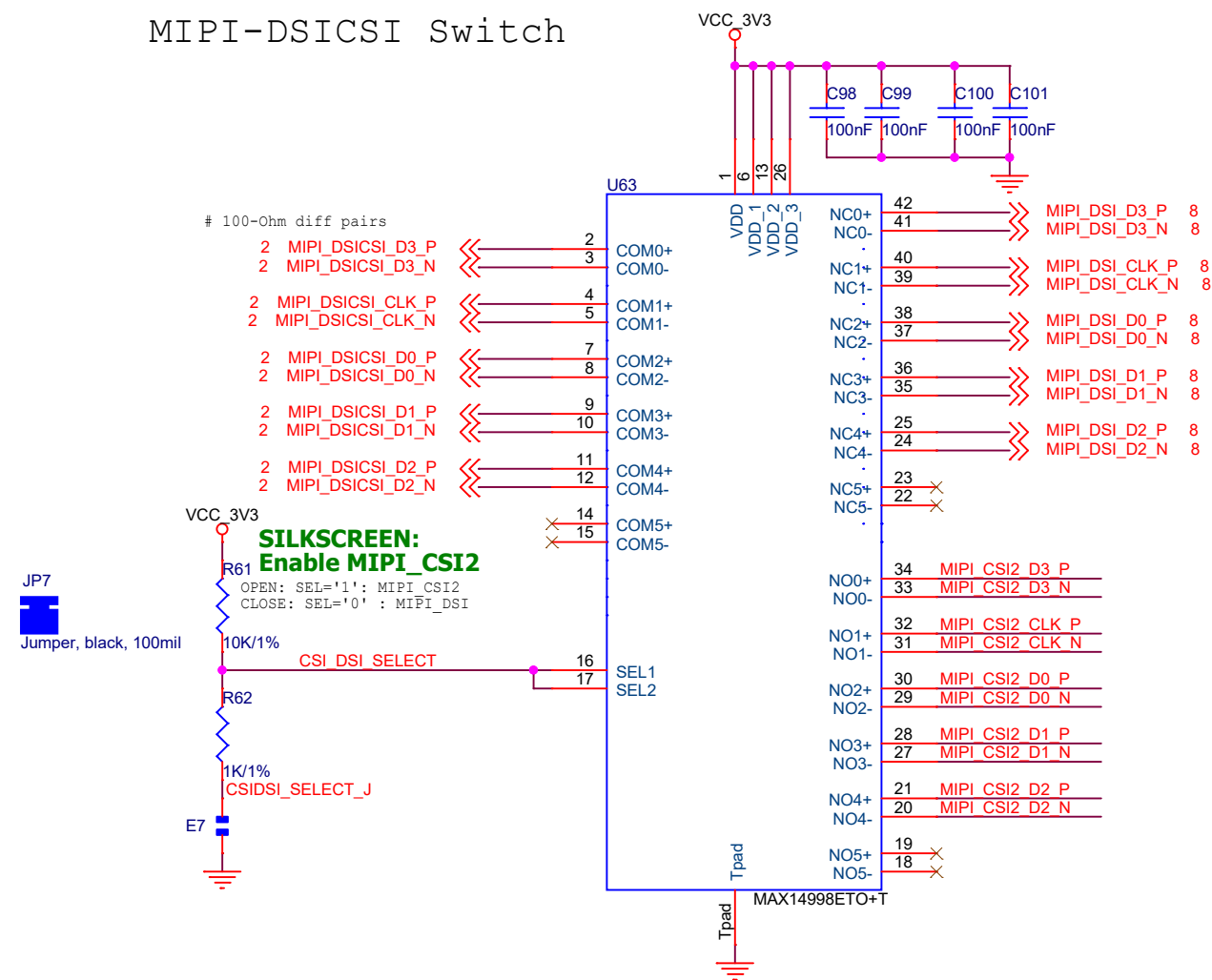




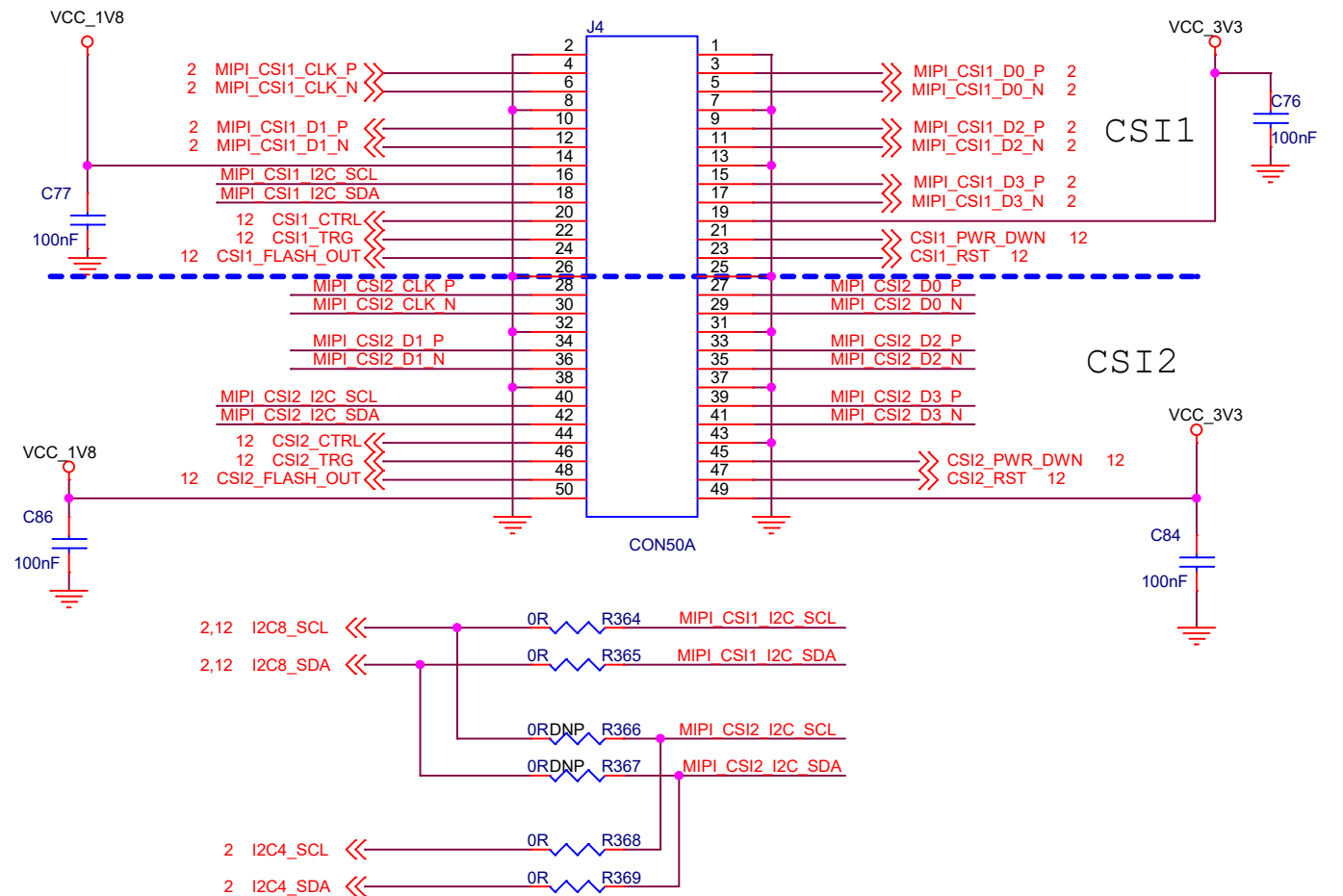




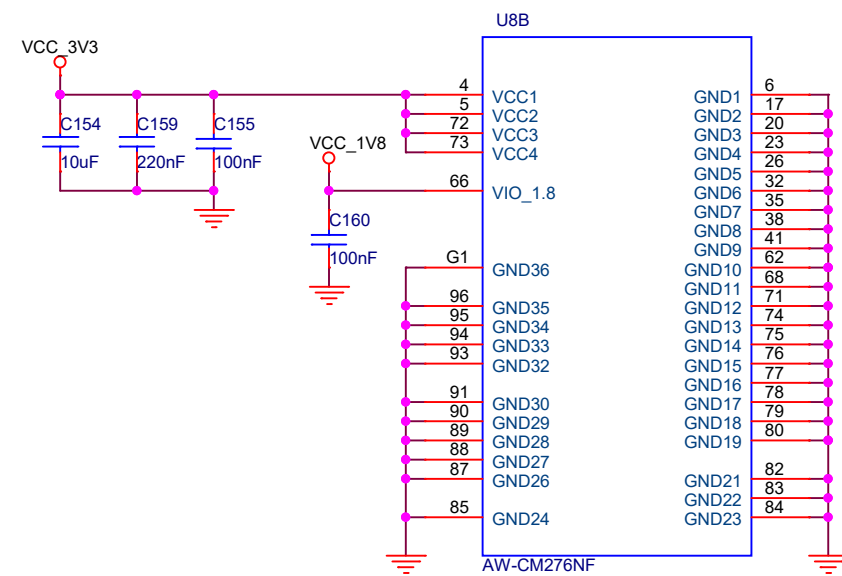
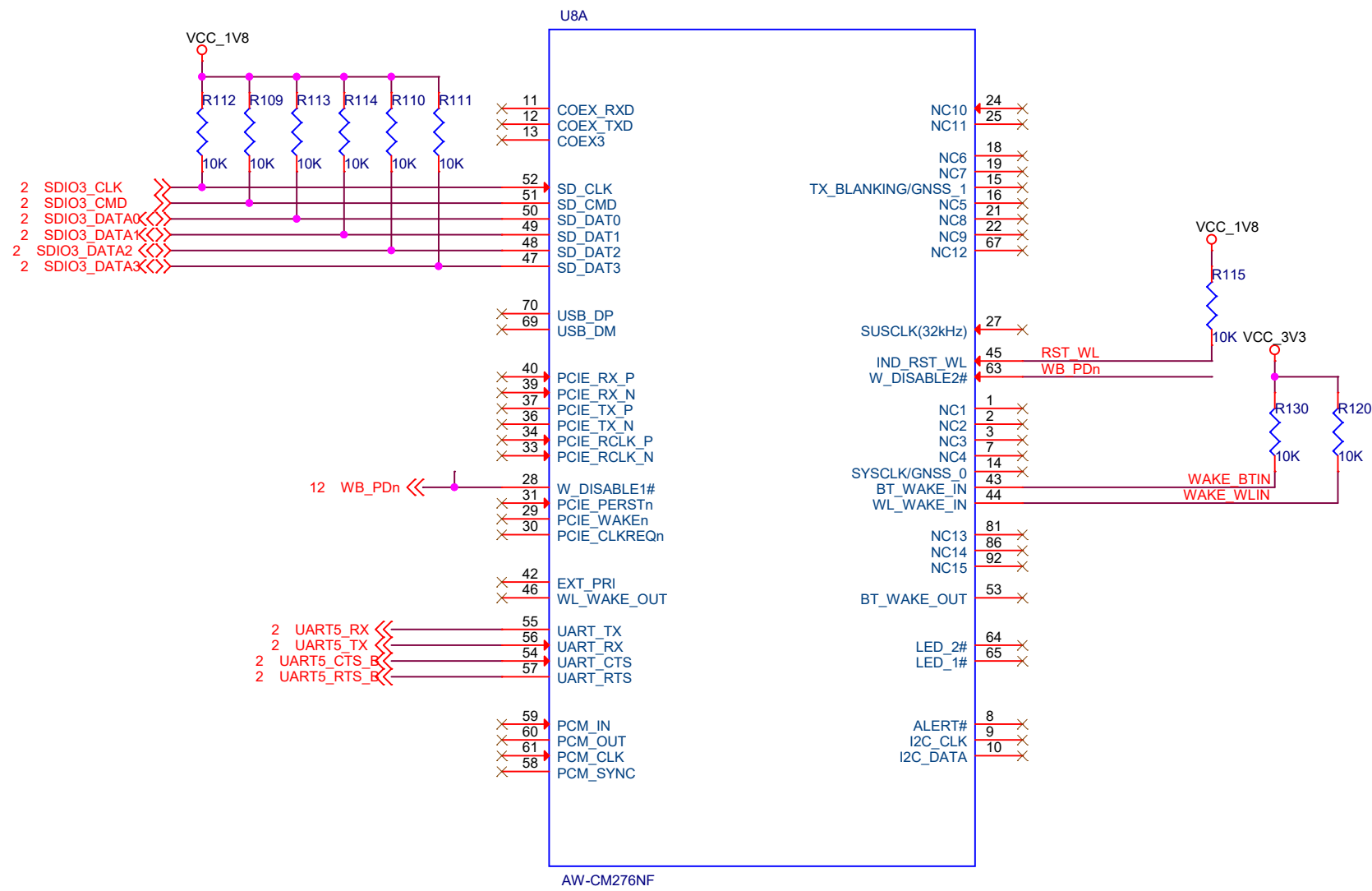
# MIPI-DSICSI Switch



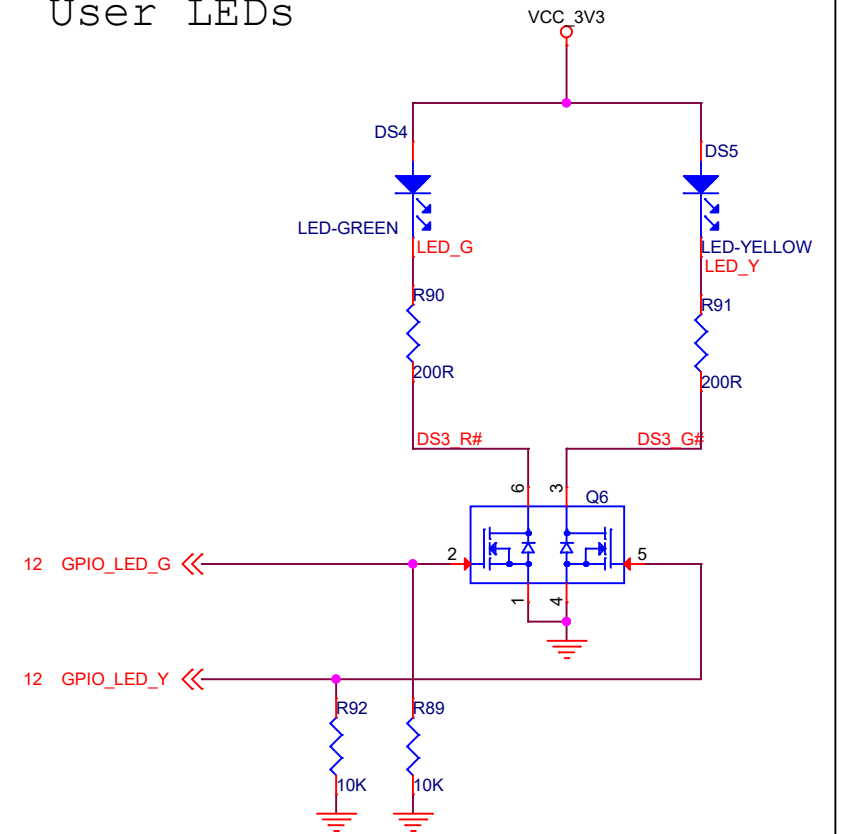
# Camera Edge Connector



WiFi/BT



## User LEDs



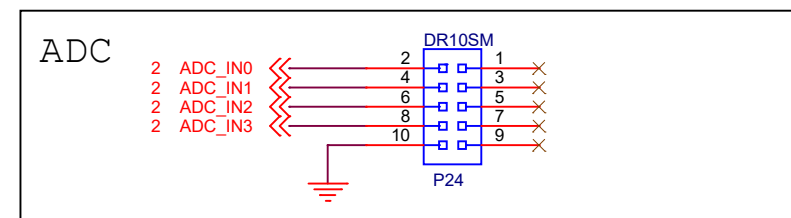
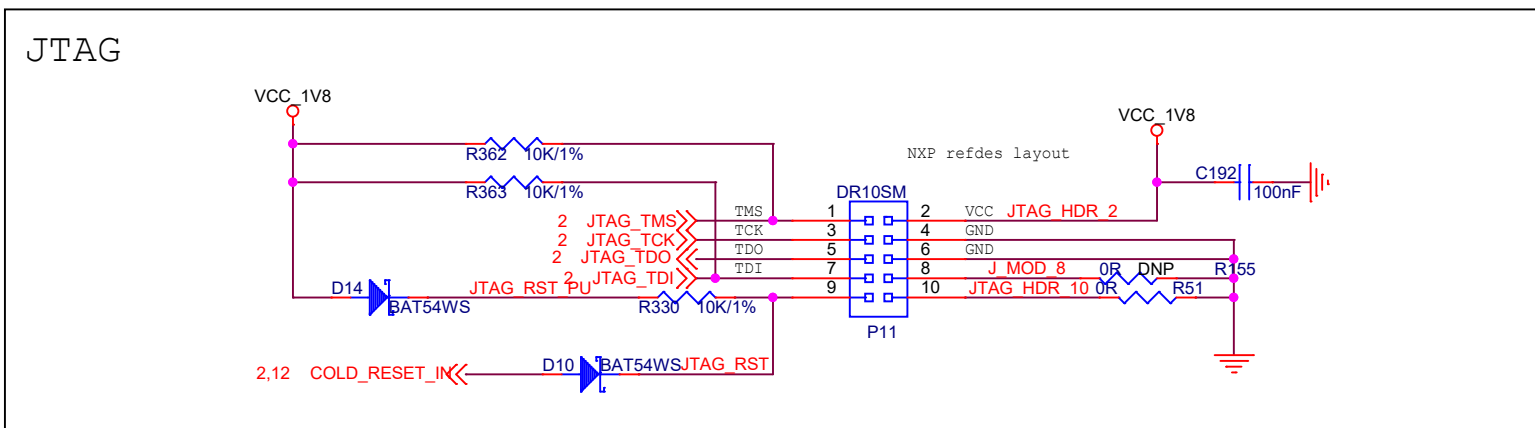
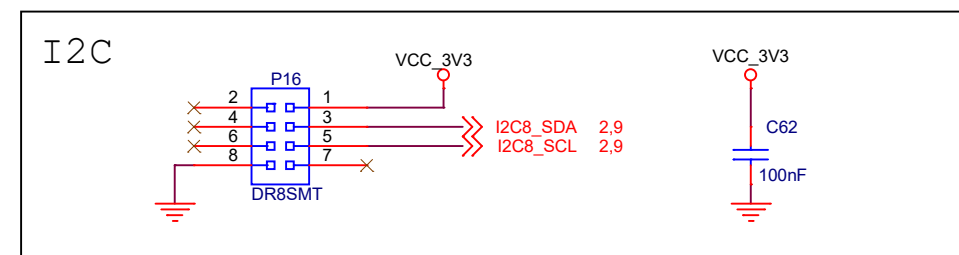
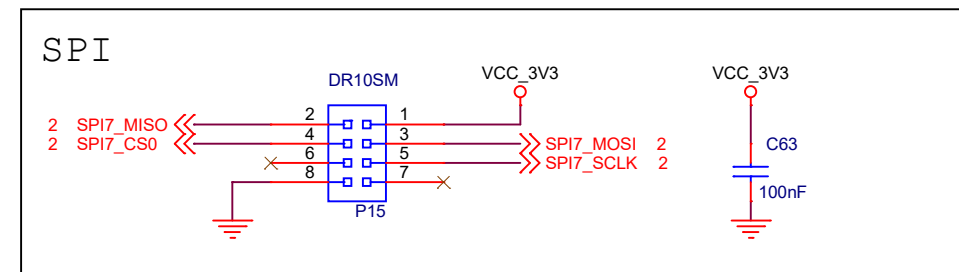
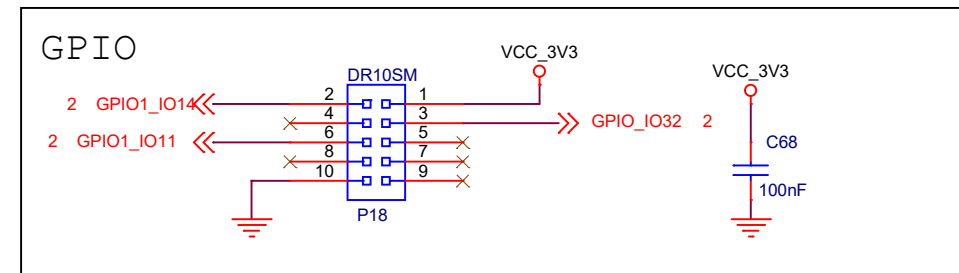
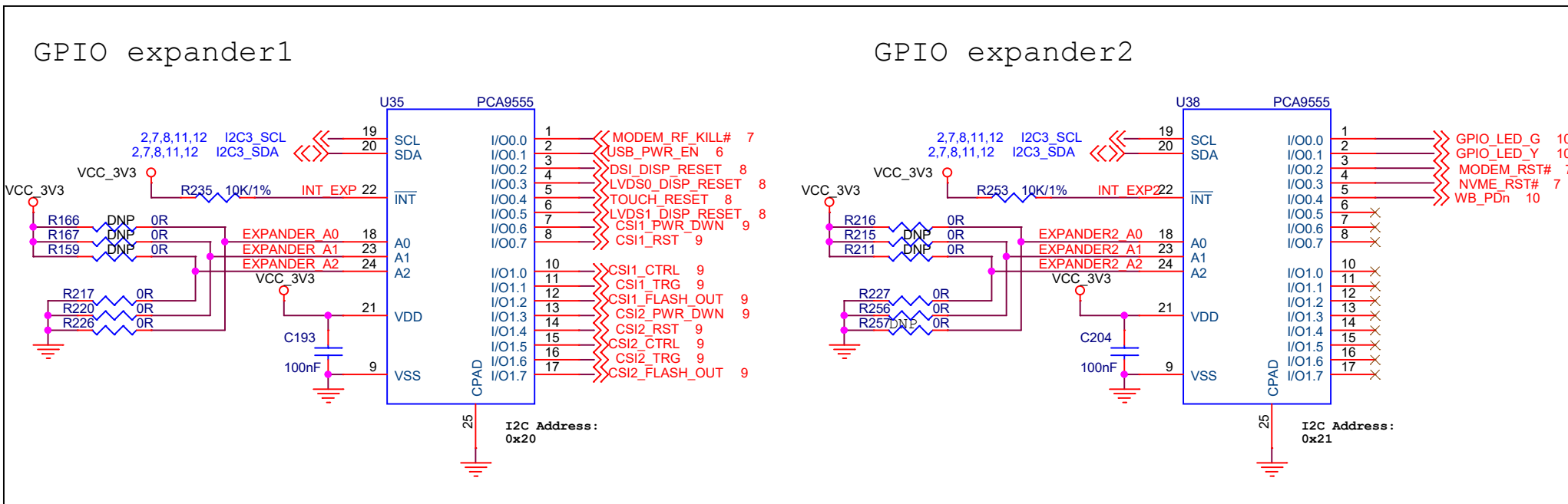
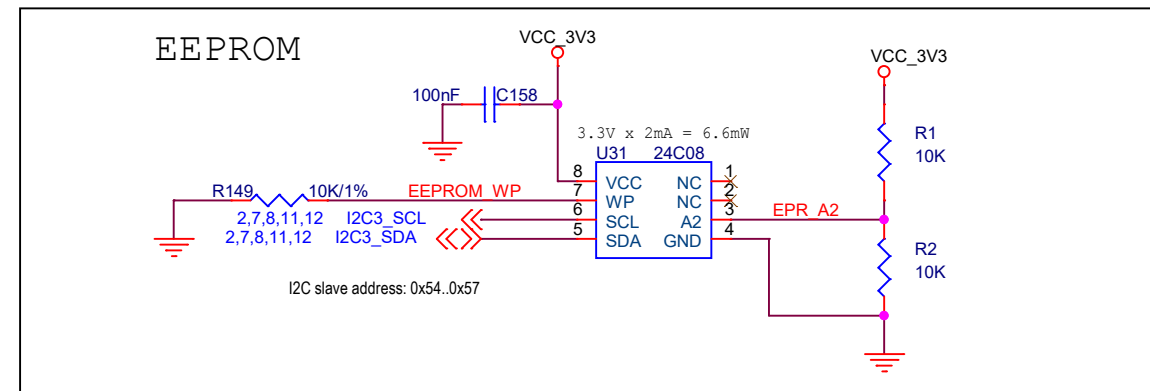
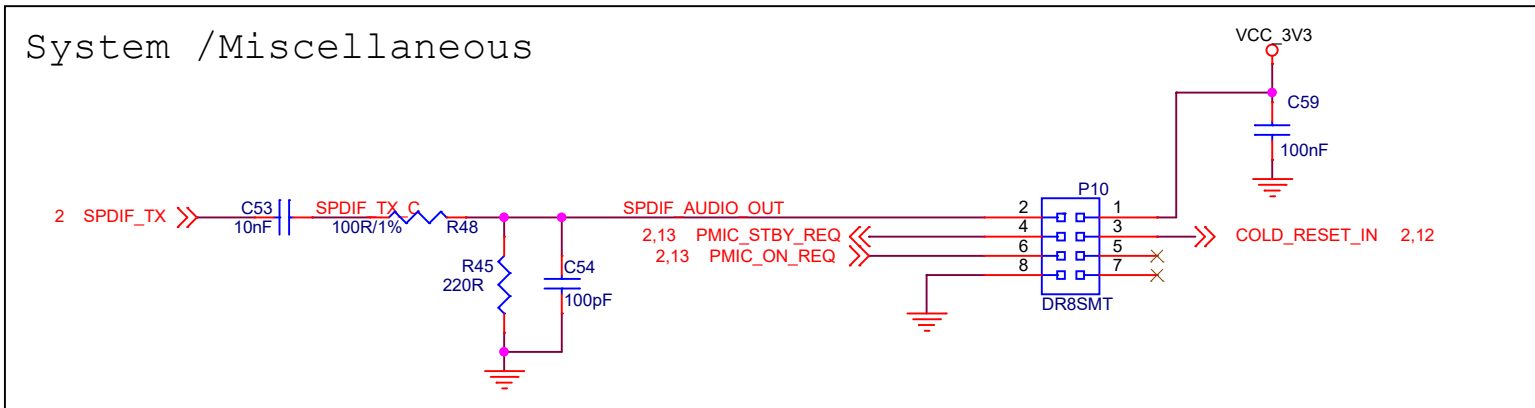
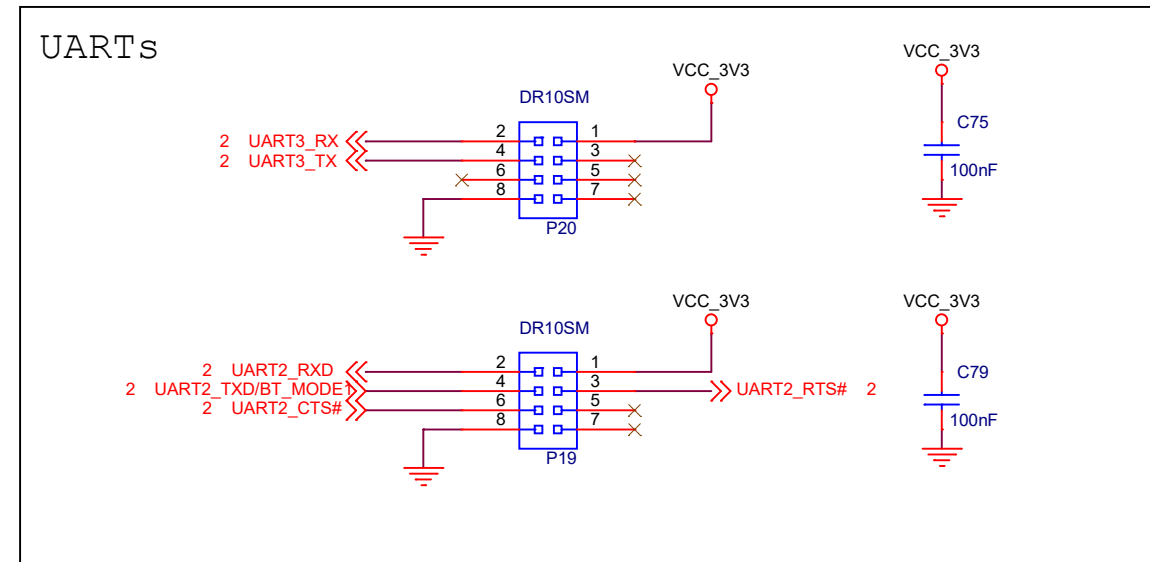
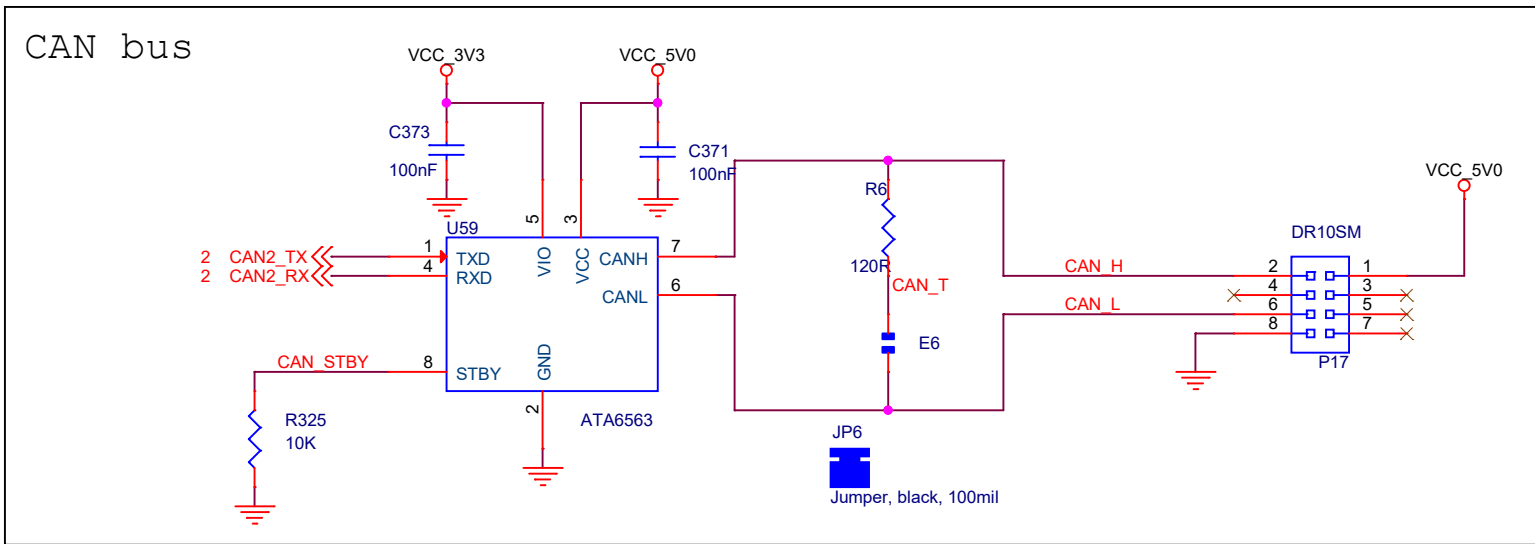
Title      WIFI-BT, user LEDs			
Size A3	Document Number <Doc>		Rev 1.0
Date:	Monday, May 12, 2025	Sheet    10    of    14	

DCVDD	1.62V - 2.0V
DBVDD	1.62V - 3.6V
MICVDD	1.70V - 3.6V
AVDD	1.70V - 2.0V
PLLVDD	1.70V - 2.0V
CPVDD	1.70V - 2.0V
SPKVDD	1.70V - 5.5V

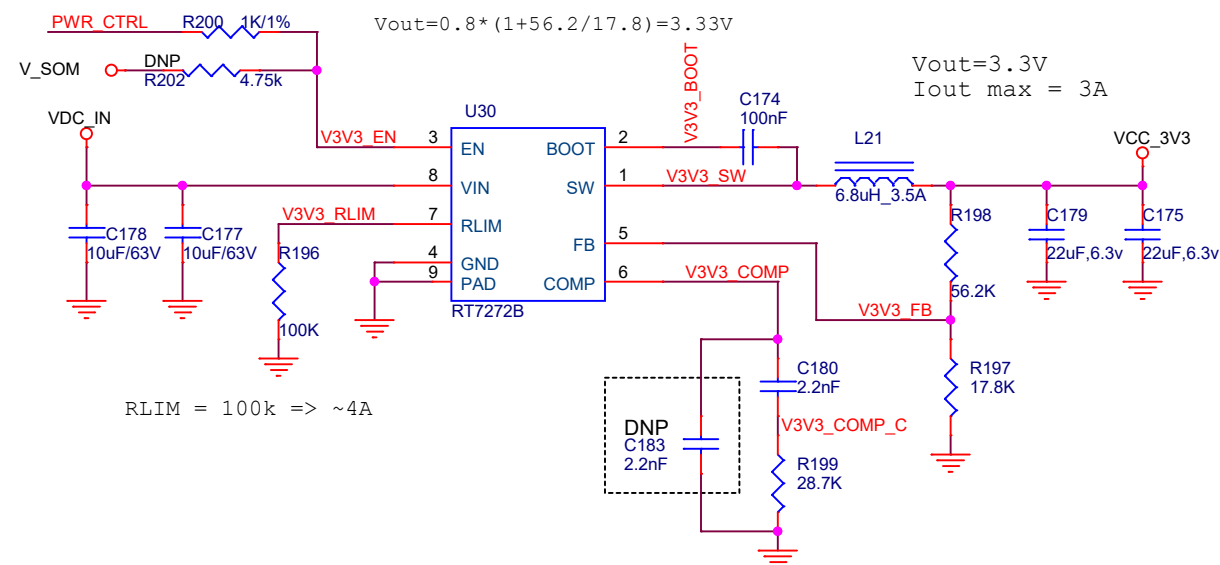


**Plugged: CO  
connected to CN**

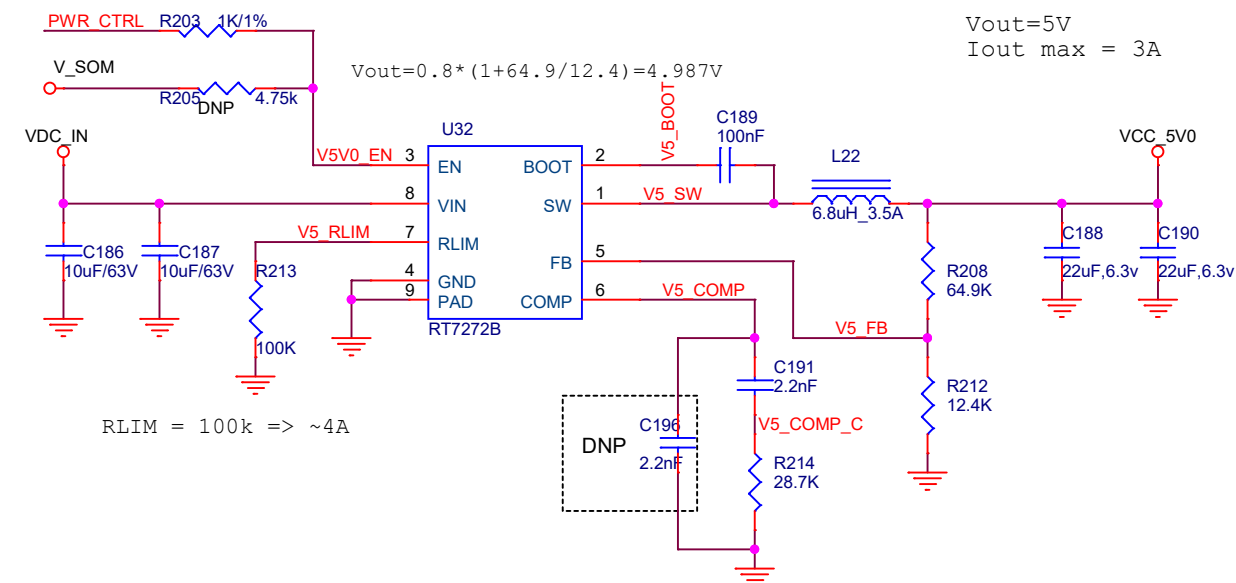
**PLUGGED: High**  
**Removed: Low**



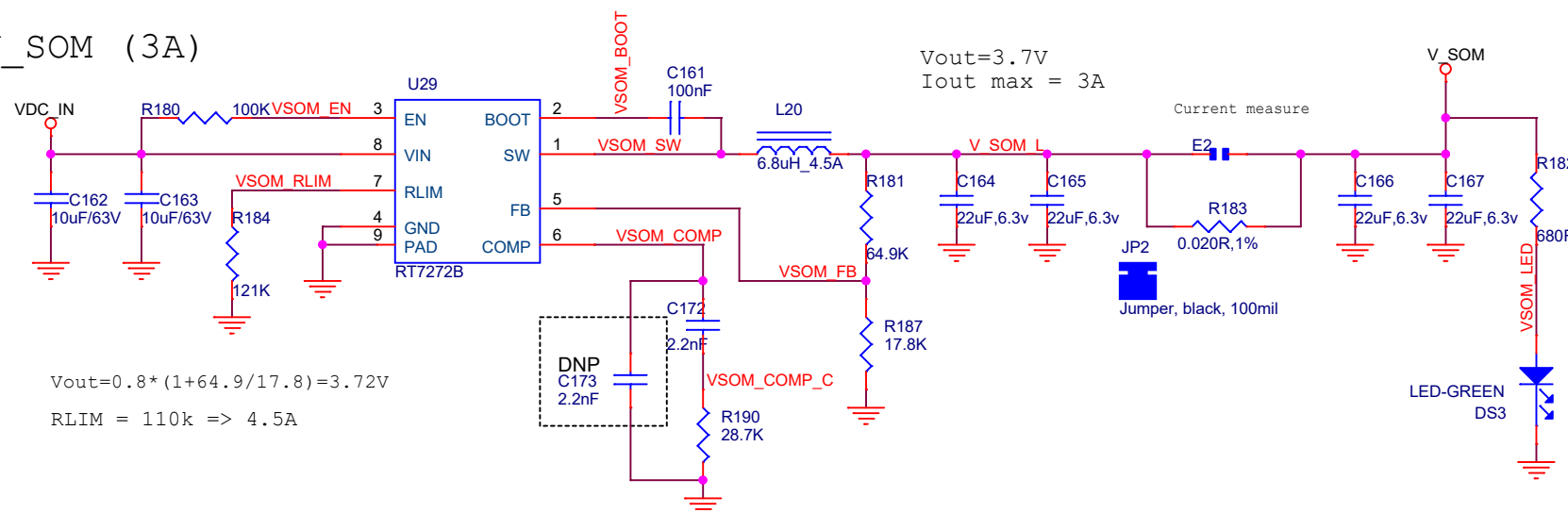
## VCC\_3V3



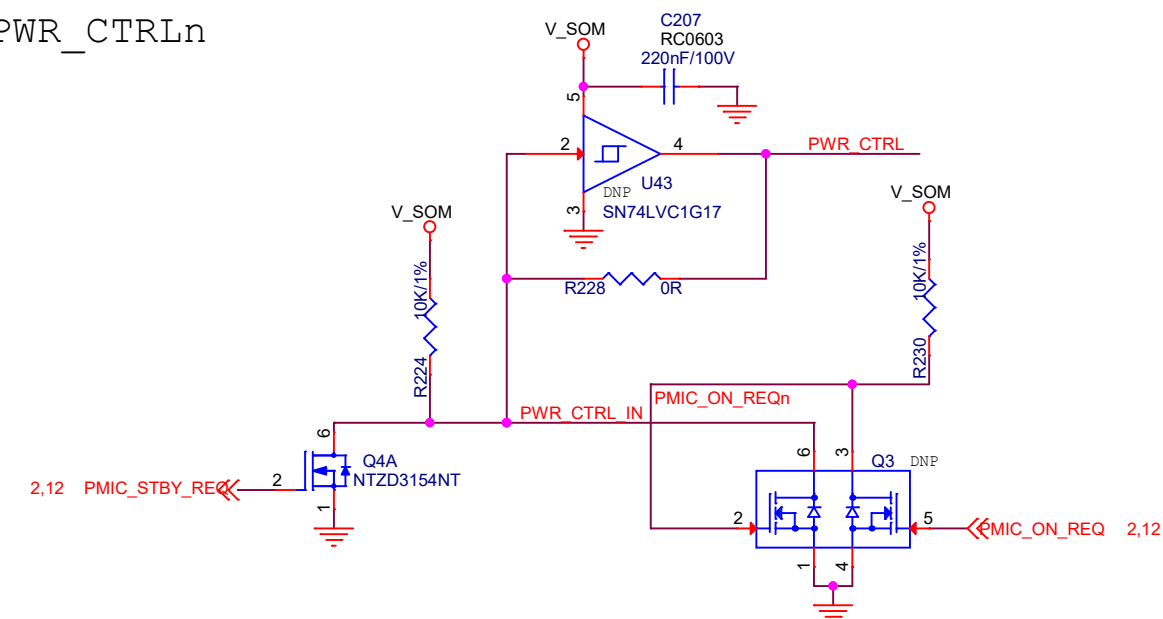
## VCC\_5V



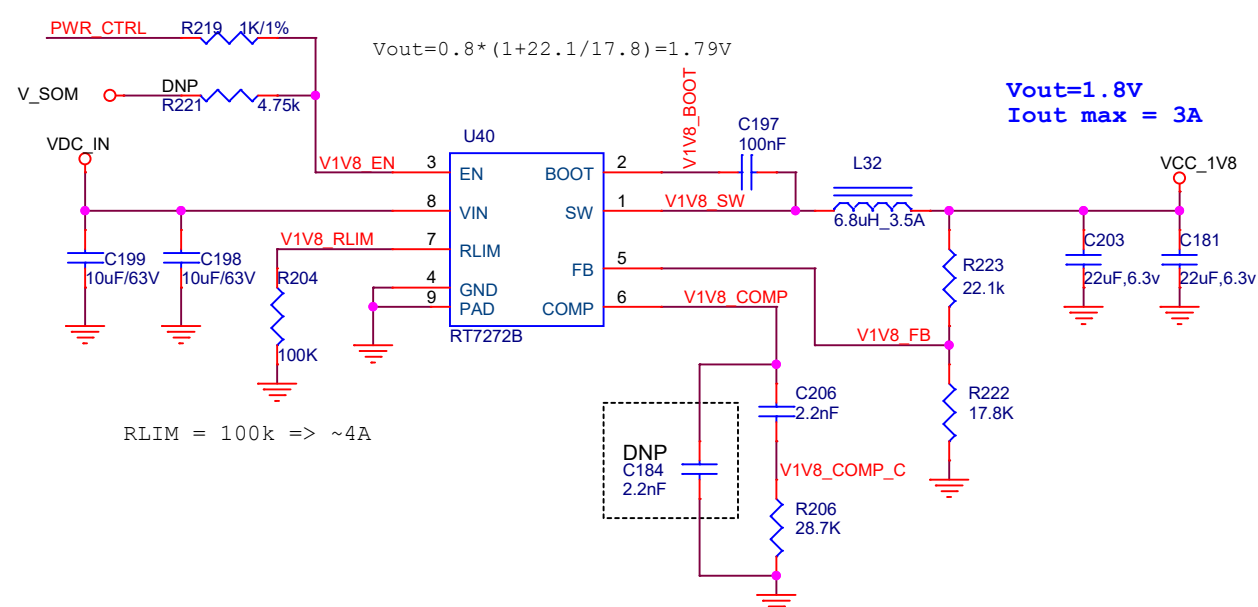
## V\_SOM (3A)



## PWR\_CTRLn



## VCC\_1V8



## VCC\_3V3 plane decoupling

